

Control of Stepping Motors

A Tutorial

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This material expands on material originally posted to the rec.railroad newsgroup in 1990. Significant parts of this material have been republished as sections 5.2.10, 10.8, 10.9 and 10.10 of the *Handbook of Small Electric Motors* edited by W. H. Yeadon and A. W. Yeadon, McGraw-Hill, 2001.

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Abstract

This tutorial covers the basic principles of stepping motors and stepping motor control systems, including both the physics of steppers, the electronics of the basic control systems, and software architectures appropriate for motor control.

Introduction

Stepping motors can be viewed as electric motors without commutators. Typically, all windings in the motor are part of the stator, and the rotor is either a permanent magnet or, in the case of variable reluctance motors, a toothed block of some magnetically soft material. All of the commutation must be handled externally by the motor controller, and typically, the motors and controllers are designed so that the motor may be held in any fixed position as well as being rotated one way or the other. Most steppers, as they are also known, can be stepped at audio frequencies, allowing them to spin quite quickly, and with an appropriate controller, they may be started and stopped "on a dime" at controlled orientations.

For some applications, there is a choice between using servomotors and stepping motors. Both types of motors offer similar opportunities for precise positioning, but they differ in a number of ways. Servomotors require analog feedback control systems of some type. Typically, this involves a potentiometer to provide feedback about the rotor position, and some mix of circuitry to drive a current through the motor inversely proportional to the difference between the desired position and the current position.

In making a choice between steppers and servos, a number of issues must be considered; which of these will matter depends on the application. For example, the repeatability of positioning done with a stepping motor depends on the geometry of the motor rotor, while the repeatability of positioning done with a servomotor generally depends on the stability of the potentiometer and other analog components in the feedback circuit.

Stepping motors can be used in simple open-loop control systems; these are generally adequate for systems that operate at low accelerations with static loads, but closed loop control may be essential for high accelerations, particularly if they involve variable loads. If a stepper in an open-loop control system is overtorqued, all knowledge of rotor position is lost and the system must be reinitialized; servomotors are not subject to this problem.

Stepping motors are known in German as *Schrittmotoren*, in French as *moteurs pas à pas*, and in Spanish as *motor paso paso*.

Other Sources of Information

Web Sites

Other Motor Control Web Pages

- [Advanced Micro Systems Stepper Motor Basics](#)
an excellent tutorial from a maker of motors and controllers.
- [motioncontrol.com](#)
a commercially operated gateway to motion control resources on the web
- [Ian Harries on Stepping Motors](#)

with a nice set of information on reverse engineering salvaged motors and a number of example applications.

- [Euclid Research MotionScope demo](#)

excellent illustrations of physical behavior of some real motors.

Motor Manufacturers

- [Advanced Micro Systems](#) (1.8 degree per step, large permanent magnet motors)
- [Astrosyn](#). (UK)
- [Donovan Micro-Tek Inc.](#) (very small motors)
- [Eastern Air Devices Inc.](#) (midsize motors and linear actuators)
- [MyMotors & Actuators](#) The Faulhaber Group (very small pancake-format motors)
- [Gunda Electronic GmbH](#) (German) ([Google's English translation](#))
- [Haydon Switch and Instrument, Inc.](#)
- [IntelliDrives](#) (high-resolution linear and 2-d planar stepping motors)
- [Lin Engineering](#) (100 to 800 steps per revolution)
- [MicroMo Electronics](#) (very small motors)
- [Mitsumi](#) (Japan)
- [Phytron, Inc.](#) (motors and controllers)
- [Portescap Inc.](#)
- [Shinano Kenshi Corp. \(SKC\)](#)
- [Micro Precision Systems](#) (remarkably small motors and controllers)

Controllers

- [Advanced Micro Systems](#)
- [Astrosyn](#). (UK)
- [Advanced Micro Systems Inc.](#)
- [Alzanti Limited](#) (UK)
- [Arrick Robotics](#)
- [Control Technology Corporation](#)
- [E-Lab Digital Engineering, Inc.](#)
- [GreenSpring Computers](#)
- [Simple Step LLC](#)
- [Netmotion](#)
- [StepperControl.com](#)

Distributors

- [ACP&D Limited](#) (UK) ([UK version](#)) (distributor for COLIBRI integrated motor/controllers and maker of COBRA linear and planar stepping motors)
- [Alzanti Limited](#) (UK)

- [Electro Sales Inc.](#) (northeast USA)
- [Flexible Technologies, Inc.](#) (Southwest USA)
- [MESA Systems Co.](#) (USA) (distributor for COLIBRI integrated motor/controllers)
- [Motionex](#) (southeast USA)
- [Smart Motion Control Inc.](#) dba ABC Motion Control (northeast USA)
- [Technovation Systems Ltd.](#) (UK)

Surplus and Hobbyist Suppliers

- [ALL Electronics](#) (new and surplus)
- [DIY Electronics](#) (kits, Hong Kong)
- [EIO's Stepper Motor Page](#) (surplus)
- [PC Gadgets](#) (the Gadgetmaster interface)
- [Hi-Tech Surplus](#)
- [Surplus Center](#) (mostly heavy industrial surplus, Nebraska)
- [Vorlac](#) (Surplus, australia)
- [Wirz Electronics](#) (Hobbyist oriented, controllers)

Motor Design, Selection and Prototype Fabricaton Services

- [Yeadon Engineering Services](#), yes@up.net (Michigan)
YES is the contact for the Small Motor Manufacturer's Association.

Other Web Pages

- [The Art of Motion Control](#);
Bruce Shapiro's stepper-controlled machine-shop and artist's studio.
- [EIO's Stepper Motor Page](#);
a surplus dealer, but listed here because of their extensive index of information about stepping motors.
- [Fractional Horsepower Motor Manufacturers](#);
an index maintained by [Industrial Quick Search](#).
- [Schmitz Engineering Liaison](#);
a rotary shaft position encoder distributor offering consulting services on encoder use. Roger Schmitz wrote [Encoder Output Choices for System Designers](#) for [MOTION Magazine](#).

Books

Handbook of Small Electric Motors

William H. Yeadon and Alan W, Yeadon, eds.
McGraw-Hill, c2001.
LC number: TK2537 .H34 2001

Stepping motors: a guide to modern theory and practice
Acarney, P. P.
P. Peregrinus on behalf of the IEE, 1984, c1982.
LC number: TK2537 .A28 1984
A third edition has recently been released.

Stepping motors and their microprocessor controls
Kenjo, Takashi
Oxford University Press, c1984.
LC number: TK2785 .K4 1984

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1. Stepping Motor Types

Part of [Stepping Motors](#)

by [Douglas W. Jones](#)

[THE UNIVERSITY OF IOWA Department of Computer Science](#)

- [Introduction](#)
- [Variable Reluctance Motors](#)
- [Unipolar Motors](#)
- [Bipolar Motors](#)
- [Bifilar Motors](#)
- [Multiphase Motors](#)

Introduction

Stepping motors come in two varieties, *permanent magnet* and *variable reluctance* (there are also *hybrid* motors, which are indistinguishable from permanent magnet motors from the controller's point of view). Lacking a label on the motor, you can generally tell the two apart by feel when no power is applied. Permanent magnet motors tend to "cog" as you twist the rotor with your fingers, while variable reluctance motors almost spin freely (although they may cog slightly because of residual magnetization in the rotor). You can also distinguish between the two varieties with an ohmmeter. Variable reluctance motors usually have three (sometimes four) windings, with a common return, while permanent magnet motors usually have two independent windings, with or without center taps. Center-tapped windings are used in unipolar permanent magnet motors.

Stepping motors come in a wide range of angular resolution. The coarsest motors typically turn 90 degrees per step, while high resolution permanent magnet motors are commonly able to handle 1.8 or even 0.72 degrees per step. With an appropriate controller, most permanent magnet and hybrid motors can be run in half-steps, and some controllers can handle smaller fractional steps or microsteps.

For both permanent magnet and variable reluctance stepping motors, if just one winding of the motor is energised, the rotor (under no load) will snap to a fixed angle and then hold that angle until the torque exceeds the holding torque of the motor, at which point, the rotor will turn, trying to hold at each successive equilibrium point.

Variable Reluctance Motors

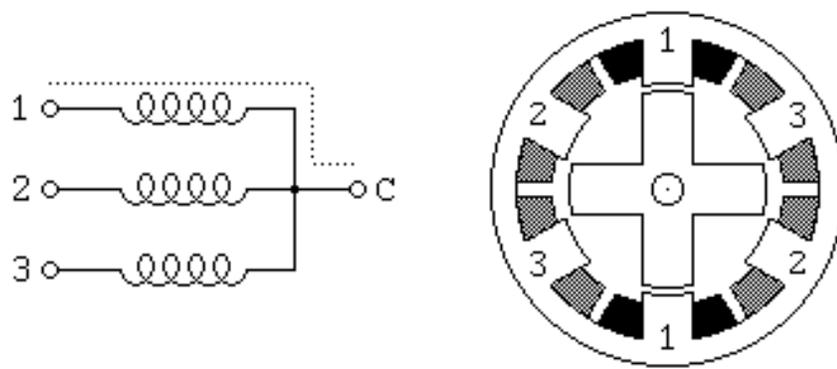


Figure 1.1

If your motor has three windings, typically connected as shown in the schematic diagram in Figure 1.1, with one terminal common to all windings, it is most likely a variable reluctance stepping motor. In use, the common wire typically goes to the positive supply and the windings are energized in sequence.

The cross section shown in Figure 1.1 is of 30 degree per step variable reluctance motor. The rotor in this motor has 4 teeth and the stator has 6 poles, with each winding wrapped around two opposite poles. With winding number 1 energized, the rotor teeth marked X are attracted to this winding's poles. If the current through winding 1 is turned off and winding 2 is turned on, the rotor will rotate 30 degrees clockwise so that the poles marked Y line up with the poles marked 2. An [animated GIF of figure 1.1](#) is available.

To rotate this motor continuously, we just apply power to the 3 windings in sequence. Assuming positive logic, where a 1 means turning on the current through a motor winding, the following control sequence will spin the motor illustrated in Figure 1.1 clockwise 24 steps or 2 revolutions:

```

Winding 1 1001001001001001001001001001
Winding 2 0100100100100100100100100100
Winding 3 0010010010010010010010010010
time ---->

```

The section of this tutorial on [Mid-Level Control](#) provides details on methods for generating such sequences of control signals, while the section on [Control Circuits](#) discusses the power switching circuitry needed to drive the motor windings from such control sequences.

There are also variable reluctance stepping motors with 4 and 5 windings, requiring 5 or 6 wires. The principle for driving these motors is the same as that for the three winding variety, but it becomes important to work out the correct order to energise the windings to make the motor step nicely.

The motor geometry illustrated in Figure 1.1, giving 30 degrees per step, uses the fewest number of rotor teeth and stator poles that performs satisfactorily. Using more motor poles and more rotor teeth allows construction of motors with smaller step angle. Toothed faces on each pole and a correspondingly finely toothed rotor allows for step angles as small as a few degrees.

Unipolar Motors

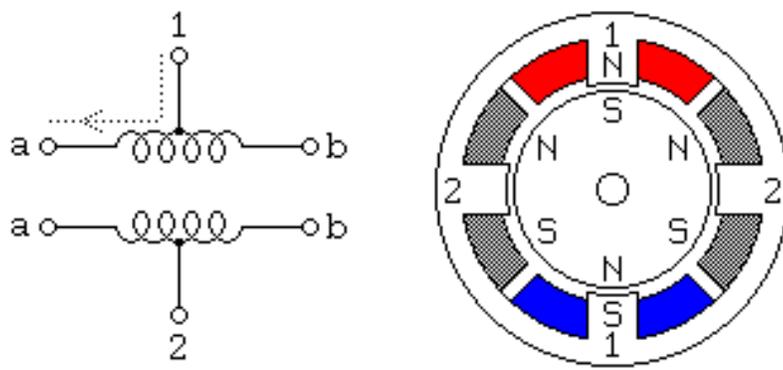


Figure 1.2

Unipolar stepping motors, both Permanent magnet and hybrid stepping motors with 5 or 6 wires are usually wired as shown in the schematic in Figure 1.2, with a center tap on each of two windings. In use, the center taps of the windings are typically wired to the positive supply, and the two ends of each winding are alternately grounded to reverse the direction of the field provided by that winding. An [animated GIF of figure 1.2](#) is available.

The motor cross section shown in Figure 1.2 is of a 30 degree per step permanent magnet or hybrid motor -- the difference between these two motor types is not relevant at this level of abstraction. Motor winding number 1 is distributed between the top and bottom stator pole, while motor winding number 2 is distributed between the left and right motor poles. The rotor is a permanent magnet with 6 poles, 3 south and 3 north, arranged around its circumference.

For higher angular resolutions, the rotor must have proportionally more poles. The 30 degree per step motor in the figure is one of the most common permanent magnet motor designs, although 15 and 7.5 degree per step motors are widely available. Permanent magnet motors with resolutions as good as 1.8 degrees per step are made, and hybrid motors are routinely built with 3.6 and 1.8 degrees per step, with resolutions as fine as 0.72 degrees per step available.

As shown in the figure, the current flowing from the center tap of winding 1 to terminal a causes the top stator pole to be a north pole while the bottom stator pole is a south pole. This attracts the rotor into the position shown. If the power to winding 1 is removed and winding 2 is energized, the rotor will turn 30 degrees, or one step.

To rotate the motor continuously, we just apply power to the two windings in sequence. Assuming positive logic, where a 1 means turning on the current through a motor winding, the following two control sequences will spin the motor illustrated in Figure 1.2 clockwise 24 steps or 2 revolutions:

```

Winding 1a 1000100010001000100010001
Winding 1b 0010001000100010001000100
Winding 2a 0100010001000100010001000
Winding 2b 0001000100010001000100010
           time ---->

```

```

Winding 1a 1100110011001100110011001
Winding 1b 0011001100110011001100110
Winding 2a 0110011001100110011001100
Winding 2b 1001100110011001100110011
           time ---->

```

Note that the two halves of each winding are never energized at the same time. Both sequences shown above will rotate a permanent magnet one step at a time. The top sequence only powers one winding at a time, as illustrated in the figure above; thus, it uses less power. The bottom sequence involves powering two windings at a time and generally produces a torque about 1.4 times greater than the top sequence while using twice as much power.

The section of this tutorial on [Mid-Level Control](#) provides details on methods for generating such sequences of control signals, while the section on [Control Circuits](#) discusses the power switching circuitry needed to drive the motor windings from such control sequences.

The step positions produced by the two sequences above are not the same; as a result, combining the two sequences allows half stepping, with the motor stopping alternately at the positions indicated by one or the other sequence. The combined sequence is as follows:

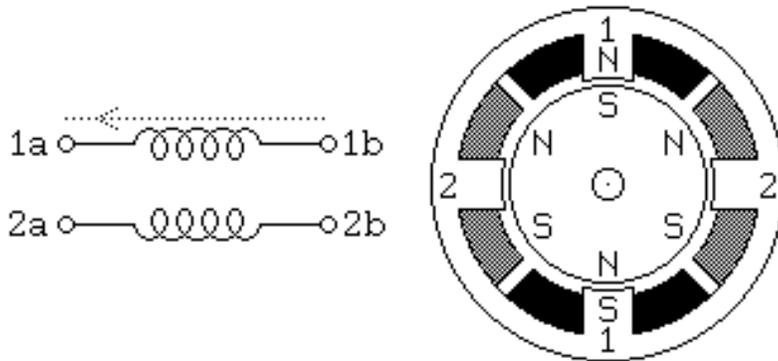
```

Winding 1a 11000001110000011100000111
Winding 1b 00011100000111000001110000
Winding 2a 01110000011100000111000001
Winding 2b 00000111000001110000011100
           time ---->

```

Bipolar Motors

Figure 1.3



Bipolar permanent magnet and hybrid motors are constructed with exactly the same mechanism as is used on unipolar motors, but the two windings are wired more simply, with no center taps. Thus, the motor itself is simpler but the drive circuitry needed to reverse the polarity of each pair of motor poles is more complex. The schematic in Figure 1.3 shows how such a motor is wired, while the motor cross section shown here is exactly the same as the cross section shown in Figure 1.2.

The drive circuitry for such a motor requires an *H-bridge* control circuit for each winding; these are discussed in more detail in the section on [Control Circuits](#). Briefly, an H-bridge allows the polarity of the power applied to each end of each winding to be controlled independently. The control sequences for single stepping such a motor are shown below, using + and - symbols to indicate the polarity of the power applied to each motor terminal:

```

Terminal 1a +---+---+---+---  ++---++---++---++---
Terminal 1b --+---+---+---+---  --++---++---++---++---

```

```

Terminal 2a --+---+---+---+---+---+---+---+---+---+---+---+---+
Terminal 2b ---+---+---+---+---+---+---+---+---+---+---+---+---+
time ---->

```

Note that these sequences are identical to those for a unipolar permanent magnet motor, at an abstract level, and that above the level of the H-bridge power switching electronics, the control systems for the two types of motor can be identical.

Note that many full H-bridge driver chips have one control input to enable the output and another to control the direction. Given two such bridge chips, one per winding, the following control sequences will spin the motor identically to the control sequences given above:

```

Enable      1 1010101010101010 1111111111111111
Direction   1 1x0x1x0x1x0x1x0x 1100110011001100
Enable      2 0101010101010101 1111111111111111
Direction   2 x1x0x1x0x1x0x1x0 0110011001100110
time ---->

```

To distinguish a bipolar permanent magnet motor from other 4 wire motors, measure the resistances between the different terminals. It is worth noting that some permanent magnet stepping motors have 4 independent windings, organized as two sets of two. Within each set, if the two windings are wired in series, the result can be used as a high voltage bipolar motor. If they are wired in parallel, the result can be used as a low voltage bipolar motor. If they are wired in series with a center tap, the result can be used as a low voltage unipolar motor.

Bifilar Motors

Bifilar windings on a stepping motor are applied to the same rotor and stator geometry as a bipolar motor, but instead of winding each coil in the stator with a single wire, two wires are wound in parallel with each other. As a result, the motor has 8 wires, not four.

In practice, motors with bifilar windings are always powered as either unipolar or bipolar motors. Figure 1.4 shows the alternative connections to the windings of such a motor.

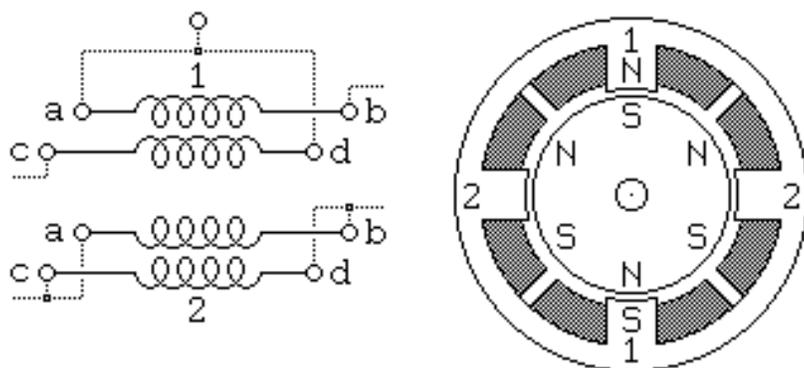


Figure 1.4

To use a bifilar motor as a unipolar motor, the two wires of each winding are connected in series and the point of connection is used as a center-tap. Winding 1 in Figure 1.4 is shown connected this way.

To use a bifilar motor as a bipolar motor, the two wires of each winding are connected either in parallel or in series. Winding 2 in Figure 1.4 is shown with a parallel connection; this allows low voltage high-current operation. Winding 1 in Figure 1.4 is shown with a series connection; if the center tap is ignored, this allows operation at a higher voltage and lower current than would be used with the windings in parallel.

It should be noted that essentially all 6-wire motors sold for bipolar use are actually wound using bifilar windings, so that the external connection that serves as a center tap is actually connected as shown for winding 1 in Figure 1.4. Naturally, therefore, any unipolar motor may be used as a bipolar motor at twice the rated voltage and half the rated current as is given on the nameplate.

The question of the correct operating voltage for a bipolar motor run as a unipolar motor, or for a bifilar motor with the motor windings in series is not as trivial as it might first appear. There are three issues: The current carrying capacity of the wire, cooling the motor, and avoiding driving the motor's magnetic circuits into saturation. Thermal considerations suggest that, if the windings are wired in series, the voltage should only be raised by the square root of 2. The magnetic field in the motor depends on the number of ampere turns; when the two half-windings are run in series, the number of turns is doubled, but because a well-designed motor has magnetic circuits that are close to saturation when the motor is run at its rated voltage and current, increasing the number of ampere-turns does not make the field any stronger. Therefore, when a motor is run with the two half-windings in series, the current should be halved in order to avoid saturation; or, in other words, the voltage across the motor winding should be the same as it was.

For those who salvage old motors, finding an 8-wire motor poses a challenge! Which of the 8 wires is which? It is not hard to figure this out using an ohm meter, an AC volt meter, and a low voltage AC source. First, use the ohm meter to identify the motor leads that are connected to each other through the motor windings. Then, connect a low-voltage AC source to one of these windings. The AC voltage should be below the advertised operating voltage of the motor; voltages under 1 volt are recommended. The geometry of the magnetic circuits of the motor guarantees that the two wires of a bifilar winding will be strongly coupled for AC signals, while there should be almost no coupling to the other two wires. Therefore, probing with an AC volt meter should disclose which of the other three windings is paired to the winding under power.

Multiphase Motors

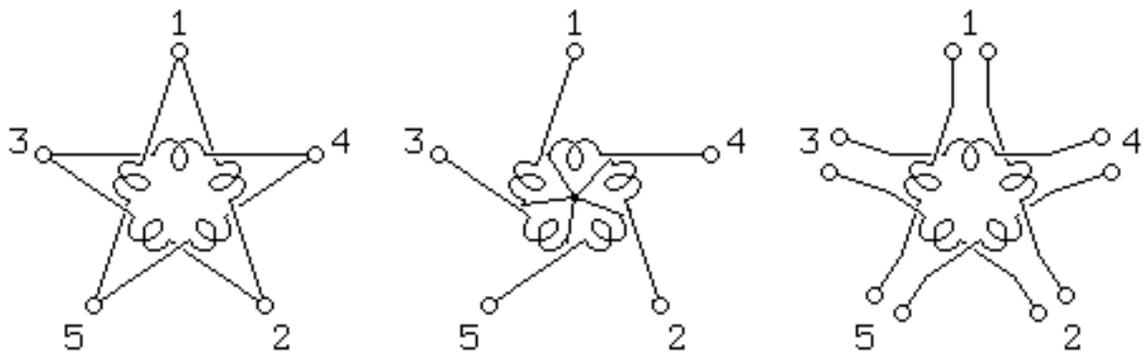


Figure 1.5

A less common class of permanent magnet or hybrid stepping motor is wired with all windings of the motor in a cyclic series, with one tap between each pair of windings in the cycle, or with only one end of each motor winding exposed while the other ends of each winding are tied together to an inaccessible internal connection. In the context of 3-phase motors, these configurations would be described as Delta and Y configurations, but they are also used with 5-phase motors, as illustrated in Figure 1.5. Some multiphase

motors expose all ends of all motor windings, leaving it to the user to decide between the Delta and Y configurations, or alternatively, allowing each winding to be driven independently.

Control of either one of these multiphase motors in either the Delta or Y configuration requires 1/2 of an H-bridge for each motor terminal. It is noteworthy that 5-phase motors have the potential of delivering more torque from a given package size because all or all but one of the motor windings are energised at every point in the drive cycle. Some 5-phase motors have high resolutions on the order of 0.72 degrees per step (500 steps per revolution).

Many automotive alternators are built using a 3-phase hybrid geometry with either a permanent magnet rotor or an electromagnet rotor powered through a pair of slip-rings. These have been successfully used as stepping motors in some heavy duty industrial applications; step angles of 10 degrees per step have been reported.

With a 5-phase motor, there are 10 steps per repeat in the stepping cycle, as shown below:

```
Terminal 1  +++-----+++++-----++
Terminal 2  --+++++-----+++++---
Terminal 3  +-----+++++-----++++
Terminal 4  +++++-----+++++-----
Terminal 5  -----+++++-----+++++
           time ---->
```

With a 3-phase motor, there are 6 steps per repeat in the stepping cycle, as shown below:

```
Terminal 1  +++-----++----
Terminal 2  --++++-----++-
Terminal 3  +-----++++-----+
           time ---->
```

Here, as in the bipolar case, each terminal is shown as being either connected to the positive or negative bus of the motor power system. Note that, at each step, only one terminal changes polarity. This change removes the power from one winding attached to that terminal (because both terminals of the winding in question are of the same polarity) and applies power to one winding that was previously idle. Given the motor geometry suggested by Figure 1.5, this control sequence will drive the motor through two revolutions.

To distinguish a 5-phase motor from other motors with 5 leads, note that, if the resistance between two consecutive terminals of the 5-phase motor is R, the resistance between non-consecutive terminals will be 1.5R.

Note that some 5-phase motors have 5 separate motor windings, with a total of 10 leads. These can be connected in the star configuration shown above, using 5 half-bridge driver circuits, or each winding can be driven by its own full-bridge. While the theoretical component count of half-bridge drivers is lower, the availability of integrated full-bridge chips may make the latter approach preferable.

2. Stepping Motor Physics

Part of [Stepping Motors](#)

by [Douglas W. Jones](#)

[THE UNIVERSITY OF IOWA Department of Computer Science](#)

- [Introduction](#)
- [Statics](#)
- [- Half-Stepping and Microstepping](#)
- [- Friction and the Dead Zone](#)
- [Dynamics](#)
- [- Resonance](#)
- [- Living with Resonance](#)
- [- Torque versus Speed](#)
- [Electromagnetic Issues](#)

Introduction

In any presentation covering the quantitative physics of a class of systems, it is important to beware of the units of measurement used! In this presentation of stepping motor physics, we will assume standard physical units:

	English	CGS	MKS
MASS	slug	gram	kilogram
FORCE	pound	dyne	newton
DISTANCE	foot	centimeter	meter
TIME	second	second	second
ANGLE	radian	radian	radian

A force of one pound will accelerate a mass of one slug at one foot per second squared. The same relationship holds between the force, mass, time and distance units of the other measurement systems. Most people prefer to measure angles in degrees, and the common engineering practice of specifying mass in pounds or force in kilograms will not yield correct results in the formulas given here! Care must be taken to convert such irregular units to one of the standard systems outlined above before

applying the formulas given here!

Statics

For a motor that turns S radians per step, the plot of torque versus angular position for the rotor relative to some initial equilibrium position will generally approximate a sinusoid. The actual shape of the curve depends on the pole geometry of both rotor and stator, and neither this curve nor the geometry information is given in the motor data sheets I've seen! For permanent magnet and hybrid motors, the actual curve usually looks sinusoidal, but looks can be misleading. For variable reluctance motors, the curve rarely even looks sinusoidal; trapezoidal and even asymmetrical sawtooth curves are not uncommon.

For a three-winding variable reluctance or permanent magnet motors with S radians per step, the period of the torque versus position curve will be $3S$; for a 5-phase permanent magnet motor, the period will be $5S$. For a two-winding permanent magnet or hybrid motor, the most common type, the period will be $4S$, as illustrated in Figure 2.1:

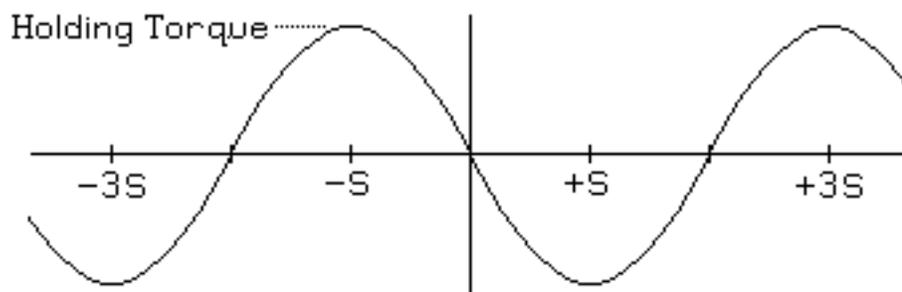


Figure 2.1

Again, for an ideal 2 winding permanent magnet motor, this can be mathematically expressed as:

$$T = -h \sin\left(\left(\frac{\pi}{2}\right) / S \Theta\right)$$

Where:

T -- torque

h -- holding torque

S -- step angle, in radians

Θ = shaft angle, in radians

But remember, subtle departures from the ideal sinusoid described here are very common.

The *single-winding holding torque* of a stepping motor is the peak value of the torque versus position curve when the maximum allowed current is flowing through one motor winding. If you attempt to apply a torque greater than this to the motor rotor while maintaining power to one winding, it will rotate freely.

It is sometimes useful to distinguish between the *electrical shaft angle* and the *mechanical shaft*

angle. In the mechanical frame of reference, 2π radians is defined as one full revolution. In the electrical frame of reference, a revolution is defined as one period of the torque versus shaft angle curve. Throughout this tutorial, Θ refers to the mechanical shaft angle, and $((\pi/2)/S)\Theta$ gives the electrical angle for a motor with 4 steps per cycle of the torque curve.

Assuming that the torque versus angular position curve is a good approximation of a sinusoid, as long as the torque remains below the holding torque of the motor, the rotor will remain within 1/4 period of the equilibrium position. For a two-winding permanent magnet or hybrid motor, this means the rotor will remain within one step of the equilibrium position.

With no power to any of the motor windings, the torque does not always fall to zero! In variable reluctance stepping motors, residual magnetization in the magnetic circuits of the motor may lead to a small residual torque, and in permanent magnet and hybrid stepping motors, the combination of pole geometry and the permanently magnetized rotor may lead to significant torque with no applied power.

The residual torque in a permanent magnet or hybrid stepping motor is frequently referred to as the *cogging torque* or *detent torque* of the motor because a naive observer will frequently guess that there is a detent mechanism of some kind inside the motor. The most common motor designs yield a detent torque that varies sinusoidally with rotor angle, with an equilibrium position at every step and an amplitude of roughly 10% of the rated holding torque of the motor, but a quick survey of motors from one manufacturer (Phytron) shows values as high as 23% for one very small motor to a low of 2.6% for one mid-sized motor.

Half-Stepping and Microstepping

So long as no part of the magnetic circuit saturates, powering two motor windings simultaneously will produce a torque versus position curve that is the sum of the torque versus position curves for the two motor windings taken in isolation. For a two-winding permanent magnet or hybrid motor, the two curves will be S radians out of phase, and if the currents in the two windings are equal, the peaks and valleys of the sum will be displaced $S/2$ radians from the peaks of the original curves, as shown in Figure 2.2:

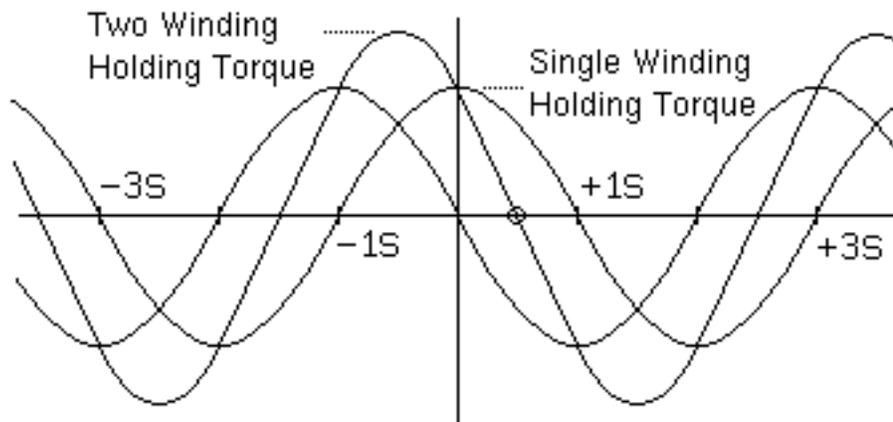


Figure 2.2

This is the basis of *half-stepping*. The *two-winding holding torque* is the peak of the composite torque

curve when two windings are carrying their maximum rated current. For common two-winding permanent magnet or hybrid stepping motors, the two-winding holding torque will be:

$$h_2 = 2^{0.5} h_1$$

where:

h_1 -- single-winding holding torque

h_2 -- two-winding holding torque

This assumes that no part of the magnetic circuit is saturated and that the torque versus position curve for each winding is an ideal sinusoid.

Most permanent-magnet and variable-reluctance stepping motor data sheets quote the two-winding holding torque and not the single-winding figure; in part, this is because it is larger, and in part, it is because the most common full-step controllers always apply power to two windings at once.

If any part of the motor's magnetic circuits is saturated, the two torque curves will not add linearly. As a result, the composite torque will be less than the sum of the component torques and the equilibrium position of the composite may not be exactly $S/2$ radians from the equilibria of the original.

Microstepping allows even smaller steps by using different currents through the two motor windings, as shown in Figure 2.3:

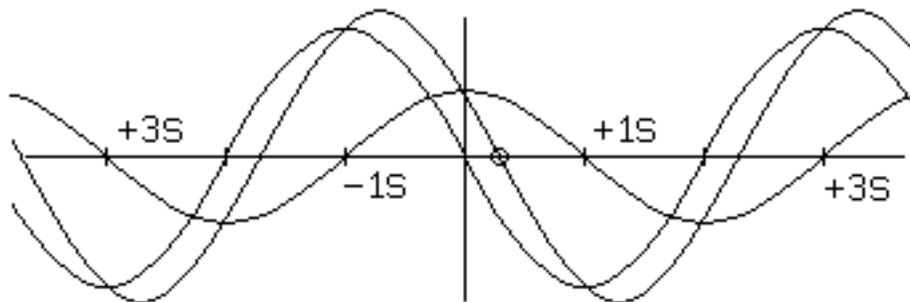


Figure 2.3

For a two-winding variable reluctance or permanent magnet motor, assuming nonsaturating magnetic circuits, and assuming perfectly sinusoidal torque versus position curves for each motor winding, the following formula gives the key characteristics of the composite torque curve:

$$h = (a^2 + b^2)^{0.5}$$

$$x = (S / (\pi/2)) \arctan(b / a)$$

Where:

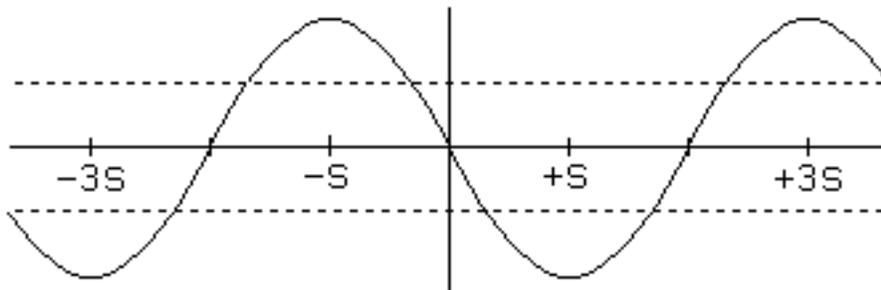
- a -- torque applied by winding with equilibrium at 0 radians.
- b -- torque applied by winding with equilibrium at S radians.
- h -- holding torque of composite.
- x -- equilibrium position, in radians.
- S -- step angle, in radians.

In the absence of saturation, the torques a and b are directly proportional to the currents through the corresponding windings. It is quite common to work with normalized currents and torques, so that the single-winding holding torque or the maximum current allowed in one motor winding is 1.0.

Friction and the Dead Zone

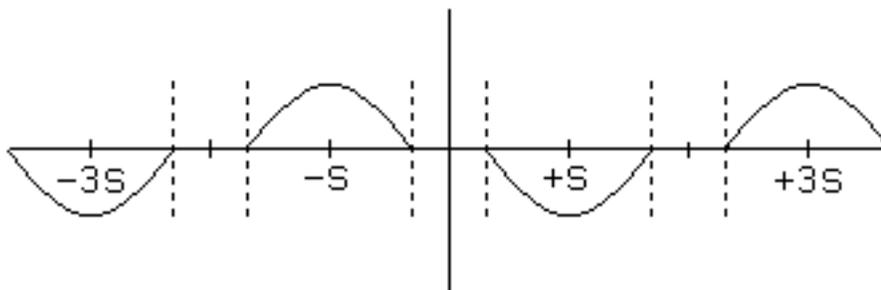
The torque versus position curve shown in Figure 2.1 does not take into account the torque the motor must exert to overcome friction! Note that frictional forces may be divided into two large categories, static or sliding friction, which requires a constant torque to overcome, regardless of velocity, and dynamic friction or viscous drag, which offers a resistance that varies with velocity. Here, we are concerned with the impact of static friction. Suppose the torque needed to overcome the static friction on the driven system is $1/2$ the peak torque of the motor, as illustrated in Figure 2.4.

Figure 2.4



The dotted lines in Figure 2.4 show the torque needed to overcome friction; only that part of the torque curve outside the dotted lines is available to move the rotor. The curve showing the available torque as a function of shaft angle is the difference between these curves, as shown in Figure 2.5:

Figure 2.5



Note that the consequences of static friction are twofold. First, the total torque available to move the load is reduced, and second, there is a *dead zone* about each of the equilibria of the ideal motor. If the motor rotor is positioned anywhere within the dead zone for the current equilibrium position, the frictional torque will exceed the torque applied by the motor windings, and the rotor will not move. Assuming an ideal sinusoidal torque versus position curve in the absence of friction, the angular width of these dead zones will be:

$$d = 2 \left(S / (\pi/2) \right) \arcsin(f/h) = \left(S / (\pi/4) \right) \arcsin(f/h)$$

where:

d -- width of dead zone, in radians

S -- step angle, in radians

f -- torque needed to overcome static friction

h -- holding torque

The important thing to note about the dead zone is that it limits the ultimate positioning accuracy! For the example, where the static friction is 1/2 the peak torque, a 90° per step motor will have dead-zones 60° wide! That means that successive steps may be as large as 150° and as small as 30°, depending on where in the dead zone the rotor stops after each step!

The presence of a dead zone has a significant impact on the utility of microstepping! If the dead zone is x° wide, then microstepping with a step size smaller than x° may not move the rotor at all. Thus, for systems intended to use high resolution microstepping, it is very important to minimize static friction.

Dynamics

Each time you step the motor, you electronically move the equilibrium position S radians. This moves the entire curve illustrated in Figure 2.1 a distance of S radians, as shown in Figure 2.6:

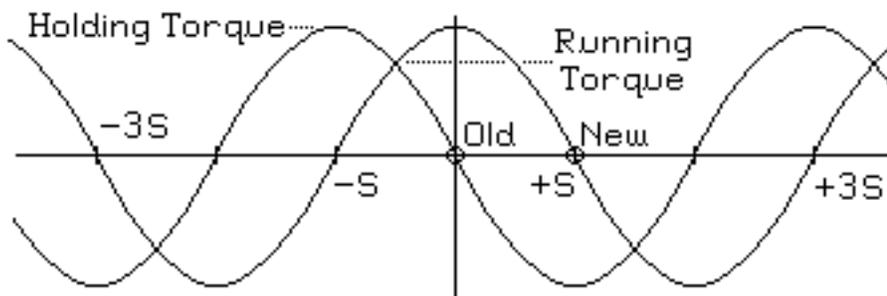


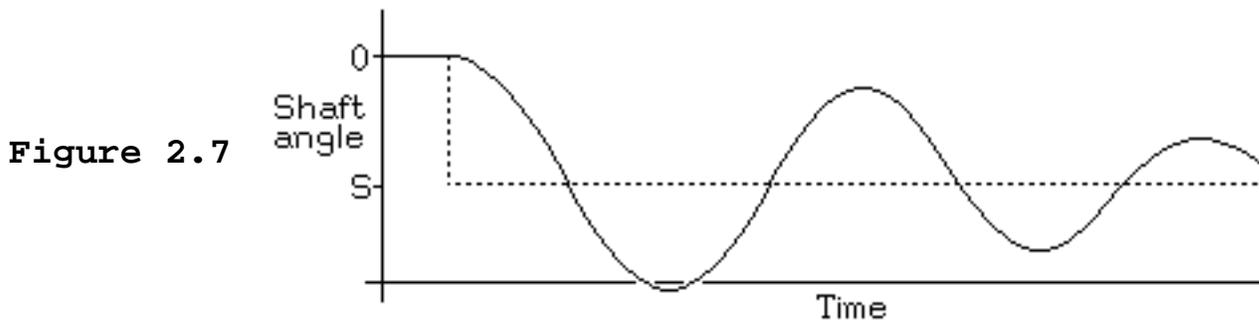
Figure 2.6

The first thing to note about the process of taking one step is that the maximum available torque is at a minimum when the rotor is halfway from one step to the next. This minimum determines the *running torque*, the maximum torque the motor can drive as it steps slowly forward. For common two-winding permanent magnet motors with ideal sinusoidal torque versus position curves and holding torque h , this will be $h/(2^{0.5})$. If the motor is stepped by powering two windings at a time, the running torque of an ideal two-winding permanent magnet motor will be the same as the single-winding holding torque.

It should be noted that at higher stepping speeds, the running torque is sometimes defined as the *pull-out torque*. That is, it is the maximum frictional torque the motor can overcome on a rotating load before the load is pulled out of step by the friction. Some motor data sheets define a second torque

figure, the *pull-in torque*. This is the maximum frictional torque that the motor can overcome to accelerate a stopped load to synchronous speed. The pull-in torques documented on stepping motor data sheets are of questionable value because the pull-in torque depends on the moment of inertia of the load used when they were measured, and few motor data sheets document this!

In practice, there is always some friction, so after the equilibrium position moves one step, the rotor is likely to oscillate briefly about the new equilibrium position. The resulting trajectory may resemble the one shown in Figure 2.7:



Here, the trajectory of the equilibrium position is shown as a dotted line, while the solid curve shows the trajectory of the motor rotor.

Resonance

The resonant frequency of the motor rotor depends on the amplitude of the oscillation; but as the amplitude decreases, the resonant frequency rises to a well-defined small-amplitude frequency. This frequency depends on the step angle and on the ratio of the holding torque to the moment of inertia of the rotor. Either a higher torque or a lower moment will increase the frequency!

Formally, the small-amplitude resonance can be computed as follows: First, recall Newton's law for angular acceleration:

$$T = \mu A$$

Where:

T -- torque applied to rotor

μ -- moment of inertia of rotor and load

A -- angular acceleration, in radians per second per second

We assume that, for small amplitudes, the torque on the rotor can be approximated as a linear function of the displacement from the equilibrium position. Therefore, Hooke's law applies:

$$T = -k \Theta$$

where:

k -- the "spring constant" of the system, in torque units per radian

Θ -- angular position of rotor, in radians

We can equate the two formulas for the torque to get:

$$\mu A = -k \Theta$$

Note that acceleration is the second derivative of position with respect to time:

$$A = d^2\Theta/dt^2$$

so we can rewrite this the above in differential equation form:

$$d^2\Theta/dt^2 = -(k/\mu) \Theta$$

To solve this, recall that, for:

$$f(t) = a \sin bt$$

The derivatives are:

$$df(t)/dt = ab \cos bt$$

$$d^2f(t)/dt^2 = -ab^2 \sin bt = -b^2 f(t)$$

Note that, throughout this discussion, we assumed that the rotor is resonating. Therefore, it has an equation of motion something like:

$$\Theta = a \sin(2\pi f t)$$

a = angular amplitude of resonance

f = resonant frequency

This is an admissible solution to the above differential equation if we agree that:

$$b = 2\pi f$$

$$b^2 = k/\mu$$

Solving for the resonant frequency f as a function of k and μ , we get:

$$f = (k/\mu)^{0.5} / 2\pi$$

It is crucial to note that it is the moment of inertia of the rotor plus any coupled load that matters. The moment of the rotor, in isolation, is irrelevant! Some motor data sheets include information on resonance, but if any load is coupled to the rotor, the resonant frequency will change!

In practice, this oscillation can cause significant problems when the stepping rate is anywhere near a resonant frequency of the system; the result frequently appears as random and uncontrollable motion.

Resonance and the Ideal Motor

Up to this point, we have dealt only with the small-angle spring constant k for the system. This can be measured experimentally, but if the motor's torque versus position curve is sinusoidal, it is also a simple function of the motor's holding torque. Recall that:

$$T = -h \sin(((\pi/2)/S) \Theta)$$

The small angle spring constant k is the negative derivative of T at the origin.

$$k = -dT / d\Theta = - (- h ((\pi/2)/S) \cos(0)) = (\pi/2)(h / S)$$

Substituting this into the formula for frequency, we get:

$$f = ((\pi/2)(h / S) / \mu)^{0.5} / 2\pi = (h / (8\pi \mu S))^{0.5}$$

Given that the holding torque and resonant frequency of the system are easily measured, the easiest way to determine the moment of inertia of the moving parts in a system driven by a stepping motor is indirectly from the above relationship!

$$\mu = h / (8\pi f^2 S)$$

For practical purposes, it is usually not the torque or the moment of inertia that matters, but rather, the maximum sustainable acceleration that matters! Conveniently, this is a simple function of the resonant frequency! Starting with the Newton's law for angular acceleration:

$$A = T / \mu$$

We can substitute the above formula for the moment of inertia as a function of resonant frequency, and then substitute the maximum sustainable running torque as a function of the holding torque to get:

$$A = (h / (2^{0.5})) / (h / (8\pi f^2 S)) = 8\pi S f^2 / (2^{0.5})$$

Measuring acceleration in steps per second squared instead of in radians per second squared, this simplifies to:

$$A_{\text{steps}} = A / S = 8\pi f^2 / (2^{0.5})$$

Thus, for an ideal motor with a sinusoidal torque versus rotor position function, the maximum

acceleration in steps per second squared is a trivial function of the resonant frequency of the motor and rigidly coupled load!

For a two-winding permanent-magnet or variable-reluctance motor, with an ideal sinusoidal torque-versus-position characteristic, the two-winding holding torque is a simple function of the single-winding holding torque:

$$h_2 = 2^{0.5} h_1$$

Where:

h_1 -- single-winding holding torque

h_2 -- two-winding holding torque

Substituting this into the formula for resonant frequency, we can find the ratios of the resonant frequencies in these two operating modes:

$$f_1 = (h_1 / \dots)^{0.5}$$

$$f_2 = (h_2 / \dots)^{0.5} = (2^{0.5} h_1 / \dots)^{0.5} = 2^{0.25} (h_1 / \dots)^{0.5} = 2^{0.25} f_1 = 1.189\dots f_1$$

This relationship only holds if the torque provided by the motor does not vary appreciably as the stepping rate varies between these two frequencies.

In general, as will be discussed [later](#), the available torque will tend to remain relatively constant up until some cutoff stepping rate, and then it will fall. Therefore, this relationship only holds if the resonant frequencies are below this cutoff stepping rate. At stepping rates above the cutoff rate, the two frequencies will be closer to each other!

Living with Resonance

If a rigidly mounted stepping motor is rigidly coupled to a frictionless load and then stepped at a frequency near the resonant frequency, energy will be pumped into the resonant system, and the result of this is that the motor will literally lose control. There are three basic ways to deal with this problem:

Controlling resonance in the mechanism

Use of elastomeric motor mounts or elastomeric couplings between motor and load can drain energy out of the resonant system, preventing energy from accumulating to the extent that it allows the motor rotor to escape from control.

Or, viscous damping can be used. Here, the damping will not only draw energy out of the resonant modes of the system, but it will also subtract from the total torque available at higher speeds.

Magnetic eddy current damping is equivalent to viscous damping for our purposes.

Figure 2.8 illustrates the use of elastomeric couplings and viscous damping in two typical stepping motor applications, one using a lead screw to drive a load, and the other using a tendon drive:

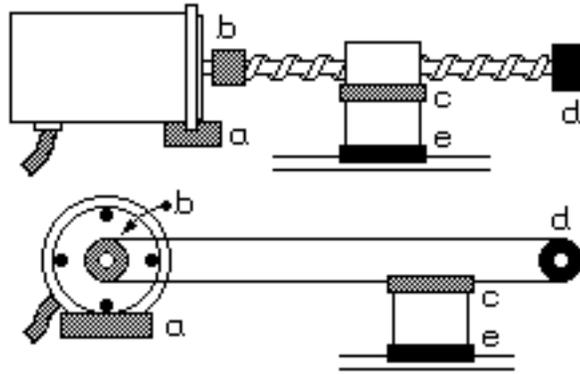


Figure 2.8

In Figure 2.8, elastomeric motor mounts are shown at a and elastomeric couplings between the motor and load are shown at b and c. The end bearing for the lead screw or tendon, at d, offers an opportunity for viscous damping, as do the ways on which the load slides, at e. Even the friction found in sealed ballbearings or teflon on steel ways can provide enough damping to prevent resonance problems.

Controlling resonance in the low-level drive circuitry

A resonating motor rotor will induce an alternating current voltage in the motor windings. If some motor winding is not currently being driven, shorting this winding will impose a drag on the motor rotor that is exactly equivalent to using a magnetic eddy current damper.

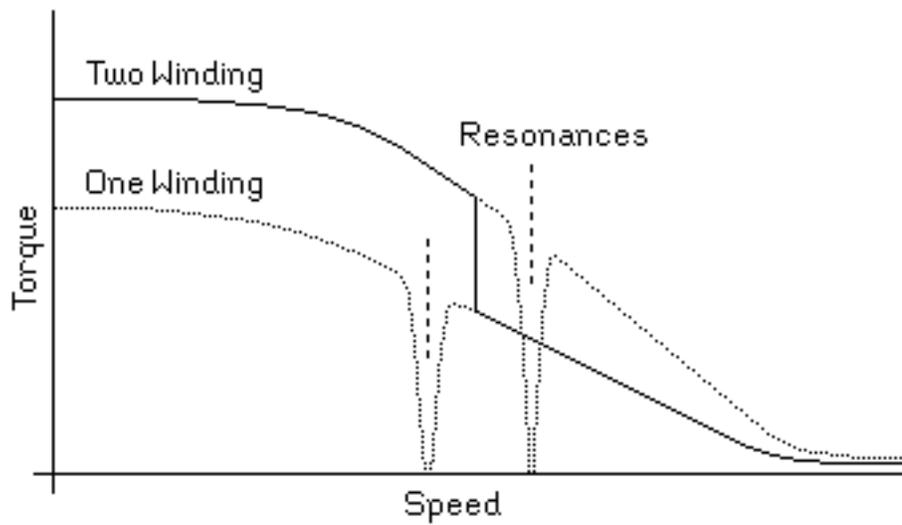
If some motor winding is currently being driven, the AC voltage induced by the resonance will tend to modulate the current through the winding. Clamping the motor current with an external inductor will counteract the resonance. Schemes based on this idea are incorporated into some of the drive circuits illustrated in later sections of this tutorial.

Controlling resonance in the high-level control system

The high level control system can avoid driving the motor at known resonant frequencies, accelerating and decelerating through these frequencies and never attempting sustained rotation at these speeds.

Recall that the resonant frequency of a motor in half-stepped mode will vary by up to 20% from one half-step to the next. As a result, half-stepping pumps energy into the resonant system less efficiently than full stepping. Furthermore, when operating near these resonant frequencies, the motor control system may preferentially use only the two-winding half steps when operating near the single-winding resonant frequency, and only the single-winding half steps when operating near the two-winding resonant frequency. Figure 2.9 illustrates this:

Figure 2.9

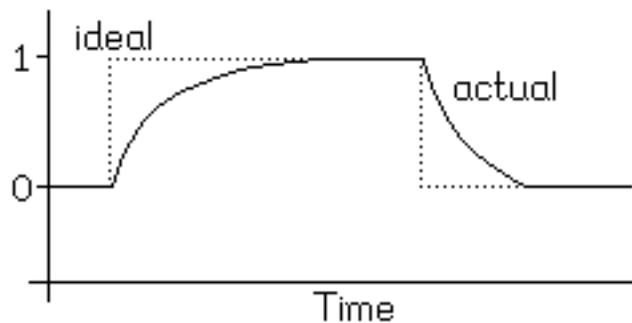


The darkened curve in Figure 2.9 shows the operating torque achieved by a simple control scheme that delivers useful torque over a wide range of speeds despite the fact that the available torque drops to zero at each resonance in the system. This solution is particularly effective if the resonant frequencies are sharply defined and well separated. This will be the case in minimally damped systems operating well below the cutoff speed defined in the next section.

Torque versus Speed

An important consideration in designing high-speed stepping motor controllers is the effect of the inductance of the motor windings. As with the torque versus angular position information, this is frequently poorly documented in motor data sheets, and indeed, for variable reluctance stepping motors, it is not a constant! The inductance of the motor winding determines the rise and fall time of the current through the windings. While we might hope for a square-wave plot of current versus time, the inductance forces an exponential, as illustrated in Figure 2.10:

Figure 2.10



The details of the current-versus-time function through each winding depend as much on the drive circuitry as they do on the motor itself! It is quite common for the time constants of these exponentials to differ. The rise time is determined by the drive voltage and drive circuitry, while the fall time depends on the circuitry used to dissipate the stored energy in the motor winding.

At low stepping rates, the rise and fall times of the current through the motor windings has little effect on the motor's performance, but at higher speeds, the effect of the inductance of the motor windings is to reduce the available torque, as shown in Figure 2.11:

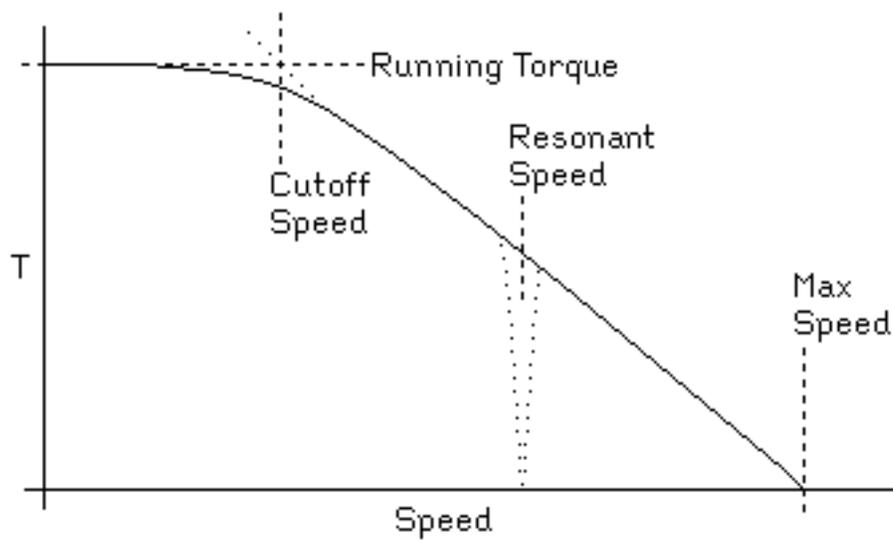


Figure 2.11

The motor's *maximum speed* is defined as the speed at which the available torque falls to zero. Measuring maximum speed can be difficult when there are resonance problems, because these cause the torque to drop to zero prematurely. The *cutoff speed* is the speed above which the torque begins to fall. When the motor is operating below its cutoff speed, the rise and fall times of the current through the motor windings occupy an insignificant fraction of each step, while at the cutoff speed, the step duration is comparable to the sum of the rise and fall times. Note that a sharp cutoff is rare, and therefore, statements of a motor's cutoff speed are, of necessity, approximate.

The details of the torque versus speed relationship depend on the details of the rise and fall times in the motor windings, and these depend on the motor control system as well as the motor. Therefore, the cutoff speed and maximum speed for any particular motor depend, in part, on the control system! The torque versus speed curves published in motor data sheets occasionally come with documentation of the motor controller used to obtain that curve, but this is far from universal practice!

Similarly, the resonant speed depends on the moment of inertia of the entire rotating system, not just the motor rotor, and the extent to which the torque drops at resonance depends on the presence of mechanical damping and on the nature of the control system. Some published torque versus speed curves show very clear resonances without documenting the moment of inertia of the hardware that may have been attached to the motor shaft in order to make torque measurements.

The torque versus speed curve shown in Figure 2.11 is typical of the simplest of control systems. More complex control systems sometimes introduce electronic resonances that act to increase the available torque above the motor's low-speed torque. A common result of this is a peak in the available torque near the cutoff speed.

Electromagnetic Issues

In a permanent magnet or hybrid stepping motor, the magnetic field of the motor rotor changes with changes in shaft angle. The result of this is that turning the motor rotor induces an AC voltage in each motor winding. This is referred to as the *counter EMF* because the voltage induced in each motor

winding is always in phase with and counter to the ideal waveform required to turn the motor in the same direction. Both the frequency and amplitude of the counter EMF increase with rotor speed, and therefore, counter EMF contributes to the decline in torque with increased stepping rate.

Variable reluctance stepping motors also induce counter EMF! This is because, as the stator winding pulls a tooth of the rotor towards its equilibrium position, the reluctance of the magnetic circuit declines. This decline increases the inductance of the stator winding, and this change in inductance demands a decrease in the current through the winding in order to conserve energy. This decrease is evidenced as a counter EMF.

The reactance (inductance and resistance) of the motor windings limits the current flowing through them. Thus, by ohms law, increasing the voltage will increase the current, and therefore increase the available torque. The increased voltage also serves to overcome the counter EMF induced in the motor windings, but the voltage cannot be increased arbitrarily! Thermal, magnetic and electronic considerations all serve to limit the useful torque that a motor can produce.

The heat given off by the motor windings is due to both simple resistive losses, eddy current losses, and hysteresis losses. If this heat is not conducted away from the motor adequately, the motor windings will overheat. The simplest failure this can cause is insulation breakdown, but it can also heat a permanent magnet rotor to above its curie temperature, the temperature at which permanent magnets lose their magnetization. This is a particular risk with many modern high strength magnetic alloys.

Even if the motor is attached to an adequate heat sink, increased drive voltage will not necessarily lead to increased torque. Most motors are designed so that, with the rated current flowing through the windings, the magnetic circuits of the motor are near saturation. Increased current will not lead to an appreciably increased magnetic field in such a motor!

Given a drive system that limits the current through each motor winding to the rated maximum for that winding, but uses high voltages to achieve a higher cutoff torque and higher torques above cutoff, there are other limits that come into play. At high speeds, the motor windings must, of necessity, carry high frequency AC signals. This leads to eddy current losses in the magnetic circuits of the motor, and it leads to skin effect losses in the motor windings.

Motors designed for very high speed running should, therefore, have magnetic structures using very thin laminations or even nonconductive ferrite materials, and they should have small gauge wire in their windings to minimize skin effect losses. Common high torque motors have large-gauge motor windings and coarse core laminations, and at high speeds, such motors can easily overheat and should therefore be derated accordingly for high speed running!

It is also worth noting that the best way to demagnetize something is to expose it to a high frequency-high amplitude magnetic field. Running the control system to spin the rotor at high speed when the rotor is actually stalled, or spinning the rotor at high speed against a control system trying to hold the rotor in a fixed position will both expose the rotor to a high amplitude high-frequency field. If such operating conditions are common, particularly if the motor is run near the curie temperature of the permanent magnets, demagnetization is a serious risk and the field strengths (and expected torques)

should be reduced accordingly!

3. Basic Stepping Motor Control Circuits

Part of [Stepping Motors](#)

by [Douglas W. Jones](#)

[THE UNIVERSITY OF IOWA Department of Computer Science](#)

- [Introduction](#)
- [Variable Reluctance Motors](#)
- [Unipolar Permanent Magnet and Hybrid Motors](#)
- [Practical Unipolar and Variable Reluctance Drivers](#)
- [Bipolar Motors and H-Bridges](#)
- [Practical Bipolar Drive Circuits](#)

Introduction

This section of the stepper tutorial deals with the basic final stage drive circuitry for stepping motors. This circuitry is centered on a single issue, switching the current in each motor winding on and off, and controlling its direction. The circuitry discussed in this section is connected directly to the motor windings and the motor power supply, and this circuitry is controlled by a digital system that determines when the switches are turned on or off.

This section covers all types of motors, from the elementary circuitry needed to control a variable reluctance motor, to the H-bridge circuitry needed to control a bipolar permanent magnet motor. Each class of drive circuit is illustrated with practical examples, but these examples are not intended as an exhaustive catalog of the commercially available control circuits, nor is the information given here intended to substitute for the information found on the manufacturer's component data sheets for the parts mentioned.

This section only covers the most elementary control circuitry for each class of motor. All of these circuits assume that the motor power supply provides a drive voltage no greater than the motor's rated voltage, and this significantly limits motor performance. The next section, on current limited drive circuitry, covers practical high-performance drive circuits.

Variable Reluctance Motors

Typical controllers for variable reluctance stepping motors are variations on the outline shown in Figure 3.1:

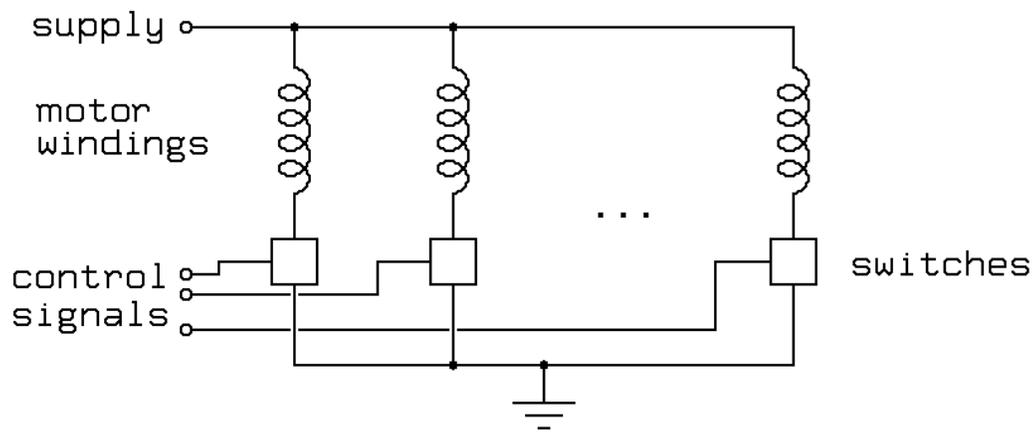


Figure 3.1

In Figure 3.1, boxes are used to represent switches; a control unit, not shown, is responsible for providing the control signals to open and close the switches at the appropriate times in order to spin the motors. In many cases, the control unit will be a computer or programmable interface controller, with software directly generating the outputs needed to control the switches, but in other cases, additional control circuitry is introduced, sometimes gratuitously!

Motor windings, solenoids and similar devices are all inductive loads. As such, the current through the motor winding cannot be turned on or off instantaneously without involving infinite voltages! When the switch controlling a motor winding is closed, allowing current to flow, the result of this is a slow rise in current. When the switch controlling a motor winding is opened, the result of this is a voltage spike that can seriously damage the switch unless care is taken to deal with it appropriately.

There are two basic ways of dealing with this voltage spike. One is to bridge the motor winding with a diode, and the other is to bridge the motor winding with a capacitor. Figure 3.2 illustrates both approaches:

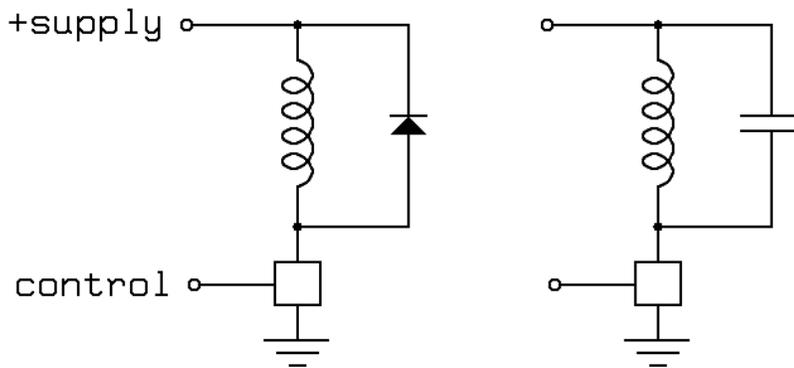


Figure 3.2

The diode shown in Figure 3.2 must be able to conduct the full current through the motor winding, but it will only conduct briefly each time the switch is turned off, as the current through the winding decays. If relatively slow diodes such as the common 1N400X family are used together with a fast switch, it may be necessary to add a small capacitor in parallel with the diode.

The capacitor shown in Figure 3.2 poses more complex design problems! When the switch is closed, the capacitor will discharge through the switch to ground, and the switch must be able to handle this brief spike of discharge current. A resistor in series with the capacitor or in series with the power supply will limit this current. When the switch is opened, the stored energy in the motor winding will charge the capacitor up to

a voltage significantly above the supply voltage, and the switch must be able to tolerate this voltage. To solve for the size of the capacitor, we equate the two formulas for the stored energy in a resonant circuit:

$$P = C V^2 / 2$$

$$P = L I^2 / 2$$

Where:

P -- stored energy, in watt seconds or coulomb volts

C -- capacity, in farads

V -- voltage across capacitor

L -- inductance of motor winding, in henrys

I -- current through motor winding

Solving for the minimum size of capacitor required to prevent overvoltage on the switch is fairly easy:

$$C > L I^2 / (V_b - V_s)^2$$

Where:

V_b -- the breakdown voltage of the switch

V_s -- the supply voltage

Variable reluctance motors have variable inductance that depends on the shaft angle. Therefore, worst-case design must be used to select the capacitor. Furthermore, motor inductances are frequently poorly documented, if at all.

The capacitor and motor winding, in combination, form a resonant circuit. If the control system drives the motor at frequencies near the resonant frequency of this circuit, the motor current through the motor windings, and therefore, the torque exerted by the motor, will be quite different from the steady state torque at the nominal operating voltage! The resonant frequency is:

$$f = 1 / (2\pi (L C)^{0.5})$$

Again, the electrical resonant frequency for a variable reluctance motor will depend on shaft angle! When a variable reluctance motor is operated with the exciting pulses near resonance, the oscillating current in the motor winding will lead to a magnetic field that goes to zero at twice the resonant frequency, and this can severely reduce the available torque!

Unipolar Permanent Magnet and Hybrid Motors

Typical controllers for unipolar stepping motors are variations on the outline shown in Figure 3.3:

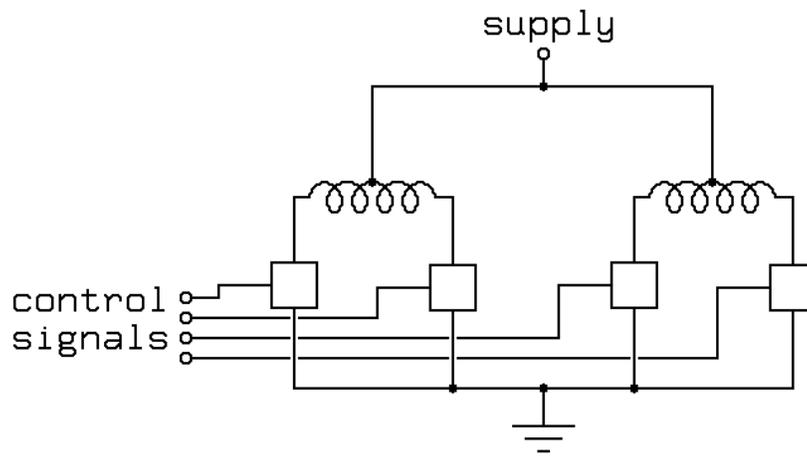


Figure 3.3

In Figure 3.3, as in Figure 3.1, boxes are used to represent switches; a control unit, not shown, is responsible for providing the control signals to open and close the switches at the appropriate times in order to spin the motors. The control unit is commonly a computer or programmable interface controller, with software directly generating the outputs needed to control the switches.

As with drive circuitry for variable reluctance motors, we must deal with the inductive kick produced when each of these switches is turned off. Again, we may shunt the inductive kick using diodes, but now, 4 diodes are required, as shown in Figure 3.4:

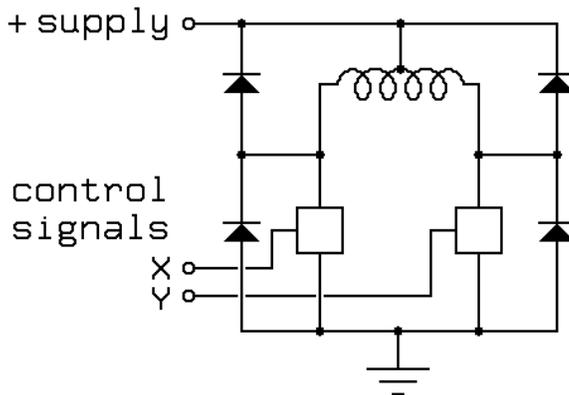
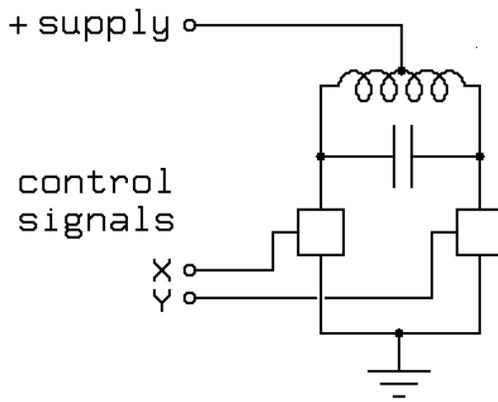


Figure 3.4

The extra diodes are required because the motor winding is not two independent inductors, it is a single center-tapped inductor with the center tap at a fixed voltage. This acts as an autotransformer! When one end of the motor winding is pulled down, the other end will fly up, and visa versa. When a switch opens, the inductive kickback will drive that end of the motor winding to the positive supply, where it is clamped by the diode. The opposite end will fly downward, and if it was not floating at the supply voltage at the time, it will fall below ground, reversing the voltage across the switch at that end. Some switches are immune to such reversals, but others can be seriously damaged.

A capacitor may also be used to limit the kickback voltage, as shown in Figure 3.5:

Figure 3.5



The rules for sizing the capacitor shown in Figure 3.5 are the same as the rules for sizing the capacitor shown in Figure 3.2, but the effect of resonance is quite different! With a permanent magnet motor, if the capacitor is driven at or near the resonant frequency, the torque will increase to as much as twice the low-speed torque! The resulting torque versus speed curve may be quite complex, as illustrated in Figure 3.6:

Figure 3.6

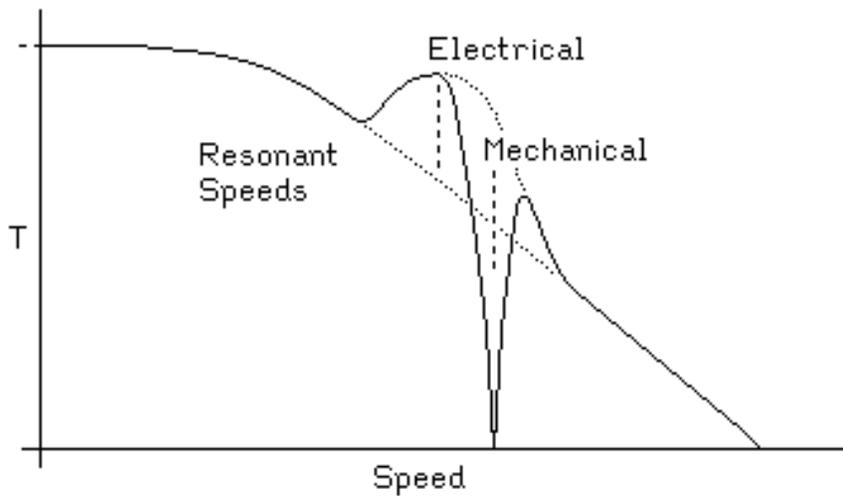


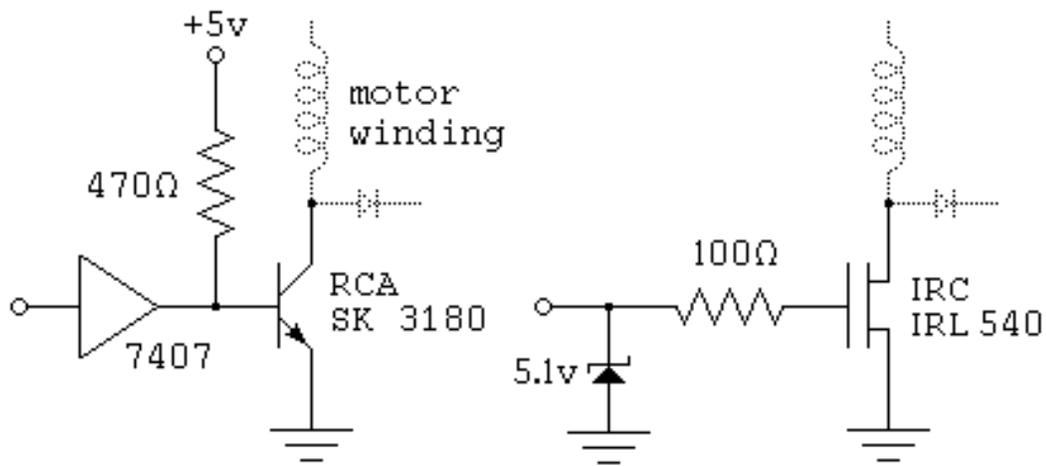
Figure 3.6 shows a peak in the available torque at the electrical resonant frequency, and a valley at the mechanical resonant frequency. If the electrical resonant frequency is placed appropriately above what would have been the cutoff speed for the motor using a diode-based driver, the effect can be a considerable increase in the effective cutoff speed.

The mechanical resonant frequency depends on the torque, so if the mechanical resonant frequency is anywhere near the electrical resonance, it will be shifted by the electrical resonance! Furthermore, the width of the mechanical resonance depends on the local slope of the torque versus speed curve; if the torque drops with speed, the mechanical resonance will be sharper, while if the torque climbs with speed, it will be broader or even split into multiple resonant frequencies.

Practical Unipolar and Variable Reluctance Drivers

In the above circuits, the details of the necessary switches have been deliberately ignored. Any switching technology, from toggle switches to power MOSFETS will work! Figure 3.7 contains some suggestions for implementing each switch, with a motor winding and protection diode included for orientation purposes:

Figure 3.7



Each of the switches shown in Figure 3.7 is compatible with a TTL input. The 5 volt supply used for the logic, including the 7407 open-collector driver used in the figure, should be well regulated. The motor power, typically between 5 and 24 volts, needs only minimal regulation. It is worth noting that these power switching circuits are appropriate for driving solenoids, DC motors and other inductive loads as well as for driving stepping motors.

The SK3180 transistor shown in Figure 3.7 is a power darlington with a current gain over 1000; thus, the 10 milliamps flowing through the 470 ohm bias resistor is more than enough to allow the transistor to switch a few amps current through the motor winding. The 7407 buffer used to drive the darlington may be replaced with any high-voltage open collector chip that can sink at least 10 milliamps. In the event that the transistor fails, the high-voltage open collector driver serves to protect the rest of the logic circuitry from the motor power supply.

The IRC IRL540 shown in Figure 3.7 is a power field effect transistor. This can handle currents of up to about 20 amps, and it breaks down nondestructively at 100 volts; as a result, this chip can absorb inductive spikes without protection diodes if it is attached to a large enough heat sink. This transistor has a very fast switching time, so the protection diodes must be comparably fast or bypassed by small capacitors. This is particularly essential with the diodes used to protect the transistor against reverse bias! In the event that the transistor fails, the zener diode and 100 ohm resistor protect the TTL circuitry. The 100 ohm resistor also acts to somewhat slow the switching times on the transistor.

For applications where each motor winding draws under 500 milliamps, the [ULN200x](#) family of darlington arrays from [Allegro Microsystems](#), also available as the [DS200x](#) from [National Semiconductor](#) and as the Motorola MC1413 darlington array will drive multiple motor windings or other inductive loads directly from logic inputs. Figure 3.8 shows the pinout of the widely available ULN2003 chip, an array of 7 darlington transistors with TTL compatible inputs:

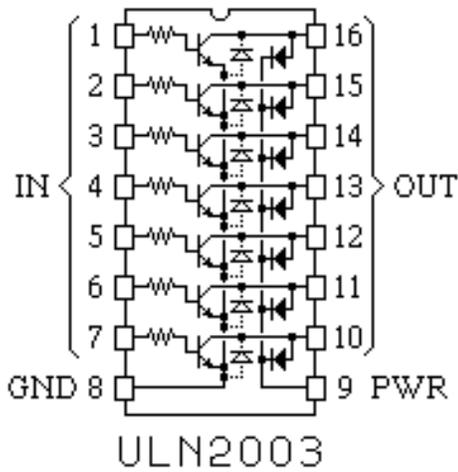


Figure 3.8

The base resistor on each darlington transistor is matched to standard bipolar TTL outputs. Each NPN darlington is wired with its emitter connected to pin 8, intended as a ground pin. Each transistor in this package is protected by two diodes, one shorting the emitter to the collector, protecting against reverse voltages across the transistor, and one connecting the collector to pin 9; if pin 9 is wired to the positive motor supply, this diode will protect the transistor against inductive spikes.

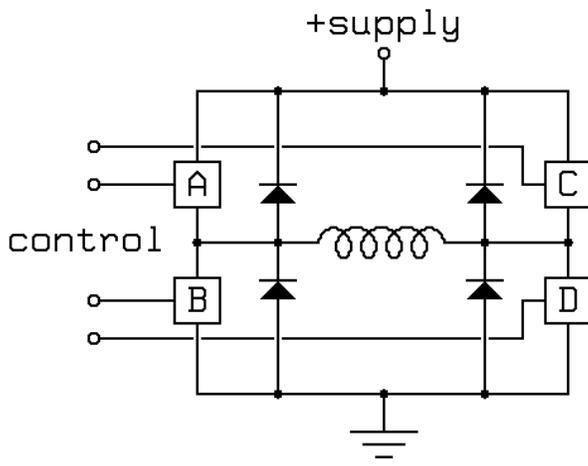
The ULN2803 chip is essentially the same as the ULN2003 chip described above, except that it is in an 18-pin package, and contains 8 darlington transistors, allowing one chip to be used to drive a pair of common unipolar permanent-magnet or variable-reluctance motors.

For motors drawing under 600 milliamps per winding, the [UDN2547B](#) quad power driver made by [Allegro Microsystems](#) will handle all 4 windings of common unipolar stepping motors. For motors drawing under 300 milliamps per winding, Texas Instruments SN7541, 7542 and 7543 dual power drivers are a good choice; both of these alternatives include some logic with the power drivers.

Bipolar Motors and H-Bridges

Things are more complex for bipolar permanent magnet stepping motors because these have no center taps on their windings. Therefore, to reverse the direction of the field produced by a motor winding, we need to reverse the current through the winding. We could use a double-pole double throw switch to do this electromechanically; the electronic equivalent of such a switch is called an H-bridge and is outlined in Figure 3.9:

Figure 3.9



As with the unipolar drive circuits discussed previously, the switches used in the H-bridge must be protected from the voltage spikes caused by turning the power off in a motor winding. This is usually done with diodes, as shown in Figure 3.9.

It is worth noting that H-bridges are applicable not only to the control of bipolar stepping motors, but also to the control of DC motors, push-pull solenoids (those with permanent magnet plungers) and many other applications.

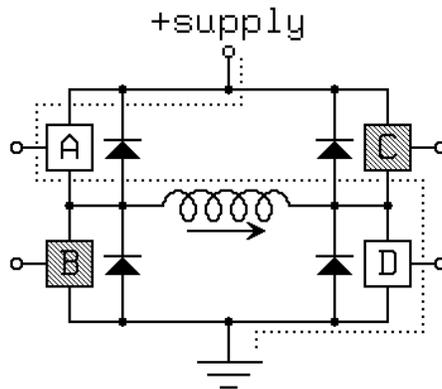
With 4 switches, the basic H-bridge offers 16 possible operating modes, 7 of which short out the power supply! The following operating modes are of interest:

Forward mode, switches A and D closed.

Reverse mode, switches B and C closed.

These are the usual operating modes, allowing current to flow from the supply, through the motor winding and onward to ground. Figure 3.10 illustrates forward mode:

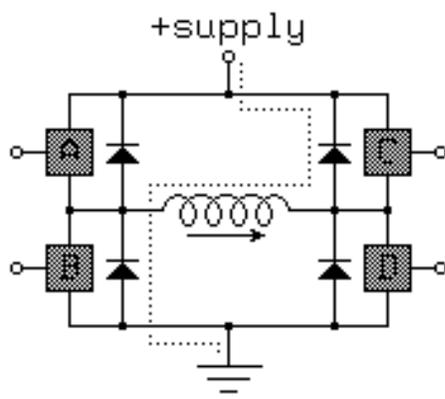
Figure 3.10



Fast decay mode or *coasting mode*, all switches open.

Any current flowing through the motor winding will be working against the full supply voltage, plus two diode drops, so current will decay quickly. This mode provides little or no dynamic braking effect on the motor rotor, so the rotor will coast freely if all motor windings are powered in this mode. Figure 3.11 illustrates the current flow immediately after switching from forward running mode to fast decay mode.

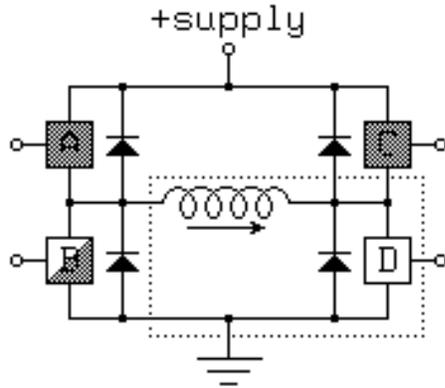
Figure 3.11



Slow decay modes or dynamic braking modes.

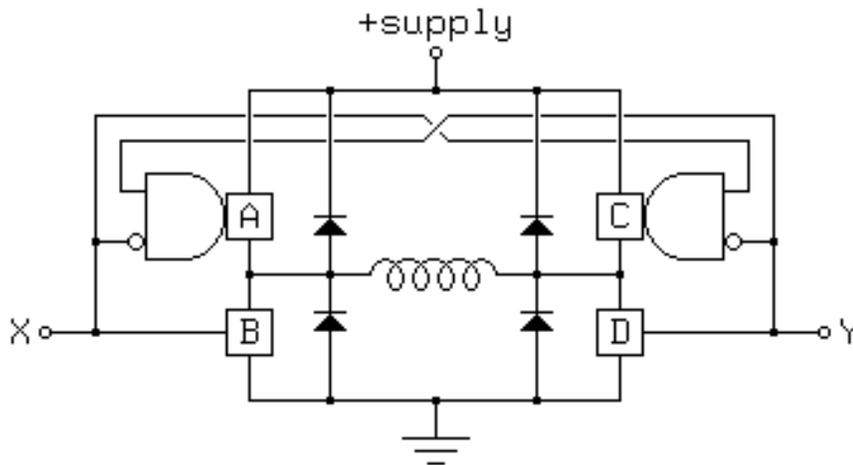
In these modes, current may recirculate through the motor winding with minimum resistance. As a result, if current is flowing in a motor winding when one of these modes is entered, the current will decay slowly, and if the motor rotor is turning, it will induce a current that will act as a brake on the rotor. Figure 3.12 illustrates one of the many useful slow-decay modes, with switch D closed; if the motor winding has recently been in forward running mode, the state of switch B may be either open or closed:

Figure 3.12



Most H-bridges are designed so that the logic necessary to prevent a short circuit is included at a very low level in the design. Figure 3.13 illustrates what is probably the best arrangement:

Figure 3.13



Here, the following operating modes are available:

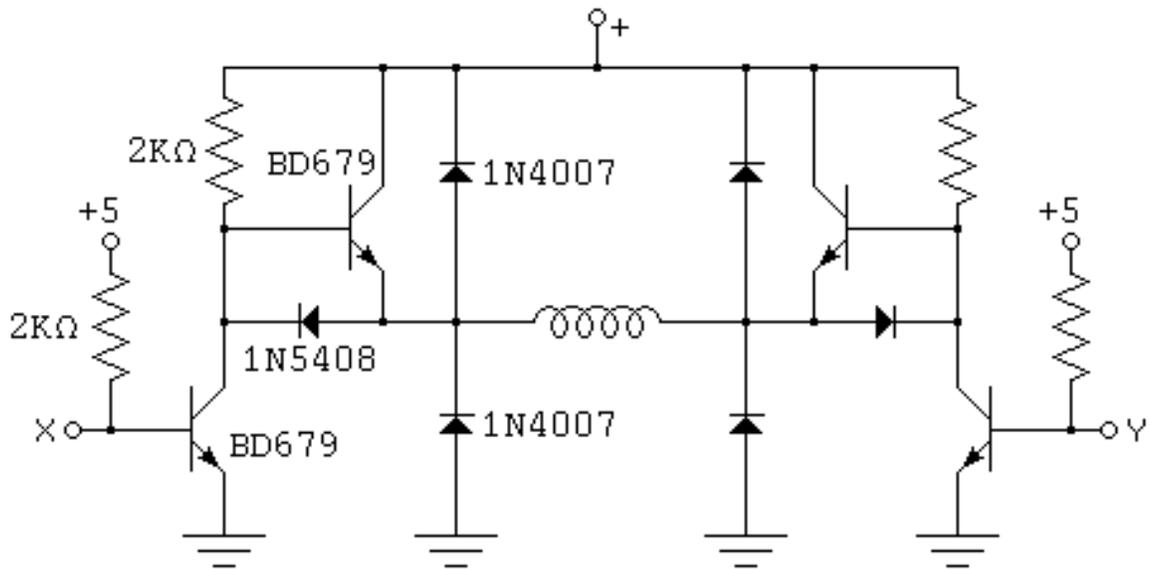
XY	ABCD	Mode
00	0000	fast decay
01	1001	forward
10	0110	reverse
11	0101	slow decay

The advantage of this arrangement is that all of the useful operating modes are preserved, and they are encoded with a minimum number of bits; the latter is important when using a microcontroller or computer system to drive the H-bridge because many such systems have only limited numbers of bits available for parallel output. Sadly, few of the integrated H-bridge chips on the market have such a simple control scheme.

Practical Bipolar Drive Circuits

There are a number of integrated H-bridge drivers on the market, but it is still useful to look at discrete component implementations for an understanding of how an H-bridge works. Antonio Raposo (ajr@cybill.inesc.pt) suggested the H-bridge circuit shown in Figure 3.14;

Figure 3.14



The X and Y inputs to this circuit can be driven by open collector TTL outputs as in the darlington-based unipolar drive circuit in Figure 3.7. The motor winding will be energised if exactly one of the X and Y inputs is high and exactly one of them is low. If both are low, both pull-down transistors will be off. If both are high, both pull-up transistors will be off. As a result, this simple circuit puts the motor in dynamic braking mode in both the 11 and 00 states, and does not offer a coasting mode.

The circuit in Figure 3.14 consists of two identical halves, each of which may be properly described as a push-pull driver. The term half H-bridge is sometimes applied to these circuits! It is also worth noting that a half H-bridge has a circuit quite similar to the output drive circuit used in TTL logic. In fact, TTL tri-state line drivers such as the 74LS125A and the 74LS244 can be used as half H-bridges for small loads, as illustrated in Figure 3.15:

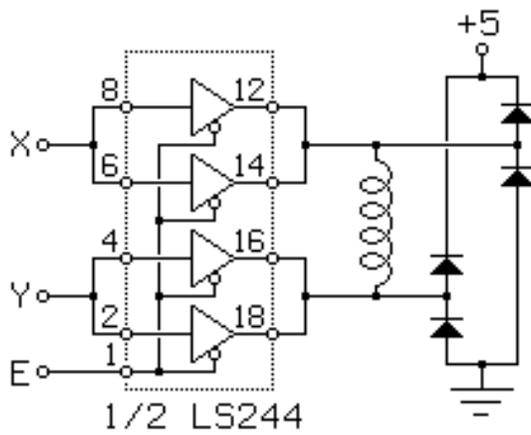


Figure 3.15

This circuit is effective for driving motors with up to about 50 ohms per winding at voltages up to about 4.5 volts using a 5 volt supply. Each tri-state buffer in the LS244 can sink about twice the current it can source, and the internal resistance of the buffers is sufficient, when sourcing current, to evenly divide the current between the drivers that are run in parallel. This motor drive allows for all of the useful states achieved by the driver in Figure 3.13, but these states are not encoded as efficiently:

XYE	Mode
--1	fast decay
000	slower decay
010	forward
100	reverse
110	slow decay

The second dynamic braking mode, $XYE=110$, provides a slightly weaker braking effect than the first because of the fact that the LS244 drivers can sink more current than they can source.

The [Microchip](#) (formerly Telcom Semiconductor) [TC4467](#) Quad CMOS driver is another example of a general purpose driver that can be used as 4 independent half H-bridges. Unlike earlier drivers, the data sheet for this driver even suggests using it for motor control applications, with supply voltages up to 18 volts and up to 250 milliamps per motor winding.

One of the problems with commercially available stepping motor control chips is that many of them have relatively short market lifetimes. For example, the Seagate IPxMxx series of dual H-bridge chips (IP1M10 through IP3M12) were very well thought out, but unfortunately, it appears that Seagate only made these when they used stepping motors for head positioning in Seagate disk drives. The Toshiba TA7279 dual H-bridge driver would be another another excellent choice for motors under 1 amp, but again, it appears to have been made for internal use only.

The [SGS-Thompson](#) (and others) [L293](#) dual H-bridge is a close competitor for the above chips, but unlike them, it does not include protection diodes. The [L293D](#) chip, introduced later, is pin compatible and includes these diodes. If the earlier L293 is used, each motor winding must be set across a bridge rectifier (1N4001 equivalent). The use of external diodes allows a series resistor to be put in the current recirculation path to speed the decay of the current in a motor winding when it is turned off; this may be

desirable in some applications. The L293 family offers excellent choices for driving small bipolar steppers drawing up to one amp per motor winding at up to 36 volts. Figure 3.16 shows the pinout common to the L293B and L293D chips:

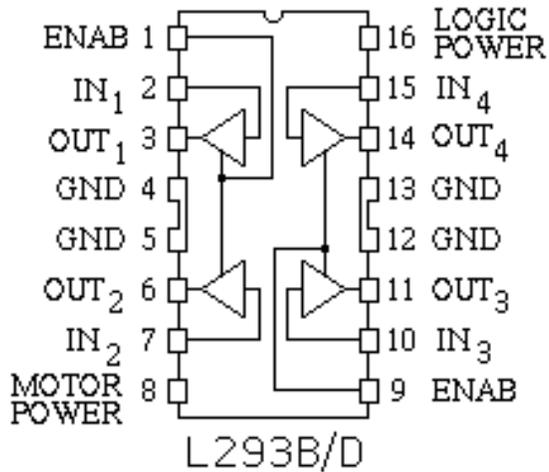


Figure 3.16

This chip may be viewed as 4 independent half H-bridges, enabled in pairs, or as two full H-bridges. This is a power DIP package, with pins 4, 5, 12 and 13 designed to conduct heat to the PC board or to an external heat sink.

The [SGS-Thompson](#) (and others) [L298](#) dual H-bridge is quite similar to the above, but is able to handle up to 2-amps per channel and is packaged as a power component; as with the LS244, it is safe to wire the two H-bridges in the L298 package into one 4-amp H-bridge (the data sheet for this chip provides specific advice on how to do this). One warning is appropriate concerning the L298; this chip very fast switches, fast enough that commonplace protection diodes (1N400X equivalent) don't work. Instead, use a diode such as the BYV27. The National Semiconductor LMD18200 H-bridge is another good example; this handles up to 3 amps and has integral protection diodes.

While integrated H-bridges are not available for very high currents or very high voltages, there are well designed components on the market to simplify the construction of H-bridges from discrete switches. For example, [International Rectifier](#) sells a line of half H-bridge drivers; two of these chips plus 4 MOSFET switching transistors suffice to build an H-bridge. The [IR2101](#), [IR2102](#) and [IR2103](#) are basic half H-bridge drivers. Each of these chips has 2 logic inputs to directly control the two switching transistors on one leg of an H-bridge. The [IR2104](#) and [IR2111](#) have similar output-side logic for controlling the switches of an H-bridge, but they also include input-side logic that, in some applications, may reduce the need for external logic. In particular, the 2104 includes an enable input, so that 4 2104 chips plus 8 switching transistors can replace an L293 with no need for additional logic.

The data sheet for the [Microchip](#) (formerly Telcom Semiconductor) [TC4467](#) family of quad CMOS drivers includes information on how to use drivers in this family to drive the power MOSFETs of H-bridges running at up to 15 volts.

A number of manufacturers make complex H-bridge chips that include current limiting circuitry; these are the subject of the next section. It is also worth noting that there are a number of 3-phase bridge drivers on the market, appropriate for driving Y or delta configured 3-phase permanent magnet steppers. Few such motors are available, and these chips were not developed with steppers in mind. Nonetheless, the Toshiba

TA7288P, the GL7438, the TA8400 and TA8405 are clean designs, and 2 such chips, with one of the 6 half-bridges ignored, will cleanly control a 5-winding 10 step per revolution motor.

4. Current Limiting for Stepping Motors

Part of [Stepping Motors](#)

by [Douglas W. Jones](#)

[THE UNIVERSITY OF IOWA Department of Computer Science](#)

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- [Resistive Current Limiters](#)
- [Linear Current Limiters](#)

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Introduction

Small stepping motors, such as those used for head positioning on floppy disk drives, are usually driven at a low DC voltage, and the current through the motor windings is usually limited by the internal resistance of the winding. High torque motors, on the other hand, are frequently built with very low resistance windings; when driven by any reasonable supply voltage, these motors typically require external current limiting circuitry.

There is good reason to run a stepping motor at a supply voltage above that needed to push the maximum rated current through the motor windings. Running a motor at higher voltages leads to a faster rise in the current through the windings when they are turned on, and this, in turn, leads to a higher cutoff speed for the motor and higher torques at speeds above the cutoff.

Microstepping, where the control system positions the motor rotor between half steps, also requires external current limiting circuitry. For example, to position the rotor 1/4 of the way from one step to another, it might be necessary to run one motor winding at full current while the other is run at approximately 1/3 of that current.

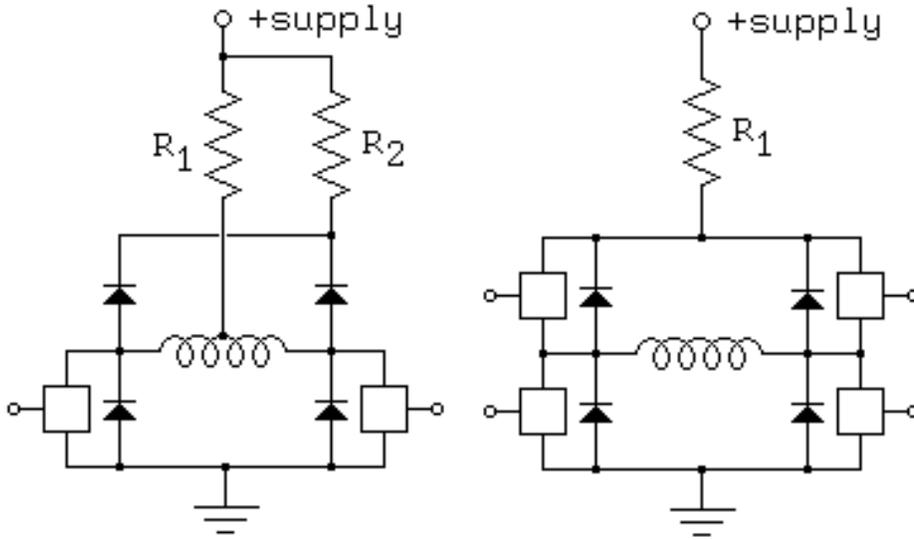
The remainder of this section discusses various circuits for limiting the current through the windings of a

stepping motor, starting with simple resistive limiters and moving up to choppers and other switching regulators. Most of these current limiters are appropriate for many other applications, including limiting the current through conventional DC motors and other inductive loads.

Resistive Current Limiters

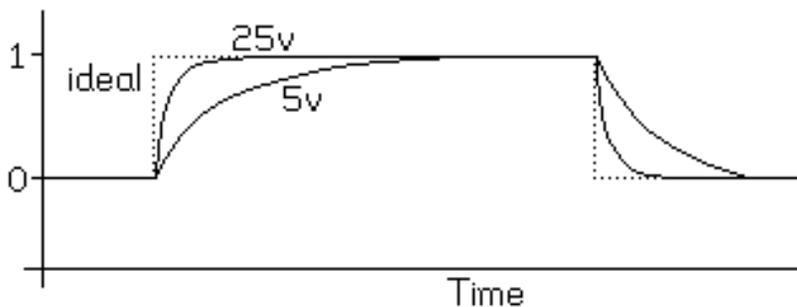
The easiest to understand current limiter is a series resistor. Most motor manufacturers recommended this approach in their literature up until the early 1980's, and most motor data sheets still give performance curves for motors driven by such circuits. The typical circuits used to control the current through one winding of a permanent magnet or hybrid motor are shown in Figure 4.1.

Figure 4.1



R_1 in this figure limits the current through the motor winding. Given a rated current of I and a motor winding with a resistance R_w , Ohm's law sets the maximum supply voltage as $I(R_w + R_1)$. Given that the inductance of the motor winding is L_w , the time constant for the motor winding will be $L_w / (R_w + R_1)$. Figure 4.2 illustrates the effect of increasing the resistance and the operating voltage on the rise and fall times of the current through one winding of a stepping motor.

Figure 4.2



R_2 is shown only in the unipolar example in Figure 4.1 because it is particularly useful there. For a bipolar H-bridge drive, when all switches are turned off, current flows from ground to the motor supply through R_1 , so the current through the motor winding will decay quite quickly. In the unipolar case, R_2 is necessary to equal this performance.

Note: When the switches in the H-bridge circuit shown in Figure 4.1 are opened, the direction of current flow through R_1 will reverse almost instantaneously! If R_1 has any inductance, for example, if it is wire-wound, it must either be bypassed with a capacitor to handle the voltage kick caused by this current reversal, or R_2 must be added to the H-bridge.

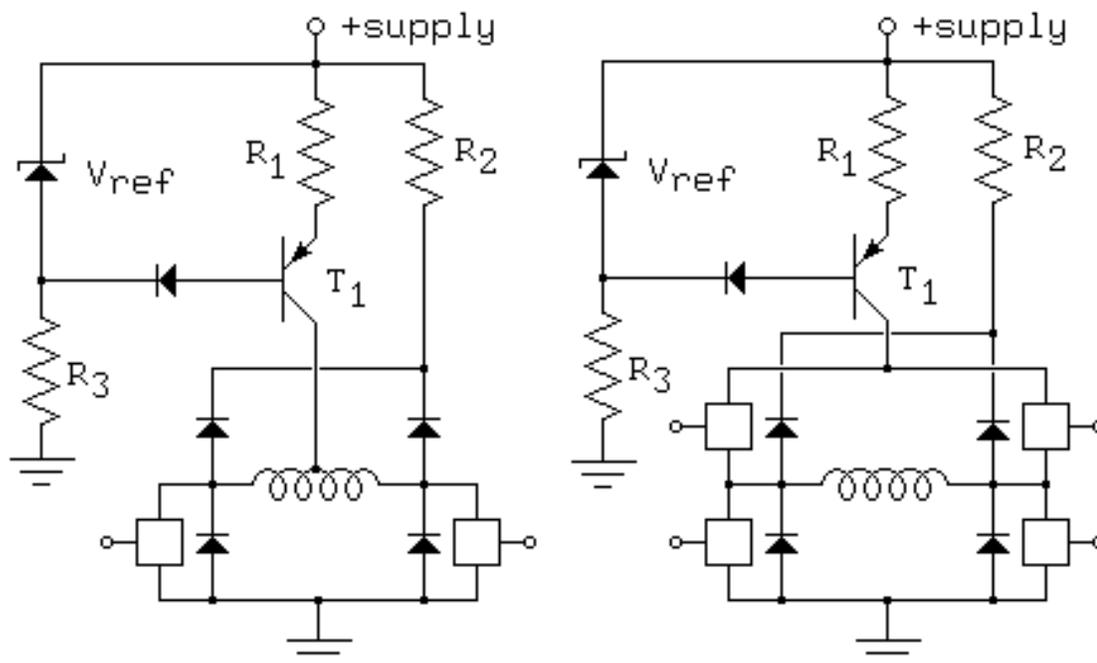
Given the rated maximum current through each winding and the supply voltage, the resistance and wattage of R_1 is easy to compute. R_2 if it is included, poses more interesting problems. The resistance of R_2 depends on the maximum voltage the switches can handle. For example, if the supply voltage is 24 volts, and the switches are rated at 75 volts, the drop across R_2 can be as much as 51 volts without harming the transistors. Given an operating current of 1.5 amps, R_2 can be a 34 ohm resistor. Note that an interesting alternative is to use a zener diode in place of R_2 .

Figuring the peak average power R_2 must dissipate is a wonderful exercise in dynamics; the inductance of the motor windings is frequently undocumented and may vary with the rotor position. The power dissipated in R_2 also depends on the control system. The worst case occurs when the control system chops the power to one winding at a high enough frequency that the current through the motor winding is effectively constant; the maximum power is then a function of the duty cycle of the chopper and the ratios of the resistances in the circuit during the on and off phases of the chopper. Under normal operating conditions, the peak power dissipation will be significantly lower.

Linear Current Limiters

A pair of high wattage power resistors can cost more than a pair of power transistors plus a heat sink, particularly if forced air cooling is available. Furthermore, a transistorized constant current source, as shown in Figure 4.3, will give faster rise times through the motor windings than the current limiting resistor shown in Figure 4.1. This is because a current source will deliver the full supply voltage across the motor winding until the current reaches the rated current; only then will the current source drop the voltage.

Figure 4.3



In Figure 4.3, a transistorized current source (T_1 plus R_1) has been substituted for the current limiting resistor R_1 used in the examples in Figure 4.1. The regulated voltage supplied to the base of T_1 serves to regulate the voltage across the sense resistor R_1 , and this, in turn, maintains a constant current through R_1 so long as any current is allowed to flow through the motor winding.

Typically, R_1 will have as low a resistance as possible, in order to avoid the high cost of a power resistor. For example, if the forward voltage drops across the diode in series with the base T_1 and V_{BE} for T_1 are both 0.65 volts, and if a 3.3 volt zener diode is used for a reference, the voltage across R_1 will be maintained at about 2.0 volts, so if R_1 is 2 ohms, this circuit will limit the current to 1 amp, and R_1 must be able to handle 2 watts.

R_3 in Figure 4.3 must be sized in terms of the current gain of T_1 so that sufficient current flows through R_1 and R_3 to allow T_1 to conduct the full rated motor current.

The transistor T_1 used as a current regulator in Figure 4.3 is run in linear mode, and therefore, it must dissipate quite a bit of power. For example, if the motor windings have a resistance of 5 ohms and a rated current of 1 amp, and a 25 volt power supply is used, T_1 plus R_1 will dissipate, between them, 20 watts!

The circuits discussed in the following sections avoid this waste of power while retaining the performance advantages of the circuit given here.

When an H-bridge bipolar drive is used with a resistive current limiter, as shown in Figure 4.1, the resistor R_2 was not needed because current could flow backwards through R_1 . When a transistorized current limiter is used, current cannot flow backwards through T_1 , so a separate current path back to the positive supply must be provided to handle the decaying current through the motor windings when the switches are opened. R_2 serves this purpose here, but a zener diode may be substituted to provide even faster turn-off.

The performance of a motor run with a current limited power supply is noticeably better than the performance of the same motor run with a resistively limited supply, as illustrated in Figure 4.4:

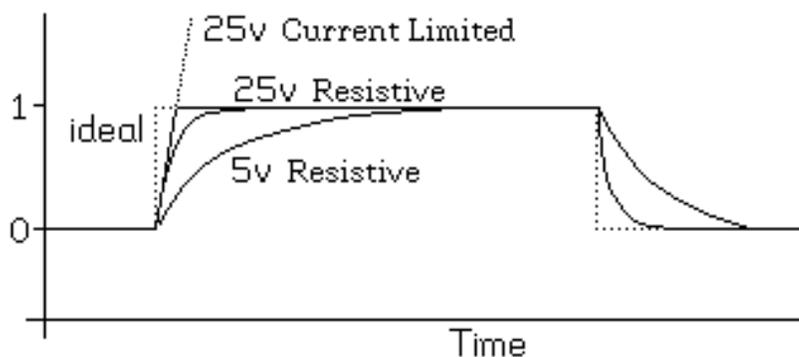


Figure 4.4

With either a current limited supply or a resistive current limiter, the initial rate of increase of the current through the inductive motor winding when the power is turned on depends only on the inductance of the winding and the supply voltage. As the current increases, the voltage drop across a resistive current limiter

will increase, dropping the voltage applied to the motor winding, and therefore, dropping the rate of increase of the current through the winding. As a result, the current will only approach the rated current of the motor winding asymptotically

In contrast, with a pure current limiter, the current through the motor winding will increase almost linearly until the current limiter cuts in, allowing the current to reach the limit value quite quickly. In fact, the current rise is not linear; rather, the current rises asymptotically towards a limit established by the resistance of the motor winding and the resistance of the sense resistor in the current limiter. This maximum is usually well above the rated current for the motor winding.

Open Loop Current Limiters

Both the resistive and the linear transistorized current limiters discussed above automatically limit the current through the motor winding, but at a considerable cost, in terms of wasted heat. There are two schemes that eliminate this expense, although at some risk because of the lack of feedback about the current through the motor.

Use of a Voltage Boost

If you plot the voltage across the motor winding as a function of time, assuming the use of a transistorized current limiter such as is illustrated in Figure 4.3, and assuming a 1 amp 5 ohm motor winding, the result will be something like that illustrated in Figure 4.5:

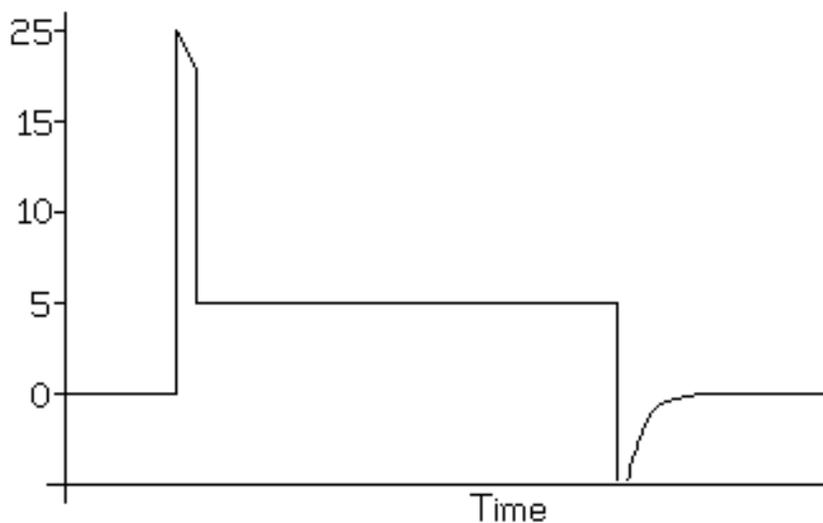


Figure 4.5

As long as the current is below the current limiter's set point, almost the full supply voltage is applied across the motor winding. Once the current reaches the set point, the voltage across the motor winding falls to that needed to sustain the current at the set point, and when the switches open, the voltage reverses briefly as current flows through the diode network and R_2 .

An alternative way to get this voltage profile is to use a dual-voltage power supply, turning on the high voltage for as long as it takes to bring the current in the motor winding up to the rated current, and then turning off the high voltage and turning on the sustaining voltage. Some motor controllers do this directly, without monitoring the current through the motor windings. This provides excellent performance and minimizes power losses in the regulator, but it offers a dangerous temptation.

If the motor does not deliver enough torque, it is tempting to simply lengthen the high-voltage pulse at the time the motor winding is turned on. This will usually provide more torque, although saturation of the magnetic circuits frequently leads to less torque than might be expected, but the cost is high! The risk of burning out the motor is quite real, as is the risk of demagnetizing the motor rotor if it is turned against the imposed field while running hot. Therefore, if a dual-voltage supply is used, the temptation to raise the torque in this way should be avoided!

The problems with dual voltage supplies are particularly serious when the time intervals are under software control, because in this case, it is common for the software to be written by a programmer who is insufficiently aware of the physical and electrical characteristics of the control system.

Use of Pulse Width Modulation

Another alternative approach to controlling the current through the motor winding is to use a simple power supply controlled by *pulse width modulator* (PWM) or by a *chopper*. During the time the current through the motor winding is increasing, the control system leaves the supply attached with a 100% duty cycle. Once the current is up to the full rated current, the control system changes the duty cycle to that required to maintain the current. Figure 4.6 illustrates this scheme:

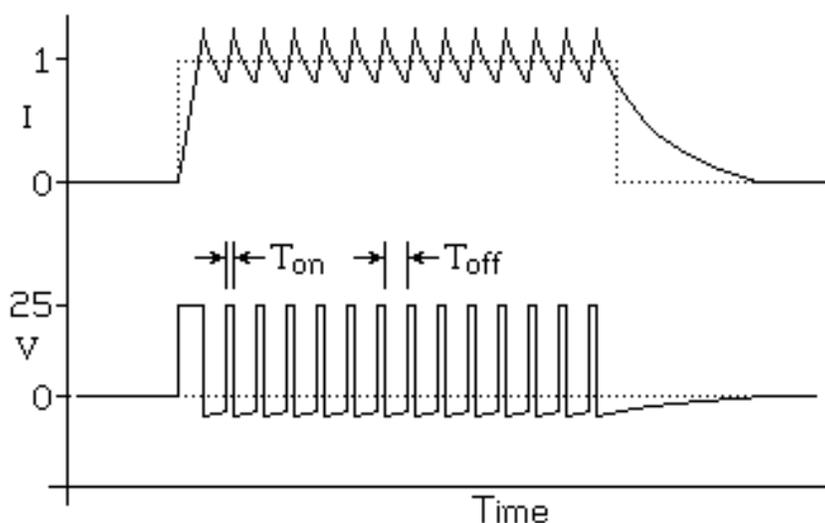


Figure 4.6

For any chopper or pulse width modulator, we can define the duty-cycle D as the fraction of each cycle that the switch is closed:

$$D = T_{on} / (T_{on} + T_{off})$$

Where

T_{on} -- time the switch is closed during each cycle

T_{off} -- time the switch is open during each cycle

The voltage curve shown above indicates the full supply voltage being applied to the motor winding during the on-phase of every chopper cycle, while when the chopper is off, a negative voltage is shown. This is the result of the forward voltage drop in the diodes that are used to shunt the current when the switches turn off,

plus the external resistance used to speed the decay of the current through the motor winding.

For large values of T_{on} or T_{off} , the exponential nature of the rise and fall of the current through the motor winding is significant, but for sufficiently small values, we can approximate these as linear. Assuming that the chopper is working to maintain a current of I and that the amplitude is small, we will approximate the rates of rise and fall in the current in terms of the voltage across the motor winding when the switch is closed and when it is open:

$$V_{on} = V_{supply} - I(R_{winding} + R_{on})$$

$$V_{off} = V_{diode} + I(R_{winding} + R_{off})$$

Here, we lump together all resistances in series with the winding and power supply in the on state as R_{on} , and we lump together all resistances in the current recirculation path when the switch(es) are open as R_{off} . The forward voltage drops of any diodes in the current recirculation path have been lumped as V_{diode} ; if the off-state recirculation path runs from ground to the power supply (H-bridge fast decay mode), the supply voltage must also be included in V_{diode} . Forward voltage drops of any switches in the on-state and off-state paths should also be incorporated into these voltages.

To solve for the duty cycle, we first note that:

$$dI/dt = V/L$$

Where

I -- current through the motor winding

V -- voltage across the winding

L -- inductance of the winding

We then substitute the specific voltages for each phase of operation:

$$I_{ripple} / T_{off} = V_{off} / L$$

$$I_{ripple} / T_{on} = V_{on} / L$$

Where

I_{ripple} -- the peak to peak ripple in the current

Solving for T_{off} and T_{on} and then substituting these into the definition of the duty cycle of the chopper, we get:

$$D = T_{on} / (T_{on} + T_{off}) = V_{off} / (V_{on} + V_{off})$$

If the forward voltage drops in diodes and switches are negligible, and if the only significant resistance is

that of the motor winding itself, this simplifies to:

$$D = I R_{\text{winding}} / V_{\text{supply}} = V_{\text{running}} / V_{\text{supply}}$$

This special case is particularly desirable because it delivers all of the power to the motor winding, with no losses in the regulation system, without regard for the difference between the supply voltage and the running voltage.

The AC ripple I_{ripple} superimposed on the running current by a chopper can be a source of problems; at high frequencies, it can be a source of RF emissions, and at audio frequencies, it can be a source of annoying noise. For example, with audio frequency chopping, most stepper controlled systems will "squeel", sometimes loudly, when the rotor is displaced from the equilibrium position. For small systems, this is usually no more than a minor nuisance, but in systems with large numbers of high power steppers, the ripple currents can induce dangerous AC voltages on nearby signal lines and dangerous currents in nearby ground lines. To find the ripple amplitude, first recall that:

$$I_{\text{ripple}} / T_{\text{off}} = V_{\text{off}} / L$$

Then solve for I_{ripple} :

$$I_{\text{ripple}} = T_{\text{off}} V_{\text{off}} / L$$

Thus, to reduce the ripple amplitude at any particular duty cycle, it is necessary to increase the chopper frequency. This cannot be done without limit because switching losses increase with frequency. Note that this change has no significant effect on AC losses; the decrease in such losses due to decreased amplitude in the ripple is generally offset by the effect of increasing frequency.

The primary problem with use of a simple chopping or pulse-width modulation control scheme is that it is completely open loop. Design of good chopper based control systems requires knowledge of motor characteristics such as inductance that are frequently poorly documented, and as with dual-voltage supplies, when motor performance is marginal, it is very tempting to increase the duty-cycle without attention to the long-term effects of this on the motor. In the designs that follow, this weakness will be addressed by introducing feedback loops into the low level drive system to directly monitor the current and determine the duty cycle.

One-Shot Feedback Current Limiting

The most common approach to automatically adjusting the duty cycle of the switches in the stepper driver involves monitoring the current to the motor windings; when it rises too high, the winding is turned off for a fixed interval. This requires a current sensing system and a one-shot, as illustrated in Figure 4.7:

Figure 4.7

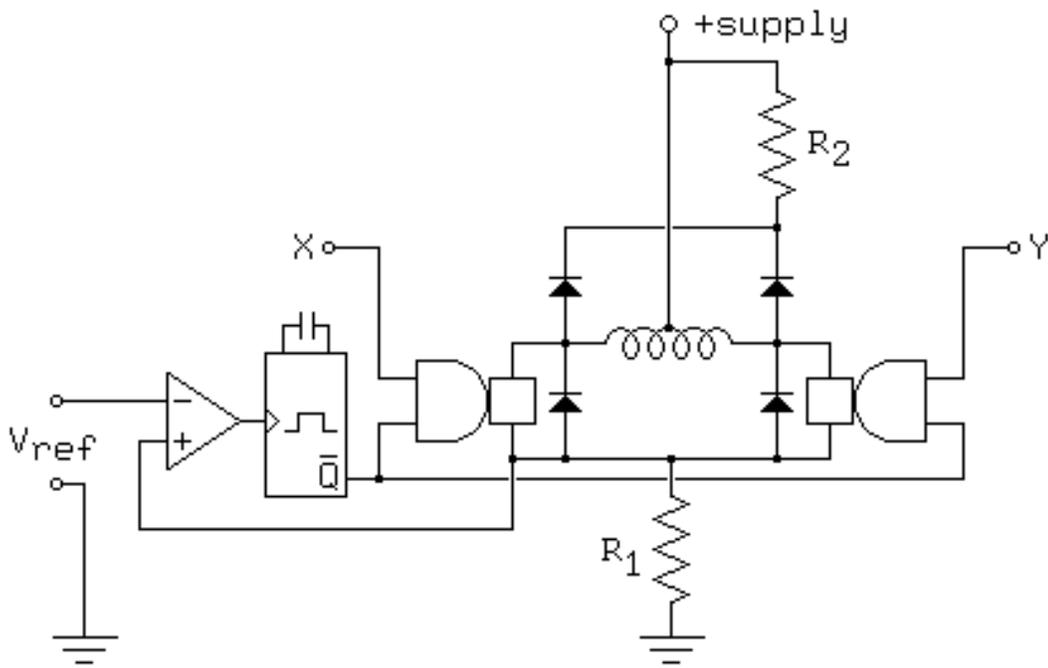


Figure 4.7 illustrates a unipolar drive system. As with the circuit given in Figure 4.3, R_1 should be as small as possible, limited only by the requirement that the sense voltage provided to the comparator must be high enough to be within its operating range. Note that when the one-shot output (\bar{Q}) is low, the voltage across R_1 no longer reflects the current through the motor winding. Therefore, the one-shot must be insensitive to the output of the comparator between the time it fires and the time it resets. Practical circuit designs using this approach involve some complexity to meet this constraint!

Selecting the value of R_2 for the circuit shown in Figure 4.7 poses problems. If R_2 is large, the current through the motor windings will decay quickly when the higher level control system turns off this motor winding, but when the winding is turned on, the current ripple will be large and the power lost in R_2 will be significant. If R_2 is small, this circuit will be very energy efficient but the current through the motor winding will decay only slowly when this winding is turned off, and this will reduce the cutoff speed for the motor.

The peak power dissipated in R_2 will be I^2R_2 during T_{off} and zero during T_{on} ; thus, the average power dissipated in R_2 when the motor winding is on will be:

$$P_2 = I^2R_2 T_{off} / (T_{on} + T_{off})$$

Recall that the duty cycle D is defined as $T_{on} / (T_{on} + T_{off})$ and may be approximated as $V_{running} / V_{supply}$. As a result, we can approximate the power dissipation as:

$$P_2 = I^2R_2 (1 - V_{running} / V_{supply}).$$

Given the usual safety margins used in selecting power resistor wattages, a better approximation is not necessary.

When designing a control system based on pulse width modulation, note that the cutoff time for the one-shot determines T_{off} , and that this is fixed, determined by the timing network attached to the one-shot.

Ideally, this should be set as follows:

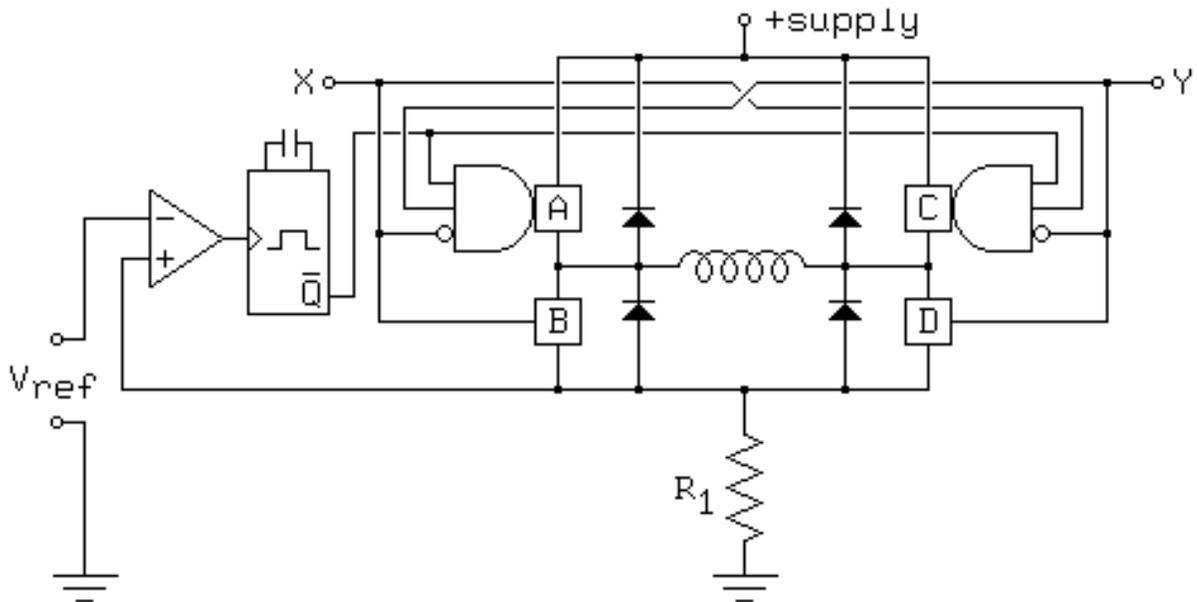
$$T_{\text{off}} = L I_{\text{ripple}} / V_{\text{off}}$$

This presumes that the inductance L of the motor winding is known, that the acceptable magnitude of I_{ripple} is known, and that V_{off} , the total reverse voltage in the current recirculation path, is known and fixed.

Note that this scheme leads to a variable chopping rate. As with the linear current limiters shown in Figure 4.3, the full supply voltage will be applied during the turn-on phase, and the chopping action only begins when the motor winding reaches the current limit set by V_{ref} . This circuit will vary the chopping rate to compensate for changes in the back EMF of the motor winding, for example, those caused by rotor motion; in this regard, it offers the same quality of regulation as the linear current limiter.

The one-shot current regulator shown in Figure 4.7 can also be applied to an H-bridge regulator. The encoded H-bridge shown in Figure 3.13 is an excellent candidate for this application, as shown in Figure 4.8:

Figure 4.8



Unlike the circuit in Figure 4.7, this circuit does not provide design tradeoffs in the selection of the resistance in the current decay path; instead, it offers the same selection of decay paths as was available in the original circuit from Figure 3.13. If the X and Y control inputs are held in a running mode (01 or 10), the current limiter will alternate between that running and slow decay modes, maximizing energy efficiency. When the time comes to turn off the current through the motor winding, the X and Y inputs may be set to 00, using fast decay mode to maximize the cutoff speed, while if the damping effect of dynamic braking is needed to control resonance, X and Y may be set to 11.

Note that the current recirculation path during dynamic braking does not pass through R_1 , and as a result, if the motor generates a large amount of power, burnt out components in the motor or controller are likely.

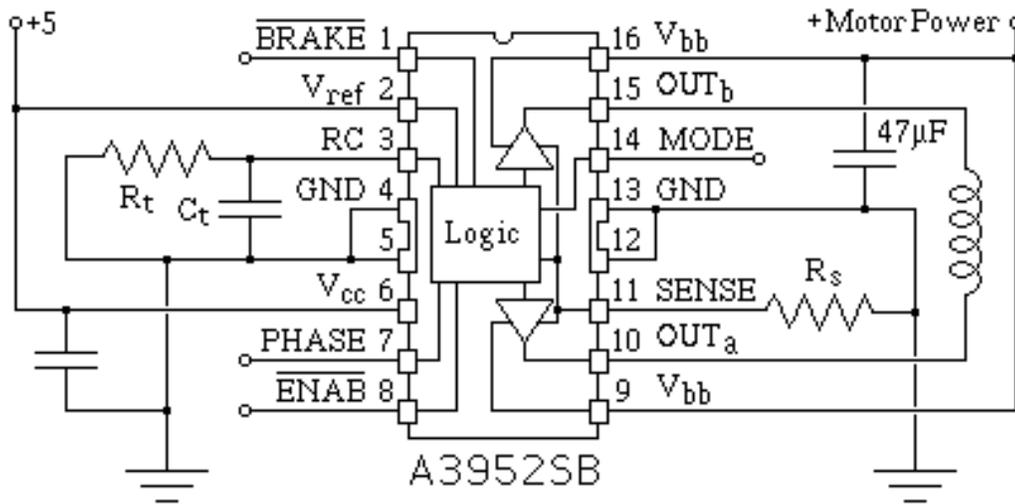
This is unlikely to cause problems with stepping motors, but when dynamic braking is used with DC motors, the current limiter should be arranged to remain engaged while in braking mode!

Practical Examples

[SGS-Thompson](#) (and others) [L293](#) (1A) and [L298](#) (2A) dual H-bridges are designed for easy use with partial feedback current limiters. These chips have enable inputs for each H-bridge that can be directly connected to the output of the one-shot, and they have ground connections for motor-power that are isolated from their logic ground connections; this allows sense resistors to be easily incorporated into the circuit.

The [3952](#) H-bridge from [Allegro Microsystems](#) can handle up to 2-amps at 50 volts and incorporates all of the logic necessary for current control, including comparators and one-shot. This chip is available in many package styles; Figure 4.9 illustrates the DIP configuration wired for a constant current limit:

Figure 4.9



If R_t is 20 Kohm, and C_t is 1000pF, T_{off} for the pulse-width modulation will be fixed at 20 (± 2) microseconds. The 3952 chip incorporates a 10 to 1 voltage divider on the V_{ref} input, so attaching V_{ref} to the 5 volt logic supply sets the actual reference voltage to 0.5 V. Thus, if the sense resistor R_s is 0.5 ohms, this arrangement will attempt to maintain a regulated current through the load of 1 A.

Note that all power switching chips are potentially serious sources of electromagnetic interference! The 47 μ F capacitor shown between the motor power and ground should be as close to the chip as possible, and the path from the SENSE pin through R_s to ground and back to a ground pin of the chip should be very short and with a very low resistance.

On the 5 volt side, because V_{ref} is taken from V_{cc} , a small decoupling capacitor should be placed very close to the chip. It may even be appropriate to isolate the V_{ref} input from V_{cc} with a small series resistor and a separate decoupling capacitor. If this is done, note that the resistance from the V_{ref} pin to ground through the chip's internal voltage divider is around 50 Kohms.

One of the more dismaying features of the 3952 chip, as well as many of its competitors, is the large number of control inputs. These are summarized in the following table:

BRAKE	ENABLE	PHASE	MODE	OUT _a	OUT _b	Notes
0	-	-	0	0	0	Brake
0	-	-	1	0	0	Limited Brake
1	1	-	0	-	-	Standby
1	1	-	1	-	-	Sleep
1	0	0	0	0	1	Reverse, Slow
1	0	0	1	0	1	Reverse, Fast
1	0	1	0	1	0	Forward, Slow
1	0	1	1	1	0	Forward, Fast

In the forward and reverse running modes, the mode input determines whether fast or slow decay modes are used during T_{off} . In the dynamic braking modes, the mode input determines whether the current limiter is enabled. This is of limited value with stepping motors, but use of dynamic braking without a current limiter can be dangerous with DC motors.

In sleep mode, the power consumption of the chip is minimized. From the perspective of the load, sleep and standby modes put the load into fast decay mode (all switches off) but in sleep mode, the chip draws considerably less power, both from the logic supply and the motor supply.

Hysteresis Feedback Current Limiting

In many cases, motor control systems are expected to operate acceptably with a number of different stepping motors. The one-shot based current regulators illustrated in Figures 4.7 to 4.9 have an accuracy that depends on the inductance of the motor windings. Therefore, if fixed accuracy is required, any motor substitution must be balanced by changes to the RC network that determines the off-time of the one-shot.

This section deals with alternative designs that eliminate the need for this tuning. These alternative designs offer fixed precision current regulation over a wide range of load inductances. The key to this approach is arrange the recirculation paths so that the current-sense resistor R_1 is always in the circuit, and then turn the switches on or off depending only on the current.

The usual way to build this type of controller is to use a comparator with a degree of hysteresis, for example, by feeding the output of the comparator back into one of its inputs through a resistor network, as illustrated in Figure 4.10:

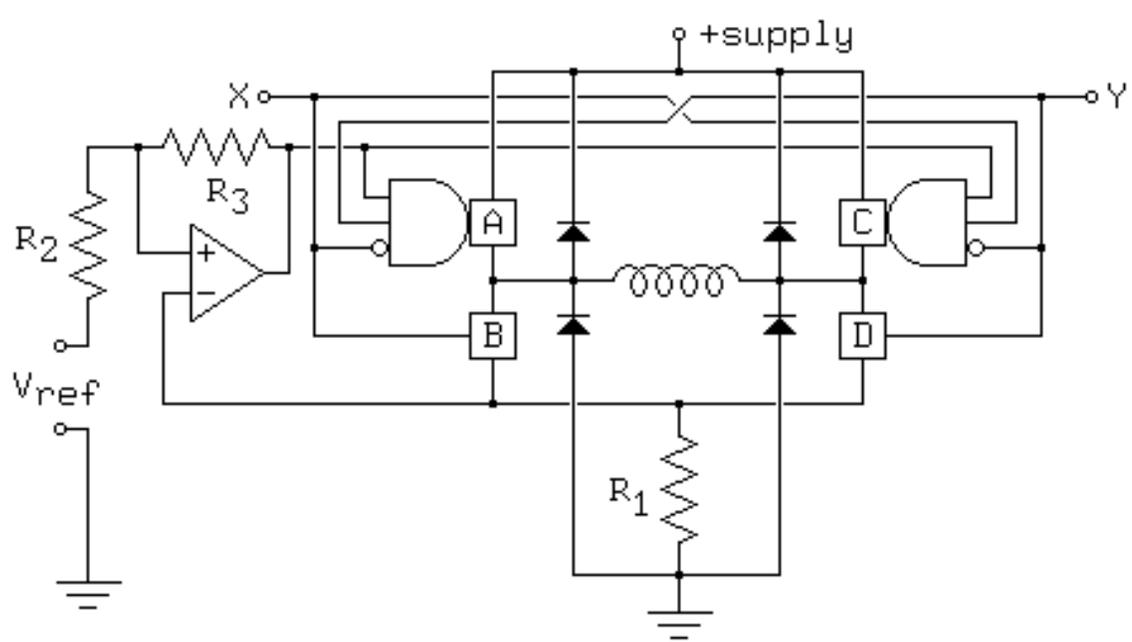


Figure 4.10

To compute the desired values of R_2 and R_3 , we note that:

$$V_{\text{ripple}} \geq V_{\text{hysteresis}}$$

Where:

$$V_{\text{ripple}} = I_{\text{ripple}} R_1$$

I_{ripple} -- the maximum ripple allowed in the current

and:

$$V_{\text{hysteresis}} = V_{\text{swing}} R_2 / (R_2 + R_3)$$

V_{swing} -- the voltage swing at the output of the comparator

We can solve this for the ratio of the resistances:

$$R_2 / (R_2 + R_3) \leq I_{\text{ripple}} R_1 / V_{\text{swing}}$$

For example, if R_1 is 0.5 ohms and we wish to regulate the current to within 10 milliamps, using a comparator with TTL compatible outputs and a voltage swing of 4 volts, the ratio must be no greater than .00125.

Note that the sum $R_2 + R_3$ determines the loading on V_{ref} , assuming that the input resistance of the comparator is effectively infinite. Typically, therefore, this sum is made quite large.

One problem with the circuit given in Figure 4.10 is that it does not limit the current through the motor in dynamic braking or slow decay modes. Even if the current through the sense resistor vastly exceeds the desired current, switches B and D will remain closed in dynamic braking mode, and if the reference voltage

is variable, rapid drops in the reference voltage will not be enforced by this control system.

The designers of the Allegro 3952 chip faced this problem, and passed the solution back to the user, providing a MODE input to determine whether the chopper alternated between running and fast decay mode or running and slow decay mode. Note that this chip uses a fixed off-time set by a one-shot, and therefore, switching between the two decay modes will change the precision of the current regulator. Given that such a change in precision is acceptable, we can modify the circuit from Figure 4.10 to automatically thrown the system into fast-decay mode if the running or dynamic braking current exceeds the set-point of the comparator by too great a margin. Figure 4.11 illustrates how this can be done using a second comparator:

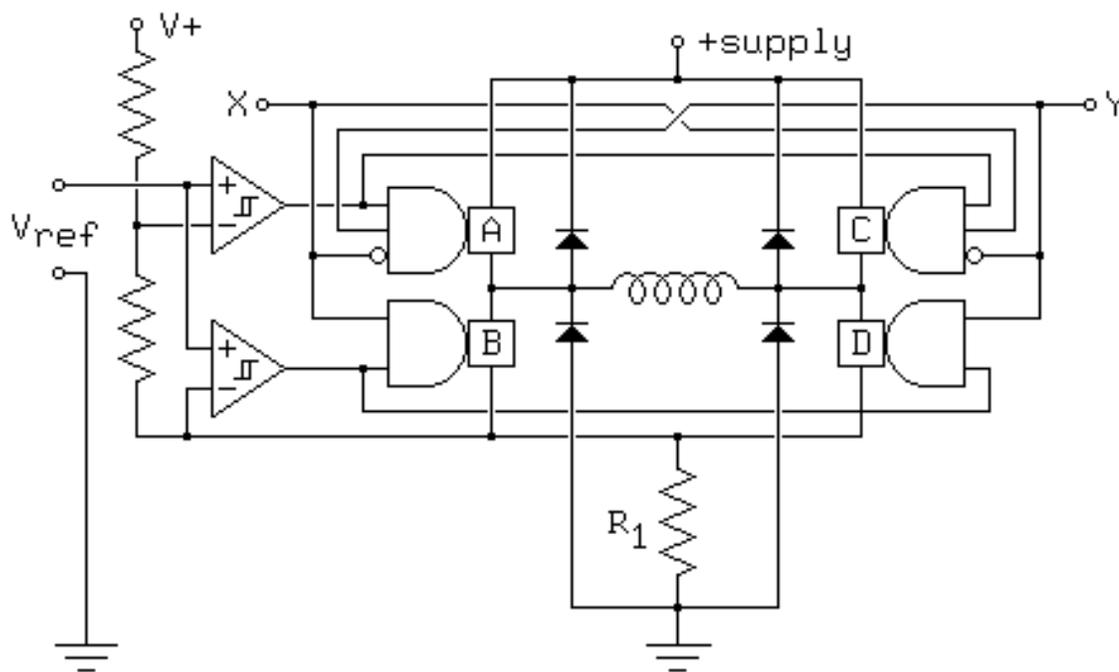


Figure 4.11

As shown in Figure 4.11, the lower comparator directly senses the voltage across R1, while the upper comparator senses a higher voltage, determined by a resistor network. This network should hold the negative inputs of the two comparators just far enough apart to guarantee that, as the voltage across R1 rises, the top comparator will always open the top switches before the bottom comparator opens the bottom switches, and as the voltage across R1 falls, the bottom comparator will always close the bottom switches before the top comparator closes the top switches.

As a result, this system has two basic steady-state running modes. If the motor winding is drawing power, one of the bottom switches will remain closed while the opposite switch on the top is used to chop the power to the motor winding, alternating the state of the system between running and slow-decay mode.

If the motor winding is generating power, the top switches will remain open and the bottom switches will do the chopping, alternating between fast-decay and slow-decay modes as needed to keep the current within limits.

If the two comparators have accuracies on the order of a millivolt with hysteresis on the order of 5 millivolts, it is reasonable to use a 5 millivolt difference between the top and bottom comparator. If we use the 5 volt logic supply as the pull-up supply for the resistor network, and we assume a nominal operating threshold of around 0.5 volts, the resistor network should have a ratio of 1:900; for example, a 90k resistor from +5 and a 100 ohm resistor between the two comparator inputs.

those that deliver only a fraction of the motor current to the sensing resistor, and those that measure the current by sensing the magnetic field around the conductor.

[National Semiconductor](#) had incorporated a very clever current sensor into a number of their H-bridges. This delivers a current to the sense resistor that is proportional to the current through the motor winding, but far lower. For example, on the [LMD18200](#) H-bridge, the sense resistor receives exactly 377 microamps per ampere flowing through the motor winding.

The key to the current sensing technology used in the National Semiconductor line of H-bridges is found in the internal structure of the DMOS power switching transistors they use. These transistors are composed of thousands of small MOSFET transistor cells wired in parallel. A small but representative fraction of these cells, typically 1 in 4000, is used to extract the sense current while the remainder of the cells control the motor current. The data sheet for the National [LMD18245](#) H-bridge contains an excellent writeup on how this is done.

When very high currents are involved, precluding use of an integrated H-bridge, an appealing and well established current sensing technology involves the use of a split ferrite core and a hall effect sensor, as illustrated in Figure 4.13:

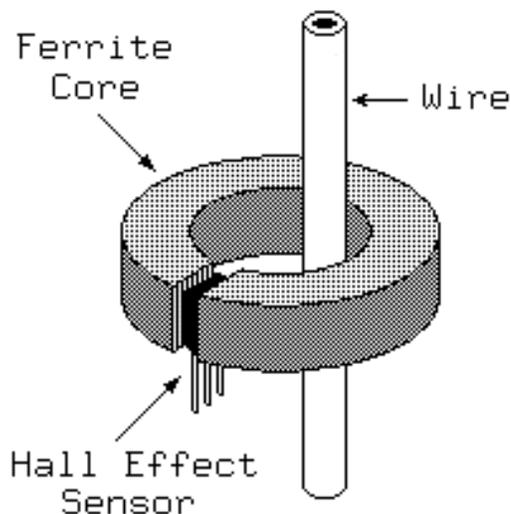


Figure 4.13

Simple linear Hall effect sensors require a small regulated bias current between two of their terminals, and they generate a DC voltage proportional to the magnetic field on a third terminal. The magnetic field across the gap sawed in the ferrite core is proportional to the current through the wire, and therefore, the voltage reported by the Hall effect sensor will be proportional to the current.

[Allegro Microsystems](#) and others make a full lines of Hall effect sensors, but pre-calibrated hall effect current sensors are available; these include the split core, the hall effect sensor, and auxiliary components, all mounted on a small PC board or potted as a unit. Newark Electronics lists a few sources of these, including [Honeywell](#), [F. W. Bell](#) and [LEM Instruments](#).

An intriguing new current sensor is just becoming available, as of 1998, based on a thin-film magnetoresistive sensor; the sensitivity of this technology eliminates the need for the ferrite core and the result is a very compact current sensor. The NT series sensors made by [F. W. Bell](#) use this technology.

5. Microstepping of Stepping Motors

Part of [Stepping Motors](#)

by [Douglas W. Jones](#)

[THE UNIVERSITY OF IOWA Department of Computer Science](#)

Under Development

- [Introduction](#)
 - [Sine-Cosine Microstepping](#)
 - [Limits of Microstepping](#)
 - [Detent Effects](#)
 - [Quantization](#)
 - [Typical Control Circuits](#)
 - [Practical Examples](#)
-

Introduction

Microstepping serves two purposes. First, it allows a stepping motor to stop and hold a position between the full or half-step positions, second, it largely eliminates the jerky character of low speed stepping motor operation and the noise at intermediate speeds, and third, it reduces problems with resonance.

Although some microstepping controllers offer hundreds of intermediate positions between steps, it is worth noting that microstepping does not generally offer great precision, both because of linearity problems and because of the effects of static friction.

Sine Cosine Microstepping

Recall, from the discussion in [Part 2 of this tutorial, on Stepping Motor Physics](#), that for an ideal two-winding variable reluctance or permanent magnet motor the torque versus shaft angle curve is determined by the following formulas:

$$h = (a^2 + b^2)^{0.5}$$

$$x = (S / (\pi/2)) \arctan(b / a)$$

Where:

- a -- torque applied by winding with equilibrium at angle 0.
- b -- torque applied by winding with equilibrium at angle S.
- h -- holding torque of composite.
- x -- equilibrium position.
- S -- step angle.

This formula is quite general, but it offers little in the way of guidance for how to select appropriate values of the current through the two windings of the motor. A common solution is to arrange the torques applied by the two windings so that their sum h has a constant magnitude equal to the single-winding holding torque. This is referred to as sine-cosine microstepping:

$$a = h_1 \sin\left(\left(\frac{\pi}{2}\right)/S\right)\Theta$$

$$b = h_1 \cos\left(\left(\frac{\pi}{2}\right)/S\right)\Theta$$

Where:

h_1 -- single-winding holding torque

$\left(\left(\frac{\pi}{2}\right)/S\right)\Theta$ -- the electrical shaft angle

Given that none of the magnetic circuits are saturated, the torque and the current are linearly related. As a result, to hold the motor rotor to angle Θ , we set the currents through the two windings as:

$$I_a = I_{\max} \sin\left(\left(\frac{\pi}{2}\right)/S\right)\Theta$$

$$I_b = I_{\max} \cos\left(\left(\frac{\pi}{2}\right)/S\right)\Theta$$

Where:

I_a -- current through winding with equilibrium at angle 0.

I_b -- current through winding with equilibrium at angle S.

I_{\max} -- maximum allowed current through any motor winding.

Keep in mind that these formulas apply to two-winding permanent magnet or hybrid stepping motors. Three pole or five pole motors have more complex behavior, and the magnetic fields in variable reluctance motors don't add following the simple rules that apply to the other motor types.

Limits of Microstepping

The utility of microstepping is limited by at least three considerations. First, if there is any static friction in the system, the angular precision achievable with microstepping will be limited. This effect was discussed in more detail in the discussion in [Part 2 of this tutorial, on Stepping Motor Physics](#), in the discussion of friction and the dead zone.

Detent Effects

The second problem involves the non-sinusoidal character of the torque versus shaft-angle curves on real motors. Sometimes, this is attributed to the detent torque on permanent magnet and hybrid motors, but in fact, both detent torque and the shape of the torque versus angle curves are products of poorly understood aspects of motor geometry, specifically, the shapes of the teeth on the rotor and stator. These teeth are almost always rectangular, and I am aware of no detailed study of the impact of different tooth profiles on the shapes of these curves.

Most commercially available microstepping controllers provide a fair approximation of the sine-cosine drive current that would drive an ideal stepping motor to uniformly spaced steps. Ideal motors are rare, and when such a controller is used with a real motor, a plot of the actual motor position as a function of the expected position will generally look something like the plot shown in Figure 5.1.

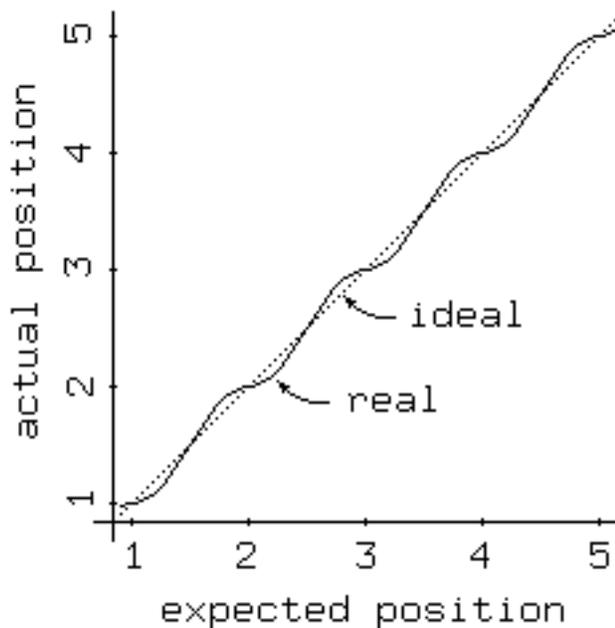


Figure 5.1

Note that the motor is at its expected position at every full step and at every half step, but that there is significant positioning error in the intermediate positions. The curve shown is the curve that would result from a perfect sin-cosine microstepping controller used with a motor that had a torque versus position curve that included a significant 4th harmonic component, usually attributed to the detent torque.

The broad details of detent effects appear to be fairly uniform from motor to motor, so in principle, it ought to be possible to adjust the tables of sines and cosines used in a sine-cosine controller to compensate for the detent effects. In practice, the effects of friction and the errors introduced by quantization combine to limit the value of such an effort.

Quantization

The third problem arises because most applications of microstepping involve digital control systems, and thus, the current through each motor winding is quantized, controlled by a digital to analog converter. Furthermore, if typical PWM current limiting circuitry is used, the current through each motor winding is not held perfectly constant, but rather, oscillates around the current control circuit's set point. As a result, the best a typical microstepping controller can do is approximate the desired currents through each motor winding.

The effect of this quantization is easily seen if the available current through one motor winding is plotted on the X axis and the available current through the other motor winding is plotted on the Y axis. Figure 5.2 shows such a plot for a motor controller offering only 4 uniformly spaced current settings for each motor winding:

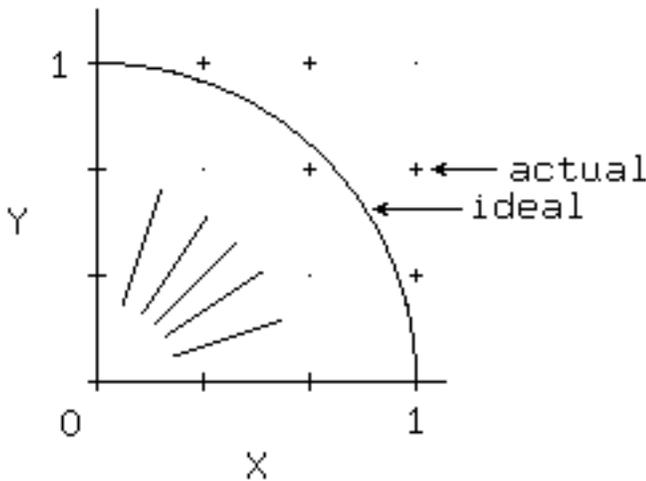


Figure 5.2

Of the 16 available combinations of currents through the motor windings, 6 combinations lead to roughly equally spaced microsteps. There is a clear tradeoff between minimizing the variation in torque and minimizing the error in motor position, and the best available motor positions are hardly uniformly spaced! Use of higher precision digital to analog conversion in the current control system reduces the severity of this problem, but it cannot eliminate it!

Plotting the actual rotor position of a motor using the microstep plan outlined in Figure 5.2 versus the expected position gives the curve shown in Figure 5.3:

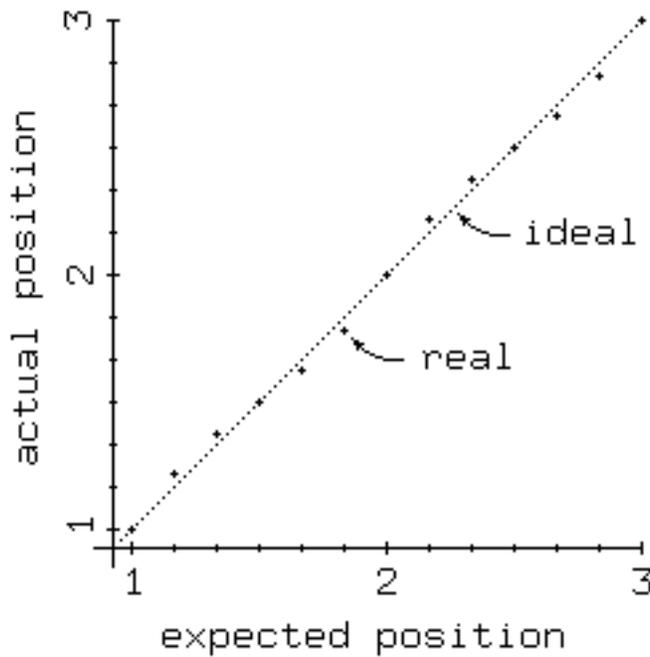


Figure 5.3

It is very common for the initial microsteps taken away from any full step position to be larger than the intended microstep size, and this tends to give the curve a staircase shape, with the downward steps aligned with the full step positions where only one motor winding carries current. The sign of the error at intermediate positions tends to fluctuate, but generally, the position errors are smallest between the full step positions, when both motor windings carry significant current.

Another way of looking at the available microsteps is to plot the equilibrium position on the horizontal axis, in fractions of a full-step, while plotting the torque at each available equilibrium position on the vertical axis. If we assume a 4-bit digital-to-analog converter, giving 16 current levels for each each motor winding, there are 256 equilibrium positions. Of these, 52 offer holding torques within 10% of the desired value, and only 33 are within 5%; these 33 points are shown in bold in Figure 5.4:

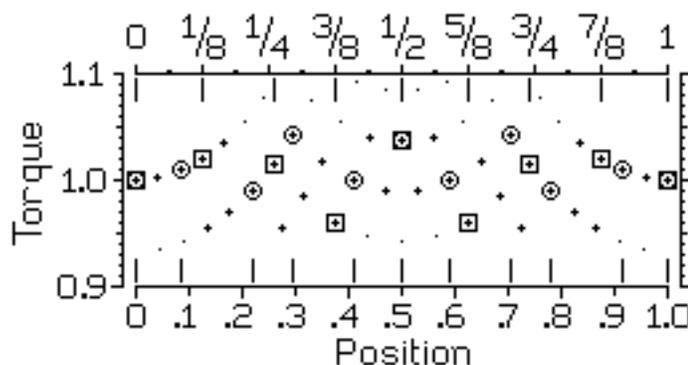


Figure 5.4

If torque variations are to be held within 10%, it is fairly easy to select 8 almost-uniformly spaced microsteps from among those shown in Figure 5.4; these are boxed in the figure. The maximum errors occur at the 1/4 step points; the maximum error is .008 full step or .06 microsteps. This error will be irrelevant if the dead-zone is wider than this.

If 10 microsteps are desired, the situation is worse. The best choices, still holding the maximum torque variation to 10%, gives a maximum position error of .026 full steps or .26 microsteps. Doubling the allowable variation in torque approximately halves the positioning error for the 10 microstep example, but does nothing to improve the 8 microstep example.

One option which some motor control system designers have explored involves the use of nonlinear digital to analog converters. This is an excellent solution for small numbers of microsteps, but building converters with essentially sinusoidal transfer functions is difficult if high precision is desired.

Typical Control Circuits

As typically used, a microstepping controller for one motor winding involves a current limited H-bridge or unipolar drive circuit, where the current is set by a reference voltage. The reference voltage is then determined by an analog-to-digital converter, as shown in Figure 5.5:

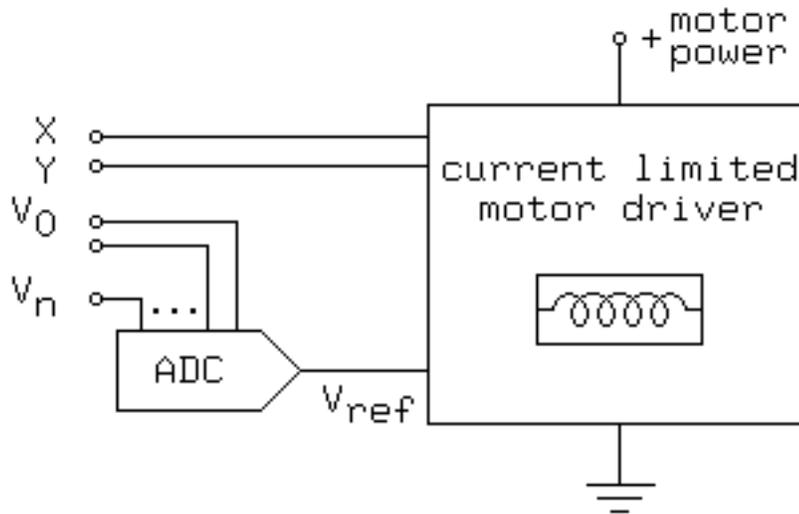


Figure 5.5

Figure 5.5 assumes a current limited motor controller such as is shown in Figures [4.7](#), [4.8](#), [4.10](#) or [4.11](#). For all of these drivers, the state of the X and Y inputs determines the whether the motor winding is on or off and if on, the direction of the current through the winding. The V_0 through V_n inputs determine the reference voltage and this the current through the motor winding.

Practical Examples

There are a fair number of nicely designed integrated circuits combining a current limited H-bridge with a small DAC to allow microstepping control of motors drawing under 2 amps per winding. The [UDN2916B](#) from [Allegro Microsystems](#) is a dual 750mA H-bridge, with a 2-bit DAC to control the current through each. bridge. Another excellent example is the [UC3770](#) from [Unitrode](#).

This chip integrate a 2-bit DAC with a PWM controlled H-bridge, packaged in either 16 pin power-dip format or in surface mountable form. The 3717 a slightly cleaner design, good for 1.2 A, while the 3770 is good for up to between 1.8 A or 2 A, depending on how the chip is cooled.

The [3955](#) from [Allegro Microsystems](#) incorporates a 3-bit non-linear DAC and handles up to 1.5 A; this is available in 16-pin power DIP or SOIC formats. The nonlinear DAC in this chip is specifically designed to minimize step-angle errors and torque variations using 8 microsteps per full-step.

The [LMD18245](#) from [National Semiconductor](#) is a good choice for microstepped control of motors drawing up to 3 amps. This chip incorporates a 4-bit linear DAC, and an external DAC can be used if higher precision is required. As indicated by the data shown in Figure 5.4, a 4-bit linear DAC can produce 8 reasonably uniformly spaced microsteps, so this chip is a good choice for applications that exceed the power levels supported by the Allegro 3955.

Historical Note

It appears that microstepping was invented in 1974 by Larry Durkos, who was working as a mechanical engineer for American Monitor Corporation. The company was a medical equipment vendor, and they were using a large Superior Electric 1.8 degree per step stepping motors to directly drive the 20 inch diameter turntable of their Kinetic Discrete Analyzer. The turntable was used to bring each of 100 blood samples into position for analysis.

That is 2 steps per sample, and the motion was so abrupt that the samples tended to spill. The system was controlled by a Computer Automation LSI 2 minicomputer (today, we would use a microcontroller), and Durkos worked out how to do computer-controlled sine-cosine microstepping in order to solve this problem. The solution was published in the technical service manuals for the KDA analyzer, but it was never patented. Representatives of Superior Electric learned of microstepping from Durkos, and that company was the first to market a microstepping controller.

6. Midlevel Control of Stepping Motors

Part of [Stepping Motors](#)

by [Douglas W. Jones](#)

[THE UNIVERSITY OF IOWA Department of Computer Science](#)

WARNING: This material is new but fairly stable

- [Introduction](#)
- [Hardware Solutions](#)
- [- Practical Examples](#)
- [Software Solutions](#)
- [- Simple Practical Examples](#)
- [- An Object Oriented Design](#)
- [- When Objects Won't Do](#)

Introduction

All of the low-level motor control interfaces described in the previous sections are quite similar, at an abstract level. Each interface has some number of logic inputs. Some of these inputs may be used to directly control which switches are open or closed, others may be encoded, while others may control subsystems such as the analog to digital converter in a microstepping interface.

The states of each of these logic inputs is referred to as the *control vector* for the motor, and a sequence of states used to rotate the motor is referred to as a *control trajectory* for the motor.

For example, the control vector for controlling a permanent magnet or hybrid stepping motor using any of the control circuits shown in Figures [3.4](#), [3.5](#), [3.13](#), [3.14](#), [3.15](#), [4.7](#), [4.8](#), [4.10](#), [4.11](#) or [4.12](#) will contain 4 bits, 2 bits to control each motor winding. In each case, the control vector can be expressed as $\langle X_1 Y_1 X_2 Y_2 \rangle$, where X_1 and Y_1 control the current through motor winding 1 and X_2 and Y_2 control the current through motor winding 2. For any interface with this control vector, the following trajectory will step the motor through one full electrical cycle, using full stepping:

1010
1001
0101
0110
1010

Similarly, the following trajectory will half-step motor through the same electrical cycle:

1010
1000
1001
0001
0101
0100
0110
0010
1010

Other controllers have different control vectors. For example, the control vector for a permanent magnet or hybrid motor controlled by a pair of [Allegro 3952](#) chips (see [Figure 4.9](#)) will be $\langle B_1 E_1 P_1 M_1 B_2 E_2 P_2 M_2 \rangle$, where B, E, P and M are the \neg BRAKE, \neg ENABLE, PHASE and MODE control inputs for each chip. In this case, the following control trajectory will full-step the motor through 1 electrical cycle:

10001000
10001010
10101010
10101000
10001000

The following control trajectory will half-step the same motor through one electrical cycle, using dynamic braking to control resonance whenever a motor winding is turned off:

10001000
10000000
10001010
00001010
10101010
10100000
10101000
00001000
10001000

It is worth noting that, while dynamic braking on disconnected motor windings is an excellent way to control resonance during low speed operation, this will reduce the available torque at higher motor speeds.

The control vectors required for microstepped motors are more complex, but the basic idea remains the same. The problem we face here is to develop higher level control systems that will generate appropriate control trajectories, moving the motor one step, half-step or microstep each time the higher level control

system requires a move.

Hardware Solutions

Early solutions to the problem of generating appropriate control trajectories for stepping motors were almost always based on direct synthesis of the control trajectory in hardware. Such solutions are still appropriate for some applications, but these days, when programmable interface controller chips are commonly used to replace random low-speed logic and when most stepping motor applications are ultimately controlled by computer systems of one kind or another, it is common to use software to generate the control trajectory.

All hardware solutions to generating the control trajectory are subsumed by the general model illustrated in Figure 6.1:

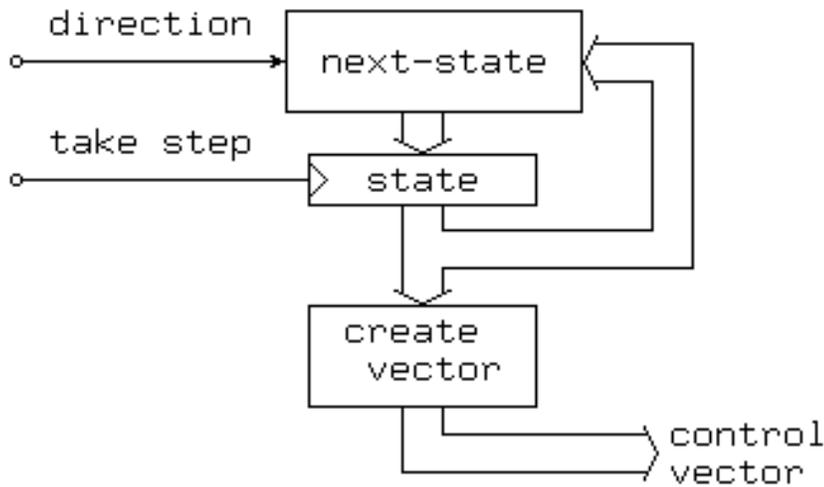


Figure 6.1

A general model for mid-level control

The *next-state* and *create-vector* blocks in Figure 6.1 are combinational logic functions, while the *state* block is a register. Depending on how the state is encoded, the *create-vector* function may be trivial; for certain applications, the *next-state* function is also trivial, but in the general case, *next-state* becomes an up-down counter while *create-vector* becomes a ROM holding the sequence of state vectors needed to form the control trajectories.

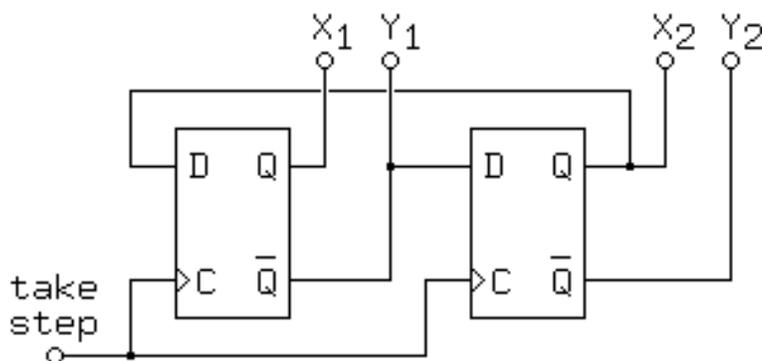
It is worth noting that some stepping motor control systems include additional inputs to the mid-level control system. Common additions include *half-step* and *brake* inputs. Braking control is meaningless in full-step mode but it is of some use in half-step mode. In the latter mode, shorting unused motor windings at low speed is an excellent way to limit resonance while at higher speeds, unused motor windings should be left open for efficiency. Finally, some motor control systems include a *disengage* input that removes power from all windings; in this case, if *brake* is asserted, it typically shorts all motor windings.

Practical Examples

Amateur astronomers frequently need very slow motors to turn their telescopes, and in recent years, stepping motors have taken the place of synchronous motors and gear trains for many applications,

particularly for [barn-door or Scotch mounts](#), a class of extremely simple camera mounts used in astrophotography. For this application, circuits as simple as that shown in Figure 6.2 are common:

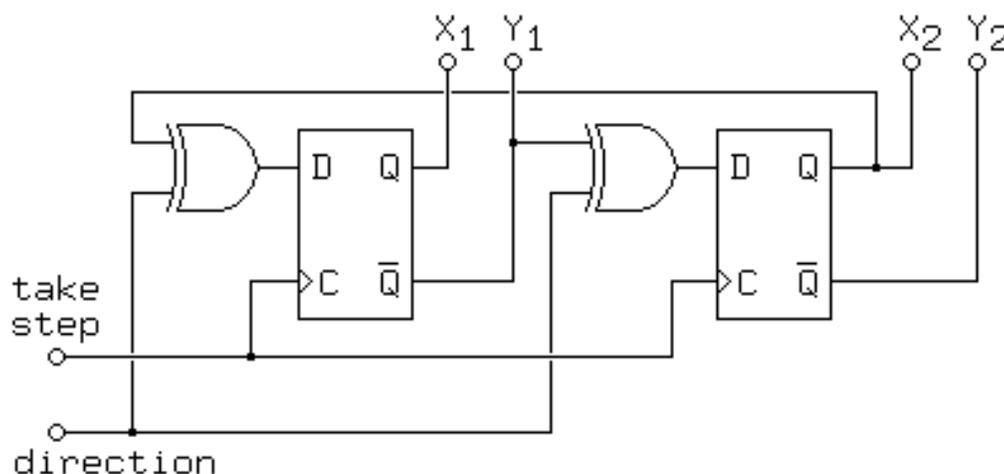
Figure 6.2



The circuit shown in Figure 6.2 only operates in one direction, generating the signals needed to turn a permanent magnet or hybrid motor one full step for each pulse on the take-step input. In terms of the general model in Figure 6.1, the next-state and create-vector functions are trivial and require no logic to generate so long as each flip-flop in the state register has outputs available in both straight and complemented form.

Modifying this circuit for bidirectional operation is straightforward, as is illustrated in Figure 6.3:

Figure 6.3



The circuit shown in Figure 6.3 uses two exclusive or gates to compute two different versions of the next-state function, depending on the value of the direction input. Each flip-flop presents both inverted and non-inverted outputs to the world; this allows [an equivalent circuit](#) to be made by substituting a double-pole double-throw switch for the exclusive-or gates, and [another equivalent circuit](#) can be derived from this by substituting a pair of 2-input 1-output multiplexors for the switch.

A number of manufacturers of stepping motor control circuitry make integrated circuits that include slightly more complex logic allowing both full and half stepping modes. For example, the [Motorola](#) (and others) [MC3479](#) chip includes a 16 volt 350mA H-bridge and control logic with step, direction, and mode inputs to control half-stepping.

The [SGS-Thompson](#) (and others) [L297](#) includes just the mid-level control logic for full or half-step control of a pair of H-bridges used to control a permanent magnet or variable reluctance motor. This chip is

specifically designed to control the [L298](#) dual 2 Amp H-bridge, and in addition to the mid-level control logic, it includes the one-shots and comparators required to use this H-bridge as a current limited chopper.

Software Solutions

If a microprocessor, programmable interface controller or other computer system is used to control a stepping motor, the computer can directly generate the control vector in software and present it to the motor interface through a parallel output port, as shown in Figure 6.4:

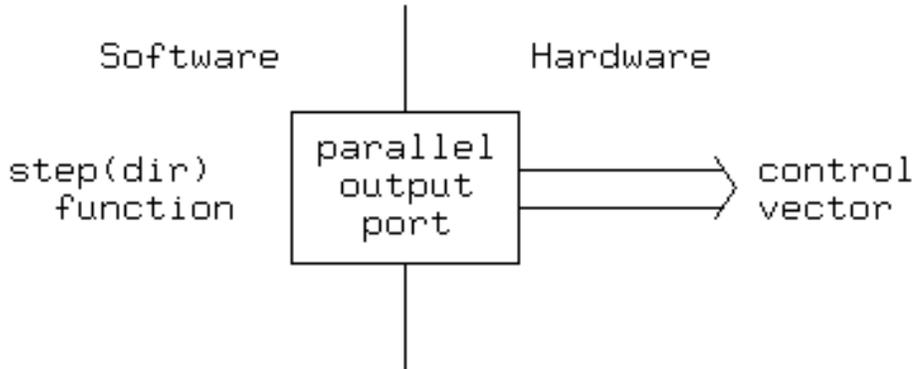


Figure 6.4

A general model for mid-level control

In this software-centered model, the basic problem of mid-level control is reduced to how the *step* function is implemented. Each call to *step(d)* causes the control vector to advance along the control trajectory in the direction specified by *dir*. The call *step(+1)* causes the motor to step forward, while the call *step(-1)* causes it to take one step back.

The control trajectory for any motor can be described as a circular array of control vectors. The simple full-stepped trajectory given in the introduction can be represented as:

t, a 4 element array where

$$t[0] = 10$$

$$t[1] = 9$$

$$t[2] = 5$$

$$t[3] = 6$$

Similarly, the corresponding half-stepped trajectory for the same motor interface would be:

t, an 8 element array where

$$t[0] = 10$$

$$t[1] = 8$$

$$t[2] = 9$$

$$t[3] = 1$$

$$t[4] = 5$$

$$t[5] = 4$$

$$t[6] = 6$$

$$t[7] = 2$$

Similar definitions, differing only in the size of the trajectory array and the values of each entry, will suffice to describe one complete cycle of the control trajectory for any stepping motor interface!

The step routine itself does not depend in any way on the control vector. For all of the above examples, the step routine is:

p , a statically allocated integer variable, initially zero.

step(d)

-- a function of one argument d
-- where d must be either +1 or -1
-- no value is returned

$p = p + d (\text{mod } \text{size}(t))$
output($t[p]$)

Here, we assume that the *output* function sends one control vector to the motor. The details of this function will depend on the computer, the interface used, and the operating system, if any. The above pseudocode also assumes a *size* function that returns the size of the array holding a cycle of the the trajectory; how this is done will definitely vary from one programming language to another.

Note that, when translating the above code into real programming languages, the simple use of the mod operator above can rarely be preserved. Mathematicians generally agree that

$$y > x (\text{mod } y) \geq 0$$

but the designers of programming languages frequently depart from this definition when x is negative. As a result, in languages such as C, C++, Java and Pascal, care must be taken to avoid negative values on the righthand side of the mod (or %) operator!

Simple Practical Examples

The following C code will control a single 3-phase variable reluctance motor taking its control vector from the three least significant bits of the parallel port of a Unix or Linux system, assuming that the parallel port has been opened in the correct mode using the file descriptor `pp`:

```
#define STEPS 3
int t[STEPS] = { 1, 2, 4 };

int p = 0;

void step( int d )
{
    char c;
    p = (p + STEPS + d) % STEPS;

    /* output t[p] using low-level Unix I/O */
```

```

    c = t[p];
    write( pp, &c, 1 );
}

```

Rewriting the first few lines of the above code allows us to convert it for use with a permanent magnet or variable reluctance motor operated in half-step mode from the 4 least significant bits of the parallel port:

```

#define STEPS 8
int t[STEPS] = { 10, 8, 9, 1, 5, 4, 6, 2 };

```

An Object Oriented Design

Things are more complex if multiple motors are in use! Although ad-hoc solutions can be common, a systematic approach is appropriate, and even in languages with no support for object oriented methodology, the easiest way to describe such solutions is in object oriented terms.

After initialization, which may depend on many low level details, the high level software isn't interested in how the motor works. Therefore, for each motor object, the visible interface needs only the *step* function. The class of motor objects is polymorphic because the details of how *step* operates for one motor may differ considerably from the details of how it works for another.

The following code is given in Java; translation to C++, Simula 67 or other object oriented languages should be straightforward:

```

abstract class StepMotor
{
    public abstract void step(int d);
    // step the motor in direction d (+1 or -1)
}

```

This is an abstract class; that is, objects of this class cannot be instantiated because we have yet to specify any of the details of how the motor or interface works. Each useful stepping motor interface must be supported by an extension or subclass of this abstract class! The class below illustrates this, incorporating the code discussed in the sections above:

```

class UniversalStepMotor extends StepMotor
{
    private int s;           // the size of the trajectory
    private int[] t;        // the trajectory for this motor
    private int p;          // motor's position in trajectory
    private OutputStream o; // the output port to use

    public void step(int d)
    // step the motor in direction d (+1 or -1)
    {
        p = (p + s + d) % s;
        o.write( t[p] );
    }
}

```

```

public UniversalStepMotor( int[] table, OutputStream out )
    // initializer
{
    s = table.length;
    t = table;
    p = 0;
    o = out;

    o.write( t[p] );
}
}

```

The above code assumes that the interfaces to motors are accessible through output streams of type `java.io.OutputStream`, and the initializer not only builds the data structure but sends an initial control vector to the motor.

Given that `MotorPort` is an object of type `java.io.OutputStream`, the following Java code will create a `UniversalStepMotor` object `m` for a 3-phase variable reluctance motor:

```

StepMotor m = UniversalStepMotor(
    new int[] {4,2,1}, // step table for the motor
    MotorPort         // OutputStream for the motor
);

```

With this declaration and initialization in place, the call `m.step(1)` will turn the motor one step.

For many applications, the class `StepMotor` defined above will need to be extended with a `reset` procedure, used to reset both the motor object and the motor interface. This procedure should probably be incorporated directly into the definition of the `StepMotor` class, as opposed to adding it by class extension. This procedure would typically be called as the first step in recovering from an error detected at higher levels in the control hierarchy.

When Objects Won't Do

For programmers who can't use object oriented design, for any reason, the following example illustrates an appropriate mid-level design that avoids the use of such features. Using Pascal, the following types can be used to describe each motor:

```

type
    direction = -1 .. +1;

    trajectory = array [0 .. MaxTrajectorySize] of int;

    StepMotor = record
        s: integer;    { size of this motor's trajectory }
        t: trajectory;
        p: integer;    { this motor's position in trajectory }
    end record;

```

```
        o: port;          { the output port to use for this motor }
    end { record };
```

The `StepMotor` record type given here corresponds exactly to an object of the class `UniversalStepMotor` defined in Java above. The array type `trajectory` is explicitly named so that a trajectory may be passed, as a formal parameter, to an initializer procedure for records of this type. The subrange type `direction` allows Pascal's subrange restriction mechanisms to check the correctness of parameters to the step function, a desirable feature that is missing in languages descended from C.

Given the above definitions, we can now construct a general purpose step procedure:

```
procedure step(var m: StepMotor, d: direction);
    { step motor m in direction d }
begin
    with m do begin
        p = (p + s + d) mod s;
        output( o, t[p] );
    }
end { step };
```

In all of the above, we have assumed that variables of the type `port` are used to describe output ports to which motors may be attached, and that the procedure `output` outputs one vector to the indicated port.

In effect, in abandoning programming languages with direct support for object orientation, we have had to change our notation from:

```
motor.step( dir );
```

to:

```
step( motor, dir );
```

This is not a major sacrifice in a system where there is only one type of motor interface, but when there are multiple motor types, support for the object oriented model will be useful. If this must be done in a language like Pascal, the declaration for the type `StepMotor` must be modified to allow for all types of motor interfaces, for example, by declaring it to be a variant record, and the `step` procedure must be modified to check the motor type and act appropriately.

7. High Level Control of Stepping Motors

Part of [Stepping Motors](#)

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[THE UNIVERSITY OF IOWA Department of Computer Science](#)

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Introduction

The key question to be answered by the high-level control system for a stepping motor is, when should the next step be taken? While this almost always depends on the application, the similarities between different applications are sufficient to justify the development of fairly complex general purpose stepping motor controllers.

Stepping motor control may be based on *open loop* or *closed loop* models. We are primarily interested in open loop models, because this is where stepping motors excel, but we will treat closed loop models briefly because they are somewhat simpler. Figure 7.1 illustrates an extreme example:

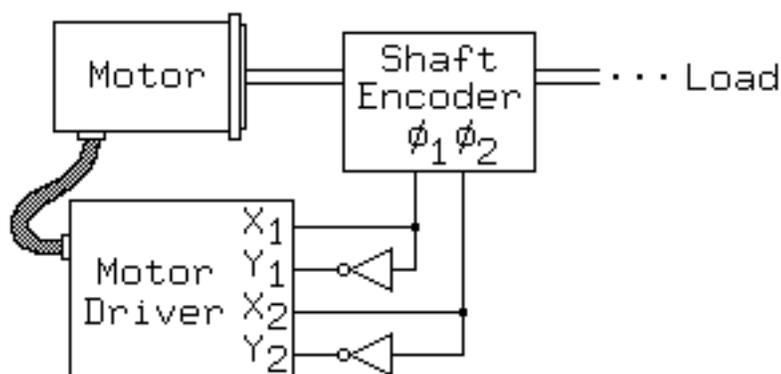


Figure 7.1

In Figure 7.1, a quadrature shaft encoder is attached to the drive shaft of a permanent magnet or hybrid stepping motor, and the two phase output of this encoder is used to directly generate the control vector for the motor driver. Rotary shaft encoders are typically rated in output pulses per channel per revolution; for this example to be useful, for a motor with n steps per revolution, the shaft encoder output must give $n/2$ pulses per channel per revolution. If this is the case, the behavior of this system will depend on how the shaft encoder is rotated around the motor shaft relative to the motor.

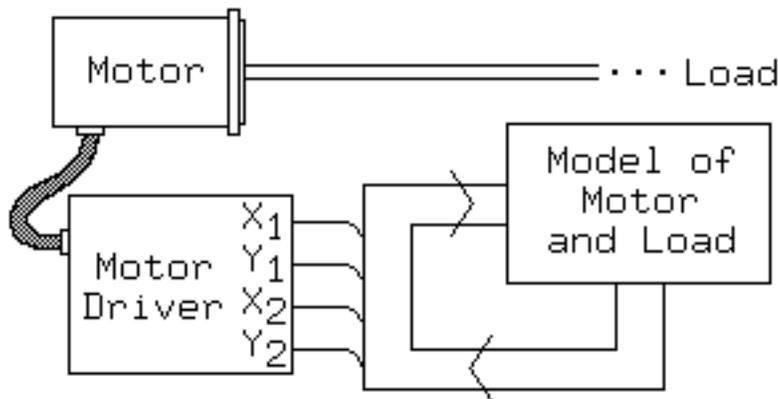
If the shaft encoder is rotated into a position where the output of the shaft encoder translates to a control vector that holds the motor shaft in its initial position, the motor shaft will not rotate of itself, and if the motor shaft is rotated by force, it will stay wherever it is left. We will refer to this position of the shaft encoder relative to the motor as the neutral position.

If the shaft encoder is rotated one step clockwise (or counterclockwise) from the neutral position, the control vector output by the shaft encoder will pull the rotor clockwise (or counterclockwise). As the rotor turns, the shaft encoder will change the control vector so that the rotor is always trying to maintain a position one step clockwise (or counterclockwise) from where it is at the moment. The torque produced by this method will fall off with rotor speed, but this control system will always produce the maximum torque the motor is able to deliver at any speed.

In effect, with this one-step displacement, we have constructed a brushless DC motor from a stepping motor and a collection of off-the-shelf parts. In practice, this is rarely done, but there are numerous applications of stepping motors in closed-loop control systems that are based on this model, usually with a microprocessor included in the feedback loop between the shaft encoder and the motor controller.

In an open-loop control system, this feedback loop is broken, but at a high level, the basic principle remains quite similar, as illustrated in Figure 7.2:

Figure 7.2



In Figure 7.2, we replace the shaft encoder from Figure 7.1 with a simulation model of the response of the motor and load to the control vector. At any instant, the actual position of the rotor is unknown! Nonetheless, we can use the simulation model to predict, based on an assumed rotor position and velocity, how the motor will respond to the control vector, and we can construct this model so that its output is the control vector generated by a simulated shaft encoder.

So long as the model is sufficiently accurate, the behavior of the motor controlled by this model will be the same as the behavior of the motor controlled by a closed loop system!

Model Variables

In the example given in Figure 7.1, the only control variable offered is the angle of the shaft encoder relative to the motor. In effect, this controls the extent to which the equilibrium point of the motor's torque versus shaft angle curve leads or follows the current rotor position. In theory, any desired motor behavior can be elicited by adjusting this angle, but it is far more convenient to speak in terms of other variables:

Θ -- The predicted shaft position (radians)

Θ_{target} -- The target shaft position determined by the application

$V = d\Theta/dt$ -- The predicted velocity (radians per second)

V_{target} -- The target velocity determined by the application

$A = dV/dt$ -- The predicted acceleration (radians per second squared)

A_{target} -- The target acceleration, may be determined by the application

As in the section on [Stepping Motor Physics](#), we will define the basic motor characteristics:

S -- step or microstep angle, in radians

μ -- moment of inertia of rotor and load

h -- the holding torque of the motor

Note that here, the step angle S is not the physical step angle of the motor, but rather, the step angle offered by the mid-level motor interface; this may be a full step, a half step, or a microstep of some size!

Models

The simplest model that will do the job is almost always the best. For some applications, this means the model is so simple that it is hard to identify it as a model! For example, consider the case where the application demands a constant motor velocity:

$$A_{\text{target}} = 0$$

$$V_{\text{target}} = \text{Constant}$$

In this case,

$$t_{\text{step}} = S / V_{\text{target}}$$

where

t_{step} -- time per step

This barely looks like a model; in part, this is because we have omitted the statement that, every t_{step} seconds, we advance the control vector one step:

repeat the following cycle forever:

wait t_{step} seconds and then

$$\Theta = \Theta + S \\ \text{step}(1)$$

A more interesting model is required if we want to maintain a constant acceleration. Obviously, we can't do this forever, but we'll use this model as a component in more complex models that require changes of velocity or position. In this case,

$$A_{\text{target}} = \text{Constant}$$

where

$$A_{\text{target}} < h / \mu$$

In developing a model, we begin with the observation that, for constant acceleration A and assuming a standing start at time 0,

$$\Theta = 1/2 A t^2$$

More generally, if the motor starts at position Θ and velocity V , after time t the new position Θ' and velocity V' will be:

$$\Theta' = 1/2 A t^2 + V t + \Theta \\ V' = A t + V$$

Setting $\Theta=0$ and $\Theta'=S$, we solve first for t , the time taken to move one step, as a function of V and A :

$$1/2 A t^2 + V t - S = 0 \\ t = (-V \pm (V^2 + 2 A S)^{0.5}) / A$$

Here, we have applied the quadratic formula, and for our situation, this gives two real roots! The additive root is the root we are concerned with; for this, we can use the resulting time to compute the velocity at the end of one step:

$$t_{\text{step}} = (-V + (V^2 + 2 A S)^{0.5}) / A$$

$$V' = (V^2 + 2 A S)^{0.5}$$

We can combine this model for acceleration with the model for constant speed running to make a motor controller that will seek V_{target} , assuming that an outside agent may change V_{target} at any time:

repeat the following cycle forever:

if $V = V_{\text{target}}$ do the following:

wait S/V_{target} seconds and then

$$\Theta = \Theta + S$$

step(1)

otherwise, if $V < V_{\text{target}}$, accelerate as follows:

wait $(-V + (V^2 + 2 A_{\text{accel}}S)^{0.5}) / A_{\text{accel}}$ seconds and then

$$\Theta = \Theta + S$$

$$V = (V^2 + 2 A_{\text{accel}}S)^{0.5}$$

step(1)

otherwise, $V > V_{\text{target}}$, decelerate as follows:

wait $(-V + (V^2 + 2 A_{\text{decel}}S)^{0.5}) / A_{\text{decel}}$ seconds and then

$$\Theta = \Theta + S$$

$$V = (V^2 + 2 A_{\text{decel}}S)^{0.5}$$

step(1)

This control system is not fully satisfactory for a number of reasons! First, it only allows the motor to operate in one direction and it fails utterly when V reaches zero; at that point, if a divide by zero operation is allowed to produce an infinite result, the program will wait infinitely and never again respond to change in the control input.

The second shortcoming of this program is simpler to correct: As written, there is an infinitesimal probability of the motor speed reaching the desired speed and staying there with V_{target} equal to V . Far more likely, what will happen is that V will oscillate around V_{target} , taking alternate accelerating and decelerating steps and never settling down at the desired running speed.

A quick and dirty solution to this latter problem is to add code to recognize when V passes V_{target} during acceleration or deceleration; when this occurs, V can be set to V_{target} . Formally, this is incorrect, but if the acceleration and deceleration rates are not too high and if there is sufficient damping in the system, this will work quite well.

In a frictionless system using sine-cosine microstepping at speeds below the cutoff speed for the motor, the available torque is effectively constant and we can use the full torque to accelerate or decelerate the motor, so the above control algorithm will work with

$$A_{\text{accel}} = A_{\text{decel}} = h / \mu$$

If there is significant static friction, we can take this into account as follows:

$$A_{\text{accel}} = (h - f) / \mu$$

$$A_{\text{decel}} = (h + f) / \mu$$

where

f -- frictional torque

If the motor is run using the maximum available acceleration and deceleration, any unexpected increase in the load will cause the motor rotor to fall behind its predicted position, and the result will be a failure of the control system. As a result, open-loop stepping motor control systems are never run at the accelerations given above! In the case of full or half-stepping, where there is no sine-cosine torque compensation, the available torque varies over a range of a factor of $2^{0.5}$, so we typically adjust the accelerations given above by this amount:

$$A_{\text{accel}} = ((h / 1.414) - f) / \mu$$

$$A_{\text{decel}} = ((h / 1.414) + f) / \mu$$

If we operate consistently near the edge of the performance envelope, and if we never request a velocity V_{target} near the resonant speed of the motor, we can safely accelerate through resonances without relying on damping. If, on the other hand, we select acceleration values that are significantly below the maximum that is possible, electrical or mechanical damping may be needed to avoid problems with resonance.

Note that it is not difficult to extend the above control model to account, at least approximately, for viscous friction and for the dropoff of torque as a function of speed. To do this, we merely modify the above formulas for A_{accel} and A_{decel} so that h and f are functions of V . Thus, instead of treating these as constants of the control algorithm, we must recompute the available acceleration at each step.

If our goal is to turn the motor smoothly from one set position to another, we must first accelerate it, then perhaps coast at fixed speed for a while, then decelerate. The decision governing when to begin decelerating rests on a knowledge of the stopping distance from any particular velocity. Assuming that the available acceleration is constant over the relevant range of speeds, we can compute this from:

$$V = A_{\text{decel}} t$$

$$\Theta = 1/2 A_{\text{decel}} t^2$$

First we solve for the stopping time,

$$t = V / A_{\text{decel}}$$

and then we solve for the stopping angle

$$\Theta = 1/2 A_{\text{decel}} (V / A_{\text{decel}})^2 = V^2 / (2 A_{\text{decel}})$$

Given this, we can outline a procedure for moving the motor from its current estimated position to a step just beyond some target position:

moveto(Θ_{target})

-- a function of one argument Θ

-- no value is returned

while $V < V_{\text{target}}$

and while $\Theta < \Theta_{\text{target}} - V^2 / (2 A_{\text{decel}})$ repeat the following to accelerate

step(1)

wait $(-V + (V^2 + 2 A_{\text{accel}} S)^{0.5}) / A_{\text{accel}}$ seconds and then

$$\Theta = \Theta + S$$

$$V = (V^2 + 2 A_{\text{accel}} S)^{0.5}$$

$$V = V_{\text{target}}$$

while $\Theta < \Theta_{\text{target}} - V^2 / (2 A_{\text{decel}})$ repeat the following to coast

step(1)

wait S/V_{target} seconds and then

$$\Theta = \Theta + S$$

while $\Theta < \Theta_{\text{target}}$ repeat the following to decelerate

step(1)

wait $(-V + (V^2 + 2 A_{\text{decel}} S)^{0.5}) / A_{\text{decel}}$ seconds and then

$$\Theta = \Theta + S$$

$$V = (V^2 + 2 A_{\text{decel}} S)^{0.5}$$

$$V = 0$$

done, Θ and Θ_{target} are within a step of each other!

The control model only moves the motor one direction, it fails to plan in terms of the quantization of available stopping positions, and it doesn't account for the cyclic nature of Θ . Nonetheless, it is a useful illustration. Note that we have used V_{target} as a limiting velocity in this code, but that this will only be relevant during long moves; for short moves, the motor will never reach this speed.

With the above, code, so long as the acceleration and deceleration rates are high enough to avoid dwelling for too long at resonant speeds, and so long as V_{target} is not too close to a resonant speed, a plot of rotor position versus time will show fairly clean moves, as illustrated in Figure 7.3:

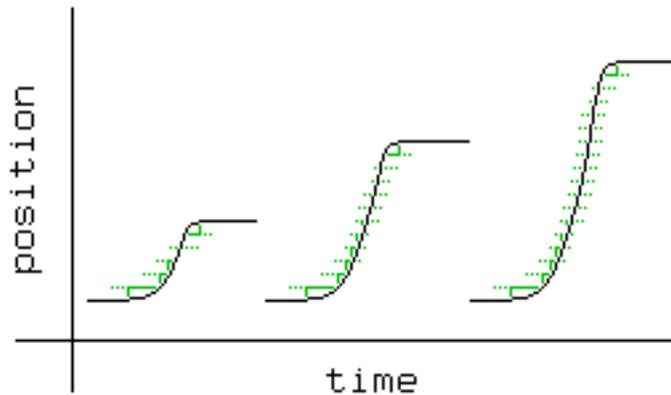


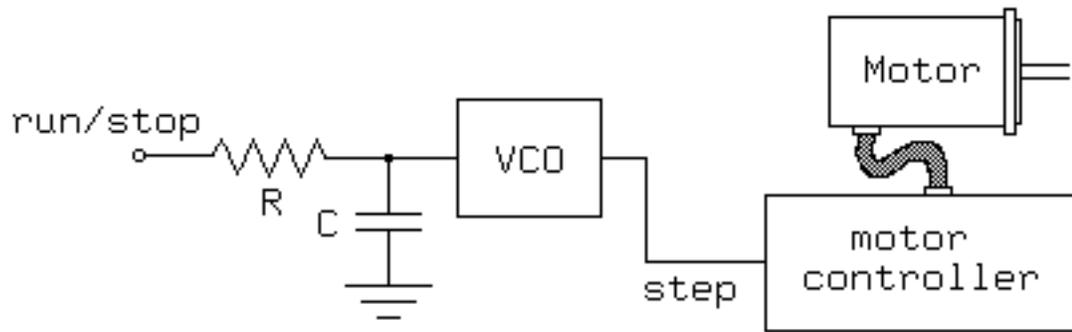
Figure 7.3

If the motor is to be accelerated at the maximum possible rate, the control model used above is not sufficient. In that case, during acceleration, the equilibrium position must be maintained between 0.5 and 1.5 steps ahead of the rotor position as the rotor moves, and during deceleration, the equilibrium position must be maintained the same distance behind the rotor position. This requires careful logic at the turnaround point, when the change is made from accelerating to decelerating modes. The above control model omits any such considerations, but it is adequate at accelerations sufficiently below the maximum available!

Hardware Solutions

Today, it is rare to find high-level stepping motor control done purely in hardware, and when it is done, it is usually only in the very simplest of applications. For example, consider the problem of starting and stopping a stepping motor under load. Direct generation of the quadratic functions necessary to achieve smooth acceleration is quite difficult in hardware, but it is easy to generate exponentials that are adequate approximations of these. The circuit outlined in Figure 7.4 illustrates how this can be done:

Figure 7.4



Here, the resistor R and capacitor C form a low pass filter on the control input of the voltage controlled oscillator VCO. When the input level is at run, the VCO output oscillates at its maximum rate. When the input level is at stop, the VCO output ceases to oscillate. The RC time constant of the low pass filter determines the rate of acceleration applied to the motor.

With such a design, the time constant RC is usually determined empirically by setting up the system and then adjusting R and C until the system operates properly.

Practical Examples

The NE555 timer can be used as a voltage controlled oscillator, but I first saw this done with discrete components on a controller for a paper-tape reader designed around 1970.

Software Solutions

The basic control models outlined at the start of this section can be directly incorporated into the software for controlling a stepping motor, and this must be done if, for example, the motor is driving a load with a variable moment of inertia or driving a load against variable frictional loadings. Most open-loop stepping motor applications are not that complicated, however! So long as the inertia and frictional loadings are constant, the control software can be greatly simplified, replacing complex model computations with a table of precomputed delays.

Consider the problem of accelerating the motor from a standing start. No matter where the motor starts, so long as the torque, moment of inertia and frictional loadings remain the same, the time sequence of steps will be the same. Therefore, we need only pre-compute this time sequence of steps and save it in an array. We can use this array as follows to accelerate the motor:

array AV , the acceleration vector, holds time intervals

i is the index into AV

$i = 0$

repeat the following cycle to accelerate forever:

wait $AV[i]$ seconds and then

```
step( 1 )  
i = i + 1
```

We may use i , the counter in the above code, as a stand-in for the motor velocity, since stepping the motor every $AV[i]$ seconds will move the motor at a speed of $S/AV[i]$.

It is a straightforward exercise in elementary physics to compute the entries in the array A . If the motor is accelerating at A_{target} ,

$$\Theta_i = 1/2 A_{\text{target}} t_i^2$$

where

Θ_i -- the shaft angle at each successive step

Solving for time as a function of position, we get:

$$t_i = (2\Theta_i / A_{\text{target}})^{0.5}$$

If we define

$$\Theta_0 = 0$$

so that

$$\Theta_i = Si$$

and

$$t_0 = 0$$

we can conclude that

$$t_i = k i^{0.5}$$

where

$$k = (2S/A_{\text{target}})^{0.5}$$

The acceleration vector entries are then:

$$A[0] = (2S/A_{\text{target}})^{0.5}$$

and

$$A[i] = (i^{0.5} - (i - 1)^{0.5})A[0]$$

The following table gives the ratios of the first 20 entries in A[i] to A[0]:

0	1.000	10	0.154
1	0.414	11	0.147
2	0.318	12	0.141
3	0.268	13	0.136
4	0.236	14	0.131
5	0.213	15	0.127
6	0.196	16	0.123
7	0.183	17	0.120
8	0.172	18	0.116
9	0.162	19	0.113

In general, we aren't interested in indefinite acceleration, but rather, we are interested in accelerating until some speed or position restriction is satisfied, and then the control system should change, for example, from acceleration to deceleration or constant speed operation. So long as friction can be ignored, so the same rates can be used for acceleration and deceleration, we can make a clean move to a target position as follows:

array AV is the acceleration vector
i is the index into AV

i = 0

$$D = \theta_{\text{target}} - \theta$$

while D nonzero do the following

if D > 0 -- spin one way

 step(1)

 D = D - 1

else -- spin the other way

 step(-1)

 D = D + 1

endif

wait AV[i] seconds and then

if (i < |D|) and (S/AV[i] < V_{target}) -- accelerate

 i = i + 1

else if i > |D| -- decelerate

```
        i = i - 1
    endif
endloop
```

Given an appropriate acceleration vector, the above code will cleanly accelerate a motor up to a speed near the target velocity, hold that speed, and then decelerate cleanly to a stop at the target position.

The above code does not take advantage of the higher rates of deceleration allowed when there is friction. In general, this should not cause any problems, but if the fastest possible moves are desired, a separate deceleration table should be maintained. Here is one idea:

```
array AV holds acceleration intervals
array C holds coasting intervals
array T holds transition information
array D holds deceleration intervals
```

```
i = 0
repeat the following until the desired speed is reached
```

```
    wait A[i] seconds and then
    step( 1 )
    i = i + 1
```

```
repeat the following to maintain the speed
```

```
    wait C[i] seconds and then
    step( 1 )
```

```
repeat the following to maintain the speed
```

```
i = i - T[i]
repeat the following until i = 0
i = i - 1
step( 1 )
```

In the above, the arrays A and D are constructed identically, except that one has intervals used for acceleration, at a rate limited by friction, while the other has intervals used for deceleration, at a rate assisted by friction. Note that, after accelerating for i steps from a standing start, the motor will reach a velocity from which it can decelerate to a halt in $i - T[i]$ steps. This relationship determines the values pre-computed in the array T.

Simple Practical Examples

An Object Oriented Design

Stepping Motor Control Software

Old Part 5 of [Stepping Motors](#) by [Douglas W. Jones](#)

Here's the code to make your motor run as if you had one of those fancy stepper controllers. I've used Pascal for no particular reason. This code assumes only one motor, and it assumes it's attached to the least significant bits of one parallel output port. In practice, it's nice to have one parallel output port per motor, although with a bit of care, you can use the high bits of a port for another motor or other applications, and you can multiplex one port to handle multiple motors. (The July 1993 issue of Model Railroader has plans for a parallel port multiplexer circuit for IBM PC systems in it).

Assume these declarations and values for a three winding variable reluctance motor:

```
const maxstep = 2;
      steps = 3;
var   steptab: array [0..maxstep] of integer;
      step: integer;
      motor: file of integer; { this is the I/O port for the motor }
begin
    step := 0;
    steptab[0] = 1; { binary 001 }
    steptab[1] = 2; { binary 010 }
    steptab[2] = 4; { binary 100 }
    write( motor, steptab[step] );
```

Assume these declarations and values for a permanent magnet motor, either unipolar, with center tapped windings, or bipolar, with H-bridge drive circuits:

```
const maxstep = 3;
      steps = 4;
var   steptab: array [0..maxstep] of integer;
      step: integer;
      motor: file of integer; { this is the I/O port for the motor }
begin
    step := 0;
    steptab[0] = 1; { binary 0001 }
    steptab[1] = 4; { binary 0100 }
    steptab[2] = 2; { binary 0010 }
    steptab[3] = 8; { binary 1000 }
    write( motor, steptab[step] );
```

Assume these declarations and values for half-step control of a permanent magnet motor:

```
const maxstep = 7;
```

```

steps = 8;
var  steptab: array [0..maxstep] of integer;
     step: integer;
     motor: file of integer; { this is the I/O port for the motor }
begin
  step := 0;
  steptab[0] = 1;  { binary 0001 }
  steptab[1] = 5;  { binary 0101 }
  steptab[2] = 4;  { binary 0100 }
  steptab[3] = 6;  { binary 0110 }
  steptab[4] = 2;  { binary 0010 }
  steptab[5] = 10; { binary 1010 }
  steptab[6] = 8;  { binary 1000 }
  steptab[7] = 9;  { binary 1001 }
  write( motor, steptab[step] );

```

Assume these declarations and values for control of a 5-phase motor, with an H-bridge on each of the 5 leads to the motor:

```

const maxstep = 9;
     steps = 10;
var  steptab: array [0..maxstep] of integer;
     step: integer;
     motor: file of integer; { this is the I/O port for the motor }
begin
  step := 0;
  steptab[0] = 13; { binary 01101 }
  steptab[1] = 9;  { binary 01001 }
  steptab[2] = 11; { binary 01011 }
  steptab[3] = 10; { binary 01010 }
  steptab[4] = 26; { binary 11010 }
  steptab[5] = 18; { binary 10010 }
  steptab[6] = 22; { binary 10110 }
  steptab[7] = 20; { binary 10100 }
  steptab[8] = 21; { binary 10101 }
  steptab[9] = 5;  { binary 00101 }
  write( motor, steptab[step] );

```

The remainder of the code is the same and doesn't depend on the motor. The following procedure will advance the motor one step in either direction, where the direction parameter must be either +1 or -1 to indicate the direction.

```

procedure onestep( direction: integer );
begin
  step := step + direction;
  if step > maxstep then step := 0
  else if step < 0 then step := maxstep;
  write( motor, steptab[step] );

```

```
end;
```

Software control of a stepping motor is a real-time task, and you need at least a bit of feedback. One bit is enough; typically, this will be a bit indicating that a cam on the turntable (or whatever the motor is driving) is interrupting a light beam or closing a microswitch. To avoid hysteresis problems in reading the position from this cam, you should only read zero to one transitions as indicating the home position when the motor is spinning in one direction. Especially with switches or where gear trains are involved between the motor and the turntable, the one to zero transition in the other direction won't usually occur at exactly the same position.

Given that you can read the sense bit and that you have a programmable interval timer interrupt on your system, it is easy to make the timer interrupt service routine operate the motor as follows:

```
const maxpos = 11111; { maxpos + 1 is calls to onestep per rev }
var position: integer; { current position of motor }
    destination: integer; { desired position of motor }
    direction: integer; { direction motor should rotate }
    last: integer; { previous value from position sensor }
    sensor: file of integer; { parallel input port }
begin
    read( sensor, last );
    position := 1;
    setdest( 0, 1 ); { force turntable to spin on power-up until
                    it finds it's home position }

procedure timer; { interval timer interrupt service routine }
var sense: integer;
begin
    read( sensor, sense );
    if (direction = 1) and (last = 0) and (sense = 1)
        then position = 0;
    last := sense;

    if position <> destination then begin
        onestep( direction );
        position := position + direction;
        if position > maxpos then position := 0
        else if position < 0 then position := maxpos;
    end;

    if position <> destination
        then settimer( interval_until_next_step );
end;
```

The following procedure is the only procedure that user code should call. This procedure sets the destination position of the turntable and sets the direction of rotation, then sets the interval timer to force an immediate interrupt and lets the timer routine finish rotating the turntable while the applications program does whatever else it wants.

```
procedure setdest( dst,dir: integer );
begin
    destination := dst;
    direction := dir;
    if position <> destination
        then settimer( min_interval ); { force a timer interrupt }
end;
```

If you want to control multiple stepping motors, it is easiest if you have one interval timers and one parallel port per motor. If your hardware has only one timer, then you can use it to simulate multiple interval timers, but this is most of the way to the job of writing a real-time executive.

A final note: If you try to step a motor too fast, it will slip and your software will lose track of the motor position. Motors typically come with a rating that indicates a maximum number of steps per second, but you may not be able to accelerate the motor to that number of steps per second from a dead start without running it at a lower speed first. This is especially true if the inertia of the load is fairly large, and in fact, with appropriate acceleration sequences, you can usually exceed the maximum rated speed.

In the above code, `interval_until_next_step` is shown as a constant. If you are dealing with high-inertia loads or very short intervals, you'll have to make this a variable, using longer intervals during starting and stopping to take care of accelerating and decelerating the motor.

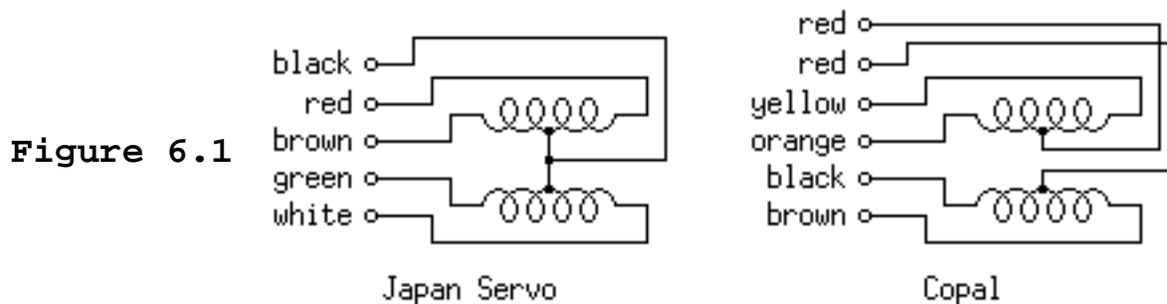
A Worked Stepping Motor Example

Old Part 6 of [Stepping Motors](#) by [Douglas W. Jones](#)

Perhaps some of the most commonly available stepping motors, for the experimenter, are the head positioning motors from old diskette drives. These can be found at electronics swap meets, in computer surplus outlets, and even in trash dumpsters. In addition to a stepper, a typical full-height 5.25 inch disk drive includes a 12 volt DC motor with tachometer and motor control circuit board, two microswitches, and a matched LED-photosensor pair.

A common stepper to find in a full-height IBM or Tandon 5.25 inch diskette drive is the type KP4M4 made by either Japan Servo Motors or Tandon; this is a permanent magnet motor with 3.6 degrees per step and 150 ohms per winding, with the center-taps of each winding tied to a common lead. Many half-height 5.25 inch diskette drives use very similar motors, although there is much more variety in newer drives, including some that use bipolar steppers.

Another stepper sometimes found in half-height drives is the 'pancake format' motor from a 1/2 height 5.25 inch diskette drive; for example, a permanent magnet motor made by Copal Electronics, with 1.8 degrees per step and 96 ohms per winding, with center taps brought out to separate leads. The leads on these motors are brought out to multipin in-line connectors, laid out as follows:



When the center-taps of these motors are connected to +12 and one end of either winding is grounded, the winding will draw from 170 mA to 250 mA, depending on the motor, so any of a number of motor drive circuits can be used. The original IBM full-height diskette drives used a pair of UDN3612N or UDN5713 chips; these are equivalent to chips in the SN7547X series (X in 1,2,3). The [ULN2003](#) darlington arrays from [Allegro Microsystems](#) is probably the most widely available of the applicable chips, so it will be used in this example.

Consider the problem of controlling multiple steppers comparable to those described above from an IBM compatible DB25-based parallel output port. The pinout of this connector is given in Figure 6.2, as seen looking at the face of the female connector on the back of an IBM PC (or equivalently, as seen from the back of the male connector that mates with this):

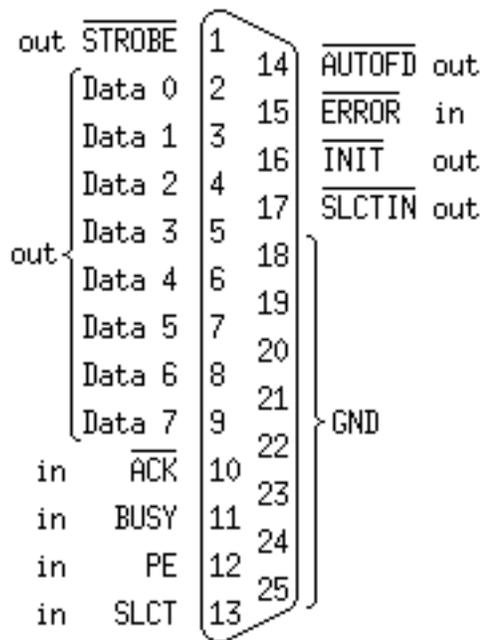


Figure 6.2

The IEEE 1284 standard gives the best available definition of the parallel port, but as an after-the-fact standard, nonconformance is common. [Some documentation](#) of this standard is available in the net. There is an extensive set of [tutorial material](#) available on the web discussing the IBM PC Parallel port. Another index of parallel port information is available from [Ian Harries](#).

There is some confusion in the documentation of this connector about the labels on the SLCT and SLCTIN lines (pins 13 and 17); this is because these names date back to a Centronics printer design of the early 1970's, and the name SLCTIN refers to an input to the printer, which is to say, an output from the computer.

The names of some of these lines are relics of the original intended purpose of this I/O port, as a printer port. Depending on the level at which you wish to use the printer, some may be ignored. If the BIOS printer support routines of the IBM PC or the parallel port driver under various versions of UNIX are to be used, however, it is important to pay attention to some of these signals:

The BIOS handles reinitializing the printer by producing a negative pulse on INIT (pin 16). We can use this as a reset pulse, but otherwise, it should be ignored! In the reset state, all motor windings should be off.

When no output activity is going on, the BIOS holds the output signal lines as follows:

- STROBE (pin 1) high, data not valid.
- AUTOFD (pin 14) high, do not feed paper.
- INIT (pin 16) high, do not initialize.
- SELCTIN (pin 17) low, printer selected.

To print a character, the BIOS waits for BUSY (pin 11) to go low, if it is not already low, and then outputs the new data (pins 2 through 9). Following this (with a delay of at least 0.5 microsecond),

STROBE (pin 1) is pulsed low for at least 0.5 microsecond. The BIOS returns the inputs ACK, BUSY, PE and SLCT (pins 10 to 13) to the user program after printing each character.

The computer is entitled to wait for ACK (pin 10) to go low for at least 5 microseconds to acknowledge the output strobe, but apparently, IBM's BIOS does not do so; instead, it relies on device to set BUSY to prevent additional outputs, and it leaves the output data unmodified until the next print request. While neither MS/DOS nor the BIOS use the interrupt capability of the parallel port, OS/2 and various versions of UNIX use it. A rising edge on ACK (pin 10) will cause an interrupt request if interrupts are enabled. This interrupt design is only useful if, in normal operation, the trailing edge of the ACK pulse happens when BUSY falls, so that finding BUSY high upon interrupt signals an error condition.

Note that all input output to the parallel port pins is done by writing to various I/O registers; so as long as interrupts are disabled and the I/O registers are directly manipulated by application software, all 5 input pins and all 12 output pins may be used for any purpose. To maintain compatibility with existing drivers, however, we will limit our misuse of these pins.

If we only wanted to support a single motor, it turns out that the logic on the standard 5.25 inch diskette drive can, with an appropriate cable, be driven directly from the parallel port. Documentation on this approach to recycling motors from old diskette drives has been put on the web by [Tomy Engdahl](#).

Since we are interested in supporting multiple motors, we will use DATA lines 4 to 7 to select the motor to control, while using the least significant bits to actually control the motor. In addition, because almost all stepping motor applications require limit switches, we will use the PE (12) bit to provide feedback from limit switches. The IEEE 1284 standard defines the PE, SLCT and ERR signals as user defined when in either Enhanced Parallel Port or Compatibility mode, so this is not a bad choice. Unfortunately, the BIOS occasionally checks this bit even when it is aware of no printer activity (for example, when the keyboard type-ahead buffer fills); thus, it is a good idea to disable the BIOS when the parallel port is used for motor control!

Note that fanout is not a problem on the IBM PC parallel port. The IEEE 1284 standard defines the parallel port outputs as being able to source and sink 14 milliamps, and older IBM PC parallel ports could sink about 24 milliamps. Given that a standard LS/TTL load sources only 0.4 milliamps and some devices (comparitors, for example) source 1.2 milliamps, an IEEE 1284 port should be able to handle up to 10 of motor interfaces in parallel.

A Minimal Design

As mentioned above, we will use the ULN2003 darlington array to drive the stepping motor. Since we want to drive multiple motors, the state of each motor must be stored in a register; while many chips on the market can store 4 bits, careful chip selection significantly reduces the parts count and allows for a single-sided printed circuit card!

With appropriate connections, both the 74LS194 and the 74LS298 can use a positive enable signal to gate a negative clock pulse; we will use the 74LS194 because it is less expensive and somewhat simpler to connect. Finally, we will use the 74LS85 chip to compare the 4 bit address field of the output with the

address assigned to the motor being driven.

Figure 6.3 summarizes the resulting design:

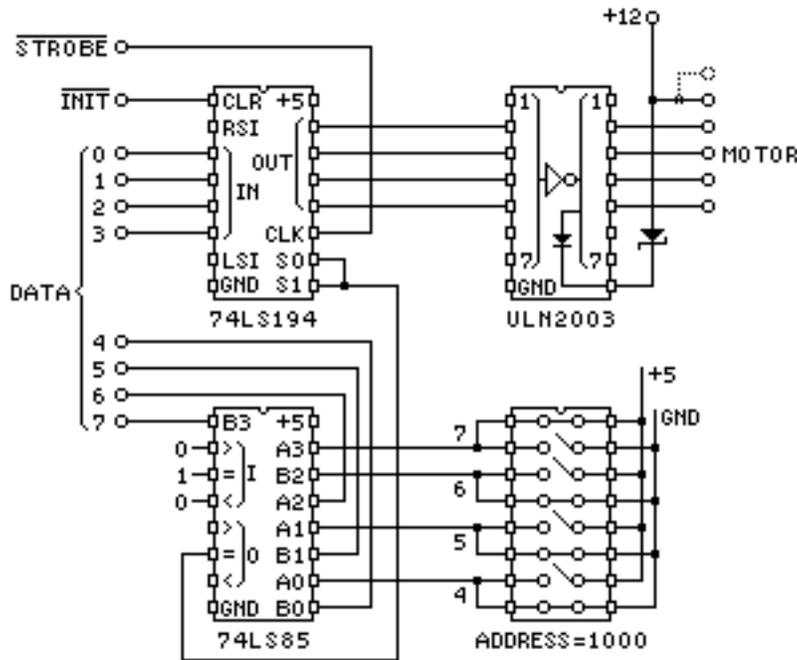


Figure 6.3

The 74LS194 was designed as a parallel-in, parallel-out shift-register with inputs to select one of 4 modes (do nothing, shift left, shift right, and load). By wiring the two mode inputs in parallel, we eliminate the shift modes, converting the mode input to an enable line. The unused right and left shift input pins on this chip can remain disconnected or can be grounded, tied to +5, or connected to any signal within the loading constraints.

Here, we show the 74LS194 being loaded only when bits 4 to 7 of the output data match a 4-bit address coded on a set of address switches. The comparison is done by a 74LS85, and the address switches are shown in Figure 6.3 as an 8-position DIP-switch. The cost of a DIP switch may be avoided in production applications by substituting jumpers.

One interesting aspect of this design is that the LS-TTL outputs driving the ULN2003 chip are used as current sources -- they pull up on the inputs to the darlington pairs. This is a borderline design, but careful reading of the LS-TTL spec sheets suggests that there is no reason it should not work, and the ULN2003 is obviously designed to be driven this way, with more than enough forward current gain to compensate for the tiny pull-up capacity of an LS-TTL output!

The Zener diode connected between pin 9 of the ULN2003 and the +12 supply increases the reverse voltage on the motor windings when they are turned off. Given the 50 volt maximum rating of the ULN2003, this may drop as much as 50-12 or 38 volts, but note that power dissipation may actually be an issue! At high frequency with unconventional control software, the power transfer to this diode can be quite efficient! With the stepping motors from old diskette drives, it may be possible to push a 12 volt zener to on the order of 1 watt. I used a 15 volt 1 watt zener, 1N3024.

If this motor is to be driven by software that directly accesses the low-level parallel port interface registers, directly loading data and then directly raising and lowering the strobe line, no additional hardware is needed. If it is to be driven through the BIOS or higher level system software, additional logic will be needed to manipulate ACK and BUSY.

Although the 74LS85 and the 74LS194 are no longer stocked by some mass-market chip dealers, they are still in production by Motorola, TI, Thompson SK and NTE. If over-the-counter availability of chips is your primary concern, adding a chipload of inverters or 2-input nand gates will allow just about any 4-bit latch to be used for the register, and address decoding can be done by a quad XOR chip and a 4-input nand gate.

If over-the-counter chips are of no concern, you can reduce the chip count even further! The Micrel MIC5800 4-bit parallel latch/driver can easily handle the loading of small unipolar steppers and combines the function of the ULN2003 and the 74LS194 used here! The resulting design is very clean.

Adding One Bit of Feedback

Surprisingly, no additional active components are needed to add one bit of feedback to this design! There are 3 spare darlington pairs on the ULN2003 driver chip, and, with a pull-up resistor for each, these can serve as open-collector inverters to translate one or two switch settings into inputs appropriate for the PC!

The ULN2003 includes pull-down resistors on each input, guaranteeing that the corresponding output will be turned off if left disconnected. Thus, connecting a ULN2003 input to a positive enable signal (pin 5 of the 74LS85 chip for example) will turn the output on only if it is both enabled and the switch is closed. It may be necessary to add a 1K pull-up to the LS-TTL output because, in addition to driving the ULN2003, it is also driving two normal LS-TTL inputs. Adding this pull-up will do no harm if it isn't needed (an LS-TTL output should be able to handle even a 300 ohm pull-up).

Since the ULN2003 is an open-collector inverter, the output needs a pull-up. We could rely on the PE input of the IBM PC parallel port to pull this line up (an open TTL input usually rises of its own accord), but it is better to provide a pull-up resistor somewhere. Here, we provide a 10K pull up on each stepping motor drive card; these pull-ups will be in parallel for all motors attached to a single parallel port, so if fewer than 10 motors are in use, proportionally smaller resistors may be substituted or the pull-ups may be omitted from some of the controller cards.

Figure 6.4 summarizes these additions to the design:

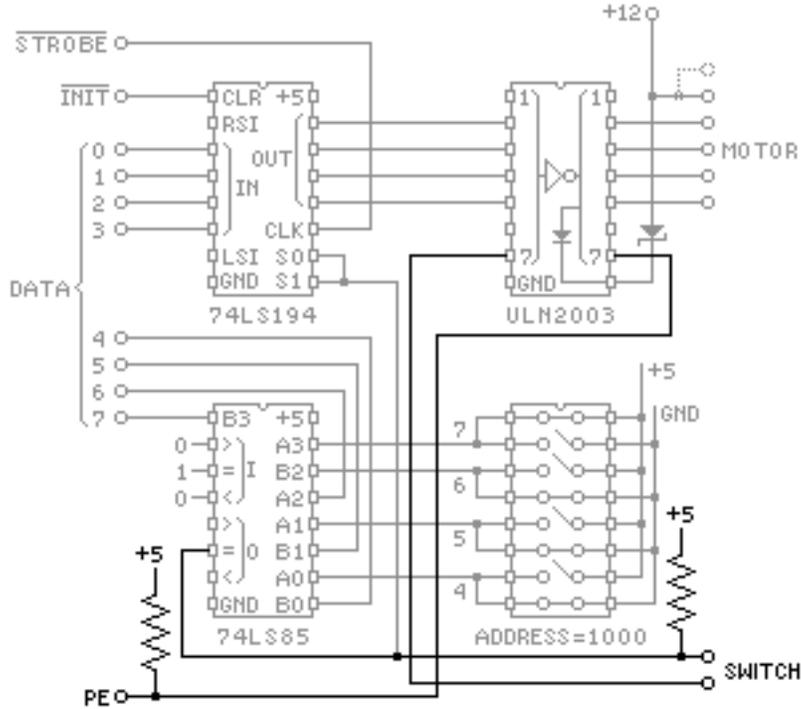


Figure 6.4

Something You Can Build

Figure 6.5 shows a single-sided PC board layout for a 2.5 inch square board that incorporates all of the ideas given above. I have etched and tested the 7/8/1996 version of this artwork.

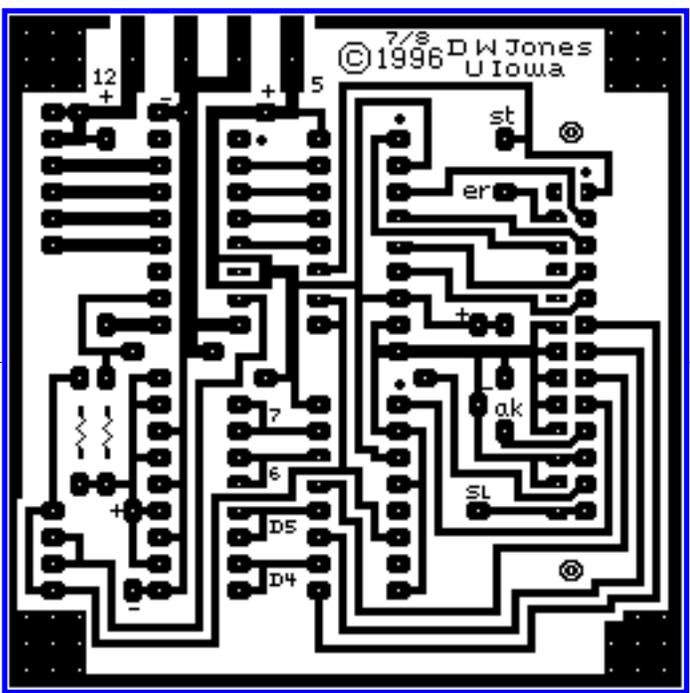


Figure 6.5

(What's that about copyright notices? Well, put simply, if you're going to sell my design, please get in touch with me about it. You're free, however, to make a handful of boards from this design to control your own motors.)

This version of the board includes a jumper to ground the BUSY signal on the parallel port (pin 11) and it brings out the STROBE, ERROR, ACK and SELECT signals to allow for possible jumpering. These

changes make it slightly more likely that this board can be hacked to work with native operating system drivers for the parallel port. As is, with no special jumpering other than the grounding of BUSY, it works under Linux.

To use the artwork in Figure 6.5 for etching a board, reproduce it at 100 pixels per inch (a [slightly finer version](#), using 150 pixels per inch, is also available). Both versions are positive images of the foil side of the board; depending on how you transfer the image to the resist for etching, you may need to flip it left-to-right and/or invert the black and white. Most GIF viewers allow for such transformations.

Figure 6.6 shows the component side of this board, with jumpers and parts in place.

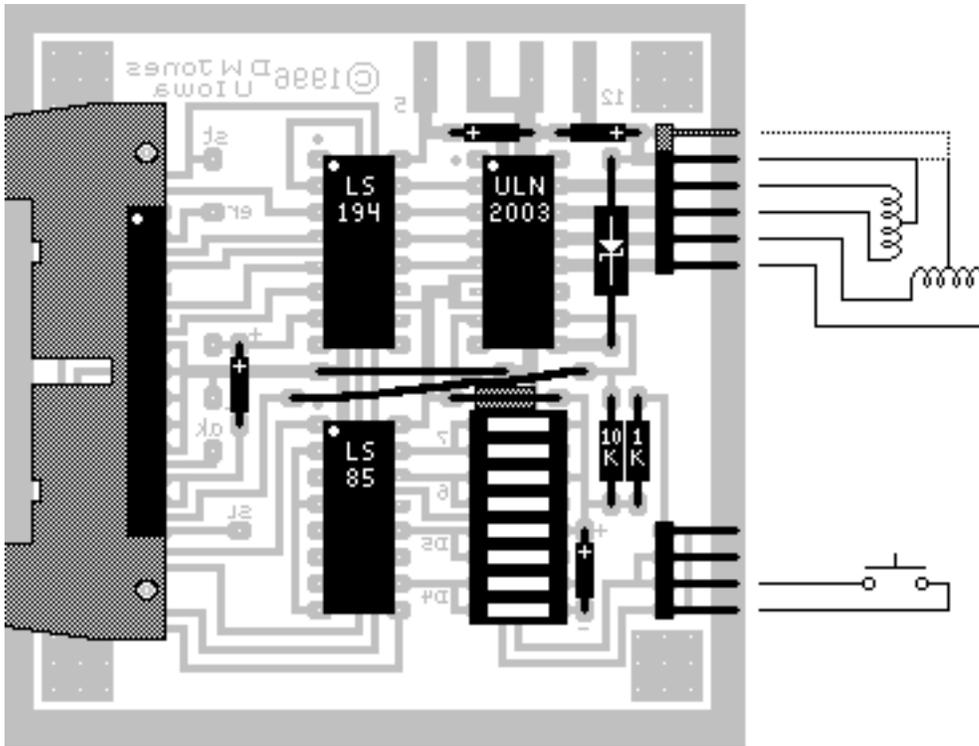


Figure 6.6

Note that this layout does not show mounting holes for the board, but that 4 corner pads are provided in appropriate locations. The layout also doesn't show a power connector, but the standard 4-pin Molex connectors used with 5.25" diskettes will fit the pads provided. The 26 pin header is wired so that it can be directly wired to a 25 pin DB-25 plug using ribbon cable and insulation displacement connectors. If multiple motors are to be used, a single ribbon cable can be made with multiple 26 pin connectors on it, one per motor.

Figure 6.6 shows 4 capacitors, 3 between +5 and ground, and 1 between +12 and ground. The two capacitors farthest from the power input connector can be .01 or .1 microfarad capacitors; it may be better to use larger capacitors at the power input pins, 1 microfarad or more.

The plug from the stepping motor goes on the the top header shown in Figure 6.6, with the center-tap lead(s) topmost. The board is arranged so that either a 5 or 6 pin header may be used here, compatible with the plugs on the motors shown in Figure 6.1. The limit switch goes on the bottom header. The latter is wired so that it can be used with either microswitch salvaged from a full-height Tandon 5.25 inch diskette drive.

The address may be hard-wired using 4 jumpers, or it may be set using a DIP-switch, as shown in Figure 6.6. Each bit of the address is set by two switches or jumper positions, the topmost pair of these sets the most significant of the 4 bits. To set a bit to 1, close the top switch and open the bottom switch in the corresponding pair (or put the jumper in the top position of the pair); to set a bit to 0, close the bottom switch and open the top one (or put the jumper in the bottom position of the pair). If it is at all likely that someone will change the address switches while power is applied to the board, use a 47 ohm resistor in place of the jumper directly above the switches! This will protect your power supply from the accidental short circuits that can result from improper switch settings.

Testing the Board

Under Linux

The standard Linux line printer driver attaches the device `/dev/lp0` to the standard parallel port found on most PCs, and `/dev/lp1` and `/dev/lp2` to the optional additional parallel ports. The line printer driver has many operating modes that may be configured and tested with the `tunelp` command. The default mode works, and the following `tunelp` command will restore these defaults:

```
tunelp /dev/lp0 -i 0 -w 0 -a off -o off -c off
```

This turns off interrupts with `-i 0` so that the board need not deal with the acknowledge signal, and it uses a very brief strobe pulse with `-w 0`, sets the parallel port to ignore the error signal with `-a off`, ignores the status when the port is opened with `-o off`, and does not check the status with each byte output with `-C off`. The settings of the `tunelp` options `-t` and `-c` should not matter because this interface is always ready and thus polling loop iteration is never required.

Given a correctly configured printer port, the C routines allow output of motor control bytes to the port:

```
/**
 * ppaccess.c
 * package for Linux access to the parallel port
 */
****/

#include <fcntl.h>
#include <sys/ioctl.h>
#include <errno.h>
#include <linux/lp.h>
#include <stdio.h>

#define paper_empty 0x20

static int pp; /* parallel port device descriptor */

void ppsetup()
/* setup access to the parallel port */
{
```

```

    pp = open("/dev/lp0",O_WRONLY, 0);
    if (pp < 0) {
        printf( "can't open, %s\n",
                _sys_errlist[errno] );
        exit( 0 );
    }
}

int ppput(int b)
/* put b to the parallel port, return the status */
{
    char cbuf = (char) b;
    int ibuf;
    /* begin critical section */
    if(write( pp, &cbuf, 1 ) != 1){
        printf( "write error, %s\n",
                _sys_errlist[errno] );
        exit( 0 );
    }
    if(ioctl( pp, LPGETSTATUS, (char*)&ibuf ) < 0){
        printf( "/dev/lp0 ioctl error, %s\n",
                _sys_errlist[errno] );
        exit( 0 );
    }
    /* end critical section */
    return ibuf & paper_empty;
}

```

The comments above indicate a critical section! This is only a concern if the parallel port is shared by multiple processes, perhaps with one process in charge of each of a number of motors. In that case, a semaphore must be used to guard the critical section (see the UNIX `semctl()` kernel call), or a file lock using `F_LOCK` and `F_ULOCK` `fcntl()` commands, preferably through the `lockf()` function with a very large size. The latter approach (file locking using `lockf()`) is probably preferable.

Given these routines, the following main program should spin the motor at a few steps per second in a direction that depends on the sense switch setting:

```

/****
*   spin.c
*   Linux program to spin a permanent magnet motor *
*   the sense switch gives forward/reverse control *
****/

#include <unistd.h>

#define motor_number 15
#define microsec     1

```

```

#define millisec      (1000*microsec)

int steptab[] = { 9, 10, 6, 5 };

main()
{
    int step = 0;
    ppsetup();
    for (;;) {
        if (ppput(steptab[step]|motor_number<<4)) {
            step = (step + 1)&3;
        } else {
            step = (step - 1)&3;
        }
        usleep( 100 * millisec );
    }
}

```

Under Quasic

To test your board, use the following little basic program. This was developed under Microsoft's Qbasic (C:DOS/QBASIC on a typical off-the-shelf PC) under bare MS/DOS (no version of windows running). This code has been tested with the prototype hardware for the design given above.

The first subroutine in this program outputs the data D to motor M attached to printer port P, and reads the status into S.

```

100 OUT P, D + (16 * M)
    OUT P + 2, &HD
    OUT P + 2, &HC
    S = INP(p + 1)
    RETURN

```

The second subroutine updates D, the motor control output, to rotate the motor one step, then uses the first subroutine to output D.

```

200 IF D = 9 THEN
    D = 10
ELSEIF D = 5 THEN
    D = 9
ELSEIF D = 6 THEN
    D = 5
ELSEIF D = 10 THEN
    D = 6
END IF
GOSUB 100

```

RETURN

The main program connects the second subroutine to the real-time clock and uses it to step the motor once per second, then repeatedly prints the status reported by the motor.

```
P = &H378
M = 15
D = 10
ON TIMER(1) GOSUB 200
TIMER ON
305 LOCATE 5, 5
PRINT "STATUS: "; S; " "
GOTO 305
```

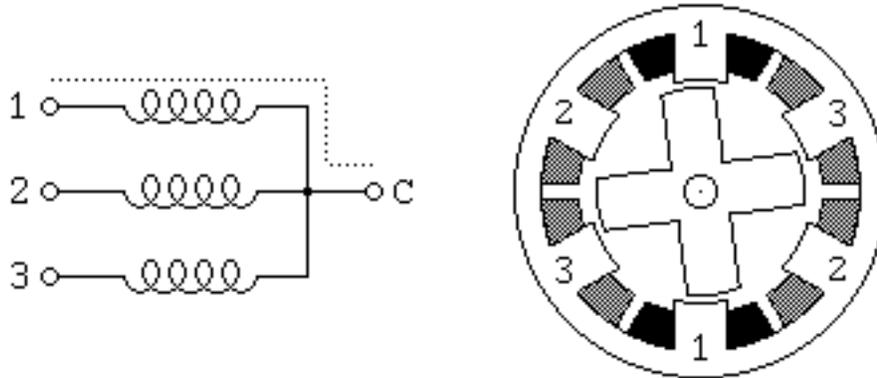
Unfortunately, QBASIC doesn't give you access to the high resolution of the hardware real-time clock, so this prototype code is only good for testing the hardware. While this code is running, the status of the microswitch (if connected) will be displayed on the screen, embedded in the rest of the I/O port status word.

Variable Reluctance Motors Animated

Appendix to [Stepping Motors Types](#) by [Douglas W. Jones](#)

Here is Figure 1.1 in animated form:

Figure 1.1



In looking at this figure, note the following:

1. The field appears to rotate counterclockwise while the rotor rotates clockwise. This arrangement is common with variable reluctance motors.
2. The field rotates in steps of 60 degrees while the rotor moves in 30 degree steps (in the opposite direction). This is reminiscent of the motion of the scale of a vernier caliper, and for this reason, variable reluctance motors are sometimes known as vernier motors.
3. It takes four complete cycles of the control system to rotate the motor rotor through one cycle. This is because the rotor has 4 poles!

Permanent Magnet Motors Animated

Appendix to [Stepping Motors Types](#) by [Douglas W. Jones](#)

Here is Figure 1.2 in animated form:

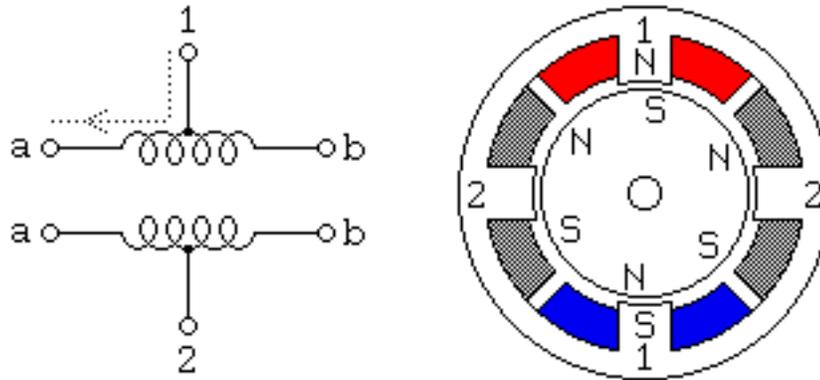


Figure 1.2

In looking at this figure, note the following:

1. This 6-pole rotor turns in a direction opposite the rotation of the stator field; a two-pole rotor inside the exact same stator would rotate with the field.
2. This illustration is based on half-step control, where alternate half steps involve one and two motor windings.
3. It takes three complete cycles of the control system to turn this 6-pole rotor one revolution. A two-pole rotor would turn a full revolution per control system cycle.

ULx2003 thru ULx2024 High-Voltage, High-Current Darlington Arrays

[Data Sheet](#) (PDF)

Selection Guides:

[High-Voltage](#)

[Peripheral](#)

[Power and](#)

[Display Drivers](#),

[LED Display](#)

[Drivers](#),

[Power Sink](#)

[Drivers](#)

when this
product is
updated

These parts have been phased out of production. Samples are no longer available.

Features

- TTL, DTL, PMOS, or CMOS-Compatible Inputs
- Output Current to 500 mA
- Output Voltage to 95 V
- Transient-Protected Outputs

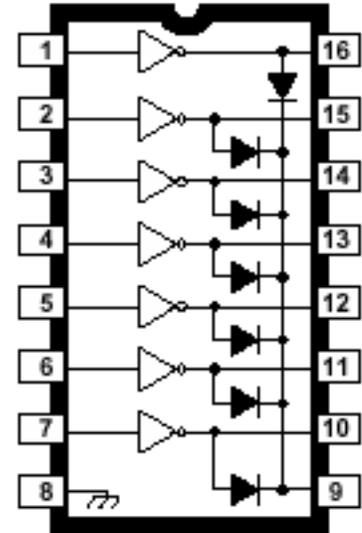
Description

Ideally suited for interfacing between low-level logic circuitry and multiple peripheral power loads, the Series ULN20xxA/L high-voltage, high-current Darlington arrays feature continuous load current ratings to 500 mA for each of the seven drivers. At an appropriate duty cycle depending on ambient temperature and number of drivers turned ON simultaneously, typical power loads totaling over 230 W (350 mA x 7, 95 V) can be controlled. Typical loads include relays, solenoids, stepping motors, magnetic print hammers, multiplexed LED and incandescent displays, and heaters. All devices feature open-collector outputs with integral clamp diodes.

The ULN2003A/L and ULN2023A/L have series input resistors selected for operation directly with 5 V TTL or CMOS. These devices will handle numerous interface needs — particularly those beyond the capabilities of standard logic buffers.

The ULN2004A/L and ULN2024A/L have series input resistors for operation directly from 6 to 15 V CMOS or PMOS logic outputs. The ULN2003A/L and ULN2004A/L are the standard Darlington arrays. The outputs are capable of sinking 500 mA and will withstand at least 50 V in the OFF state. Outputs may be paralleled for higher load current capability. The ULN2023A/L and ULN2024A/L will withstand 95 V in the OFF state.

These Darlington arrays are furnished in 16-pin dual in-line plastic packages (suffix 'A') and 16-lead surface-mountable SOICs (suffix 'L'). All devices are pinned with outputs opposite inputs to facilitate ease of circuit board layout. All



Dwg. No. A-9594

devices are rated for operation over the temperature range of -20°C to +85°C. Most (see matrix, next page) are also available for operation to -40°C; to order, change the prefix from 'ULN' to 'ULQ'.

Complete Part Numbers

Part Number	Package Type	RoHS Compliant	Part Composition/ RoHS Data	Temperature	Comments	Samples
ULN2003A	16-pin DIP	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULN2003L	16-lead SOIC	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULQ2003A	16-pin DIP	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
ULQ2003L	16-lead SOIC	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
ULN2004A	16-pin DIP	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULN2004L	16-lead SOIC	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULQ2004A	16-pin DIP	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
ULQ2004L	16-lead SOIC	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
ULN2023A	16-pin DIP	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULN2023L	16-lead SOIC	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULQ2023A	16-pin DIP	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
ULN2024A	16-pin DIP	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
ULN2024L	16-lead SOIC	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available

Related Applications

[Automotive](#) > [Driver Information Systems](#) > [Primary Instrumentation \(dashboard\)](#)

DS2003 - High Current/Voltage Darlington Driver



[Datasheet](#)

[Packaging](#)

[Samples & Pricing](#)

[Eval. Boards](#)

[Models](#)

[Knowledge Base](#)

Features

- Seven high gain Darlington pairs
- High output voltage ($V_{CE} = 50V$)
- High output current ($I_C = 350\text{ mA}$)
- TTL, PMOS, CMOS compatible
- Suppression diodes for inductive loads
- Extended temperature range

General Description

The DS2003 is comprised of seven high voltage, high current NPN Darlington transistor pairs. [More...](#)

Typical Application

[See Datasheet for Application Information](#)

Parametric Table [expand](#)

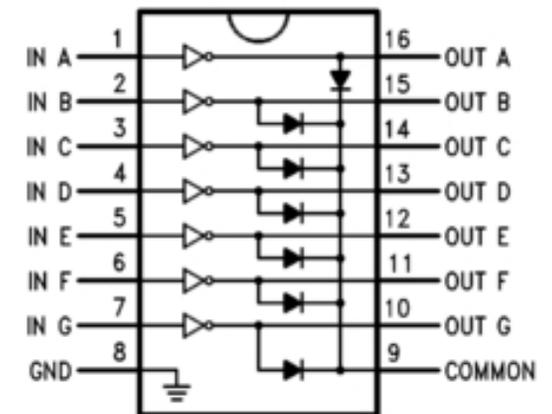
Output Bus	CMOS/TTL
Propagation Delay	5000 ns
Drivers	1

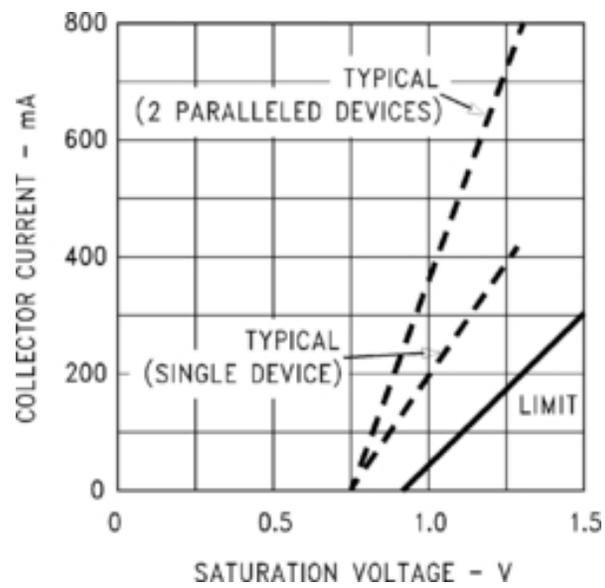
Typical Performance

[click for larger image](#)

Connection Diagram

[click for larger image](#)





Datasheet

	RoHS Compliance Information	Size in Kbytes	Date			
DS2003 High Current/Voltage Darlington Drivers		642 Kbytes	25-Mar-05	View Online	Download	Receive via Email

If you have trouble printing or viewing PDF file(s), see [Printing Problems](#).

Package Availability, Models, Samples & Pricing

Part Number	Package						Factory Lead Time		Models	Samples & Electronic Orders	Budgetary Pricing		Std Pack Size	Package Marking Format
	Type	Pins	Spec.	MSL Rating	Peak Reflow	RoHS Report	Weeks	Qty			Qty	\$US each		
DS2003CM	SOIC NARROW	16	STD	1	235		Full production		N/A	Samples (Fee) Buy Now	1K+	\$0.41	rail of 48	NSUZXYTT DS2003CM
			NOPB	1	260	RoHS	6 weeks	7476						
DS2003TM	SOIC NARROW	16	STD	1	235		Full production		N/A	Samples (Fee) Buy Now	1K+	\$0.41	rail of 48	NSUZXYTT DS2003TM
			NOPB	1	260	RoHS	6 weeks	16736						

DS2003CMX	SOIC NARROW	16	STD	1	235		Full production		N/A		1K+	\$0.41	reel of 2500	NSUZXYTT DS2003CM
			NOPB	1	260	RoHS	6 weeks	10848						
DS2003TMX	SOIC NARROW	16	STD	1	235		Full production		N/A		1K+	\$0.41	reel of 2500	NSUZXYTT DS2003TM
			NOPB	1	260	RoHS	6 weeks	103320						
DS2003TMT	TSSOP	16	STD	1	260		Full production		N/A	 	1K+	\$0.61	rail of 92	NSZXYTT DS2003T MT
			NOPB	1	260	RoHS	6 weeks	1023						
DS2003TMTX	TSSOP	16	STD	1	260		Full production		N/A		1K+	\$0.61	reel of 2500	NSZXYTT DS2003T MT
			NOPB	1	260	RoHS	6 weeks	2500						
DS2003CN	MDIP	16	STD	1	NA		Full production		N/A	 	1K+	\$0.42	rail of 25	NSUZXYTT DS2003CN DS9667CN
			NOPB	1	NA	RoHS	6 weeks	21940						
DS2003TN	MDIP	16	STD	1	NA		Full production		N/A		1K+	\$0.42	rail of 25	NSUZXYTT DS2003TN
			NOPB	1	NA	RoHS	6 weeks	3330						
DS2003 MDC	Unpackaged Die						Lifetime buy		N/A				tray of N/A	-
							N/A	5000						

Obsolete Parts

Obsolete Part	Alternate Part or Supplier	Source	Last Time Buy Date
DS2003CJ	XR2003C	EXR	03/10/98
DS2003MJ	MC1413	MOTOROLA PHILIPS	03/09/99

General Description

The DS2003 is comprised of seven high voltage, high current NPN Darlington transistor pairs. All units feature common emitter, open collector outputs. To maximize their effectiveness, these units contain suppression diodes for inductive loads and appropriate emitter base resistors for leakage.

The DS2003 has a series base resistor to each Darlington pair, thus allowing operation directly with TTL or CMOS operating at supply voltages of 5.0V.

The DS2003 offers solutions to a great many interface needs, including solenoids, relays, lamps, small motors, and LEDs. Applications requiring sink currents beyond the capability of a single output may be accommodated by paralleling the outputs.

[Information as of 27-Sep-2006]

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UDx2547

Protected Quad Power Drivers

[Data Sheet](#) (PDF)

Selection Guides:
[Automotive ICs,](#)

[High-Voltage Peripheral Power and Display Drivers,](#)

[Power Sink Drivers](#)

when this product is updated

Technical Publications:
[A Primer on Driving Incandescent Lamps](#) (PDF)

These parts have been phased out of production. Samples are no longer available.

Features

- Output SOA Protection
- Low Output-Saturation Voltage
- Diagnostic FAULT Output
- TTL and 5 V CMOS Compatible Inputs
- Independent Over-Current Protection for Each Driver
- Independent Thermal Protection for Each Driver
- 600 mA Output Current per Channel

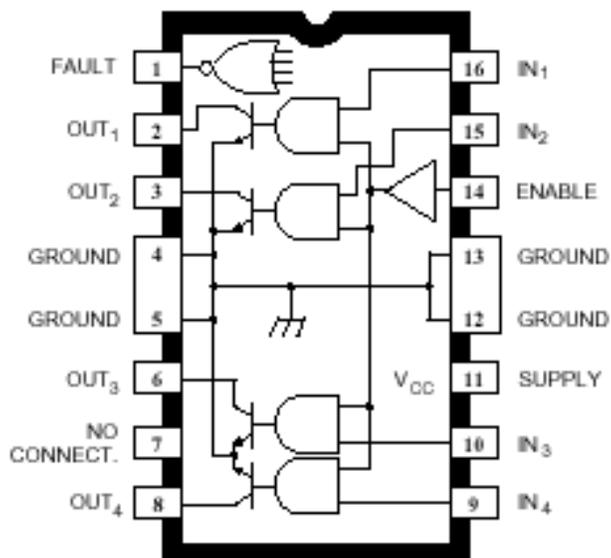
Description

Providing interface between low-level logic and power loads, the UDK/UDN/UDQ2547B and UDK/UDN/UDQ2547EB quad power drivers combine logic gates and high-current bipolar outputs with complete output protection. Each of the four outputs will sink 600 mA in the ON state. The outputs have a minimum breakdown voltage (load dump) of 60 V and a sustaining voltage of 40 V. The inputs are compatible with TTL and 5 V CMOS logic systems and include internal pull-down resistors to ensure that the outputs remain OFF if the inputs are open-circuited.

Over-current protection for each channel has been designed into these devices and is activated at approximately 1.3 A. It protects each output from short circuits with supply voltages up to 25 V. When a maximum driver output current is reached, that output drive is reduced linearly, maintaining a constant load current. If the over-current or short circuit condition continues, each channel has an independent thermal limit circuit which will sense the rise in junction temperature and turn OFF the individual channel that is at fault. Foldback circuitry decreases the output current if excessive voltage is present across the output and assists in keeping the device within its SOA (safe operating area).

Each output also includes diagnostics for increased device protection. If any output is shorted or opened, the diagnostics can signal the controlling circuitry through a

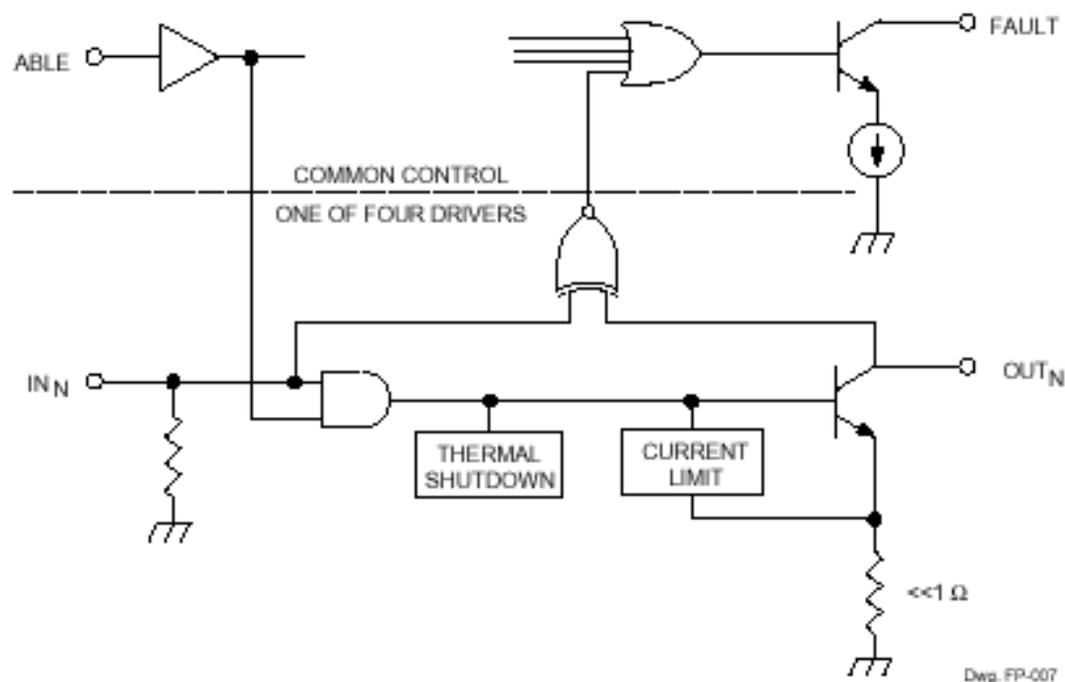
UDx2547B



Dwg. PP-018A

common FAULT pin. These devices can be used to drive various resistive loads including incandescent lamps (without warming or limiting resistors). With the addition of external output clamp diodes, they can be used to drive inductive loads such as relays, solenoids, or dc stepping motors.

Functional Block Diagram



Complete Part Numbers

Part Number	Package Type	RoHS Compliant	Part Composition/ RoHS Data	Temperature	Comments	Samples
UDN2547B	16-pin DIP	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
UDN2547EB	28-lead PLCC	No	view data	-20°C to 85°C	<i>discontinued</i>	samples not available
UDQ2547B	16-pin DIP	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
UDQ2547EB	28-lead PLCC	No	view data	-40°C to 85°C	<i>discontinued</i>	samples not available
UDK2547EB	28-lead PLCC	No	view data	-40°C to 125°C	<i>discontinued</i>	samples not available

Related Applications

[Automotive](#) > [Body Systems](#) > [Body Control Module](#)

[Automotive](#) > [Body Systems](#) > [Electric Sun Roof](#)

[Automotive](#) > [Body Systems](#) > [HVAC](#)

[Automotive](#) > [Body Systems](#) > [Power Seats](#)

[Automotive](#) > [Body Systems](#) > [Power Sliding Door](#)

[Automotive](#) > [Body Systems](#) > [Rain Sensing Wipers](#)

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Allegro MicroSystems, Inc., 115 Northeast Cutoff, Worcester, MA 01606 USA,
TEL: 1.508.853.5000, FAX: 1.508.853.7895



L293B L293E

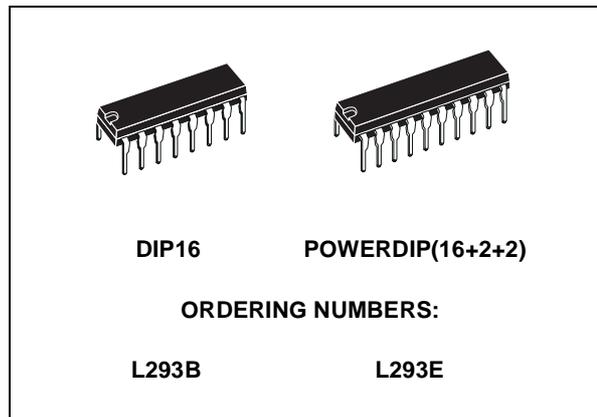
PUSH-PULL FOUR CHANNEL DRIVERS

- OUTPUT CURRENT 1A PER CHANNEL
- PEAK OUTPUT CURRENT 2A PER CHANNEL (non repetitive)
- INHIBIT FACILITY
- HIGH NOISE IMMUNITY
- SEPARATE LOGIC SUPPLY
- OVERTEMPERATURE PROTECTION

DESCRIPTION

The L293B and L293E are quad push-pull drivers capable of delivering output currents to 1A per channel. Each channel is controlled by a TTL-compatible logic input and each pair of drivers (a full bridge) is equipped with an inhibit input which turns off all four transistors. A separate supply input is provided for the logic so that it may be run off a lower voltage to reduce dissipation.

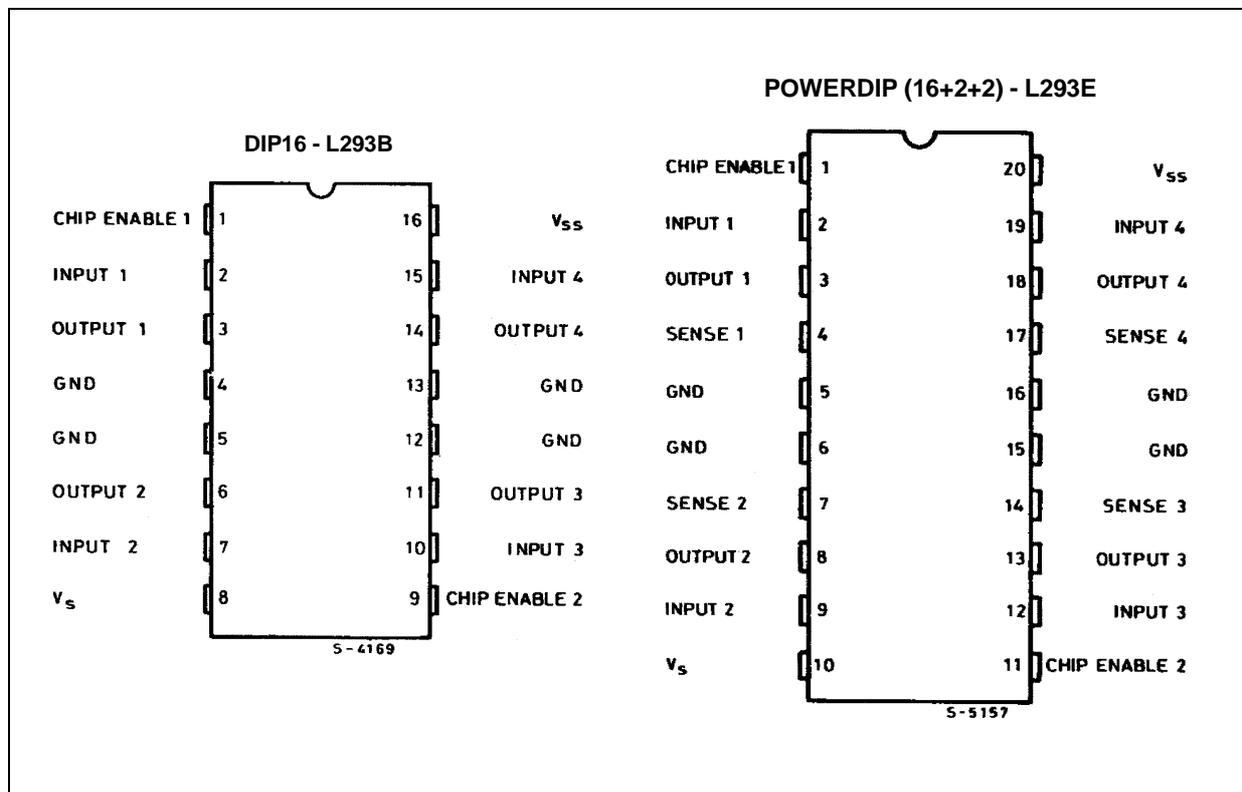
Additionally, the L293E has external connection of



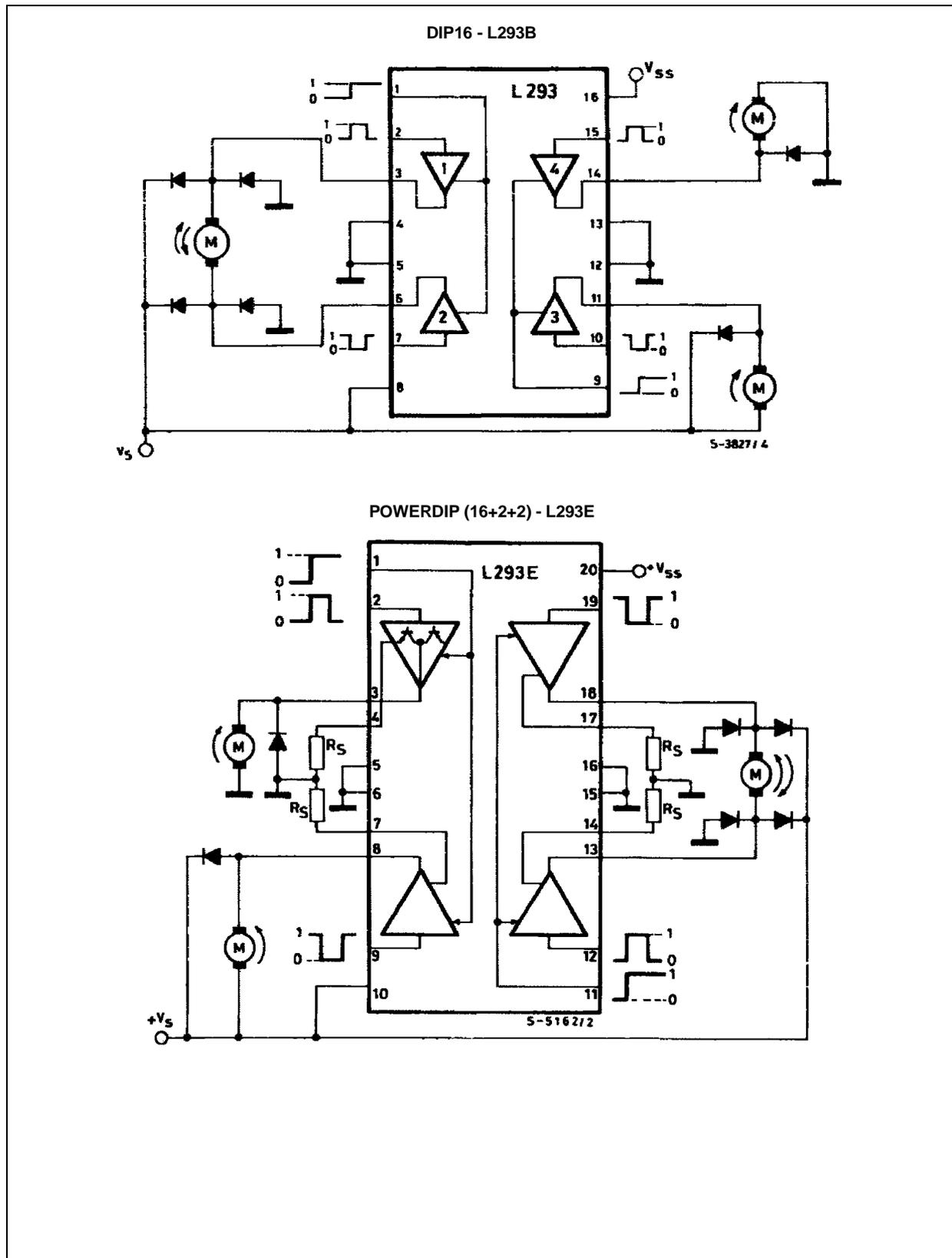
sensing resistors, for switchmode control.

The L293B and L293E are package in 16 and 20-pin plastic DIPs respectively ; both use the four center pins to conduct heat to the printed circuit board.

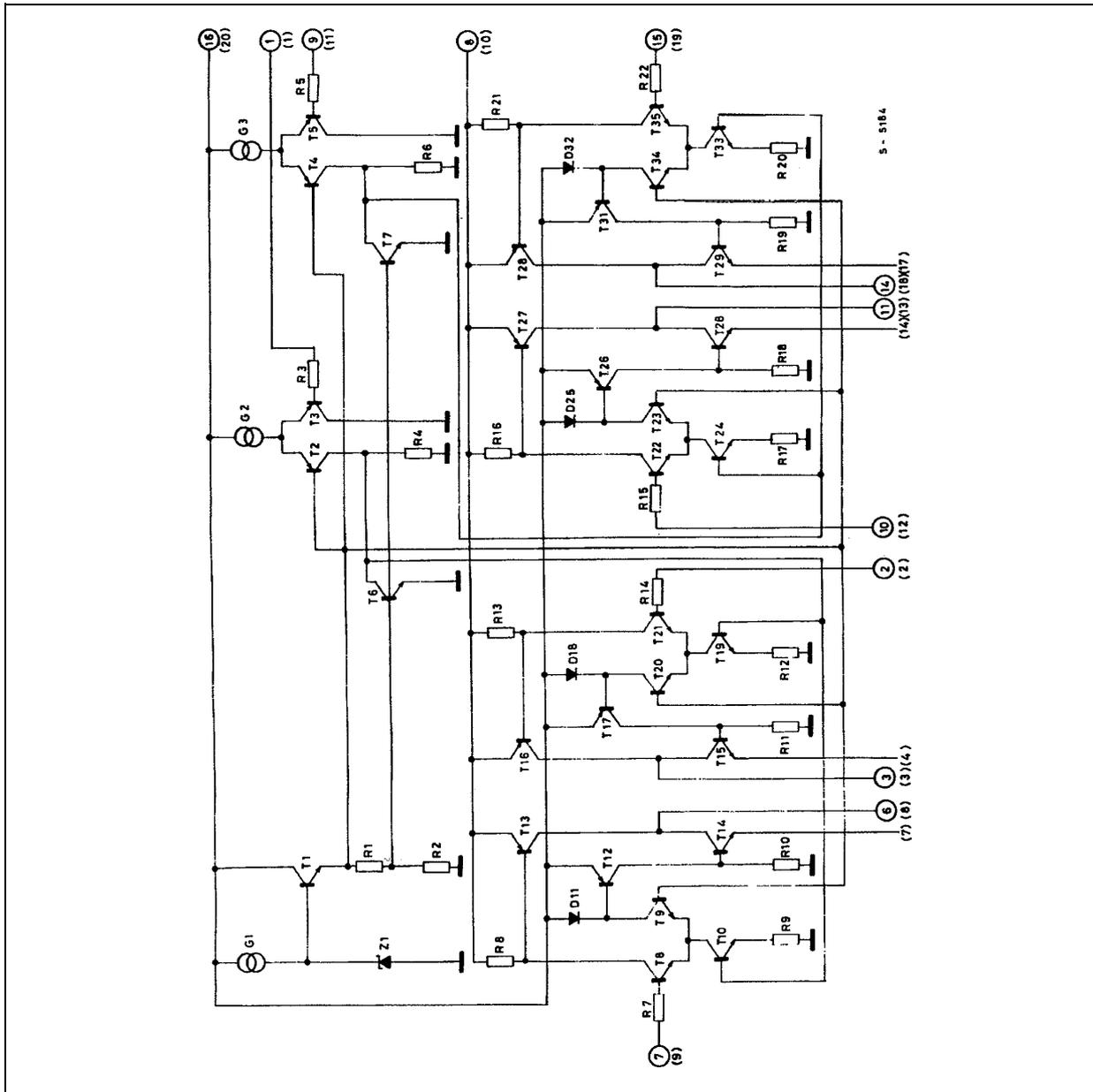
PIN CONNECTION (Top view)



BLOCK DIAGRAMS



SCHEMATIC DIAGRAM



(*) In the L293 these points are not externally available. They are internally connected to the ground (substrate).
 O Pins of L293 () Pins of L293E.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_s	Supply Voltage	36	V
V_{ss}	Logic Supply Voltage	36	V
V_i	Input Voltage	7	V
V_{inh}	Inhibit Voltage	7	V
I_{out}	Peak Output Current (non repetitive $t = 5ms$)	2	A
P_{tot}	Total Power Dissipation at $T_{ground-pins} = 80^{\circ}C$	5	W
T_{stg}, T_j	Storage and Junction Temperature	-40 to +150	$^{\circ}C$



L293E L293B

THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th\ j-case}$	Thermal Resistance Junction-case	Max. 14	$^{\circ}C/W$
$R_{th\ j-amb}$	Thermal Resistance Junction-ambient	Max. 80	$^{\circ}C/W$

ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_s	Supply Voltage		V_{SS}		36	V
V_{SS}	Logic Supply Voltage		4.5		36	V
I_s	Total Quiescent Supply Current	$V_i = L; I_o = 0; V_{inh} = H$		2	6	mA
		$V_i = h; I_o = 0; V_{inh} = H$		16	24	mA
		$V_{inh} = L$			4	mA
I_{SS}	Total Quiescent Logic Supply Current	$V_i = L; I_o = 0; V_{inh} = H$		44	60	mA
		$V_i = h; I_o = 0; V_{inh} = H$		16	22	mA
		$V_{inh} = L$		16	24	mA
V_{iL}	Input Low Voltage		-0.3		1.5	V
V_{iH}	Input High Voltage	$V_{SS} \leq 7V$	2.3		V_{SS}	V
		$V_{SS} > 7V$	2.3		7	V
I_{iL}	Low Voltage Input Current	$V_{iL} = 1.5V$			-10	μA
I_{iH}	High Voltage Input Current	$2.3V \leq V_{iH} \leq V_{SS} - 0.6V$		30	100	μA
V_{inhL}	Inhibit Low Voltage		-0.3		1.5	V
V_{inhH}	Inhibit High Voltage	$V_{SS} \leq 7V$	2.3		V_{SS}	V
		$V_{SS} > 7V$	2.3		7	V
I_{inhL}	Low Voltage Inhibit Current	$V_{inhL} = 1.5V$		-30	-100	μA
I_{inhH}	High Voltage Inhibit Current	$2.3V \leq V_{inhH} \leq V_{SS} - 0.6V$			± 10	μA
V_{CEsatH}	Source Output Saturation Voltage	$I_o = -1A$		1.4	1.8	V
V_{CEsatL}	Sink Output Saturation Voltage	$I_o = 1A$		1.2	1.8	V
V_{SENS}	Sensing Voltage (pins 4, 7, 14, 17) (**)				2	V
t_r	Rise Time	0.1 to $0.9 V_o$ (*)		250		ns
t_f	Fall Time	0.9 to $0.1 V_o$ (*)		250		ns
t_{on}	Turn-on Delay	$0.5 V_i$ to $0.5 V_o$ (*)		750		ns
t_{off}	Turn-off Delay	$0.5 V_i$ to $0.5 V_o$ (*)		200		ns

* See figure 1

** Referred to L293E

TRUTH TABLE

V_i (each channel)	V_o	V_{inh} (**)
H	H	H
L	L	H
H	X (*)	L
L	X (*)	L

(*) High output impedance

(**) Relative to the considerate channel

Figure 1. Switching Timers

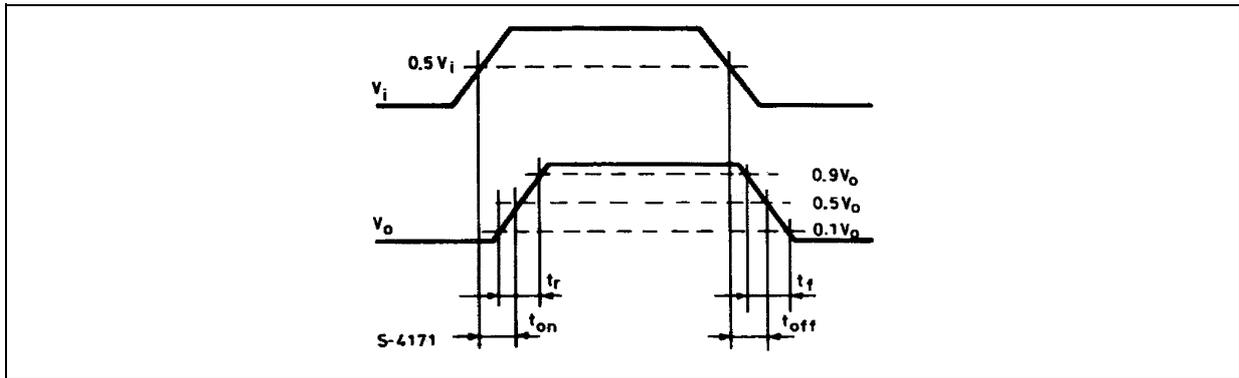


Figure 2. Saturation voltage versus Output Current

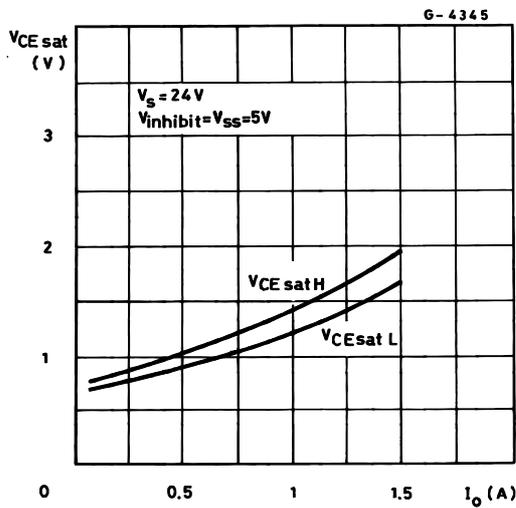


Figure 3. Source Saturation Voltage versus Ambient Temperature

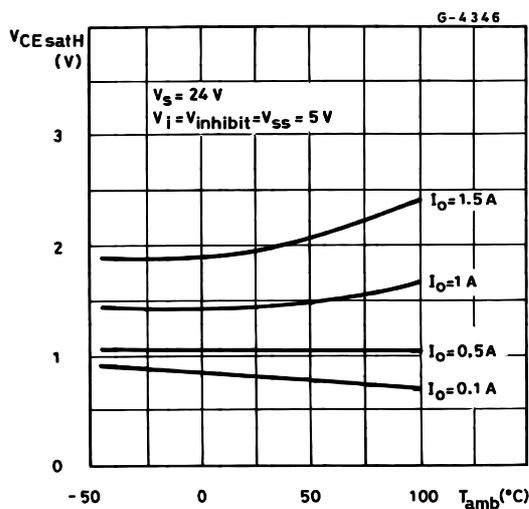


Figure 4. Sink Saturation Voltage versus Ambient Temperature

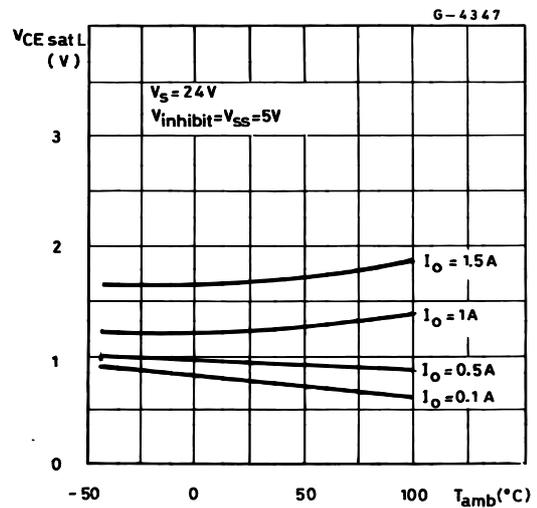


Figure 5. Quiescent Logic Supply Current versus Logic Supply Voltage

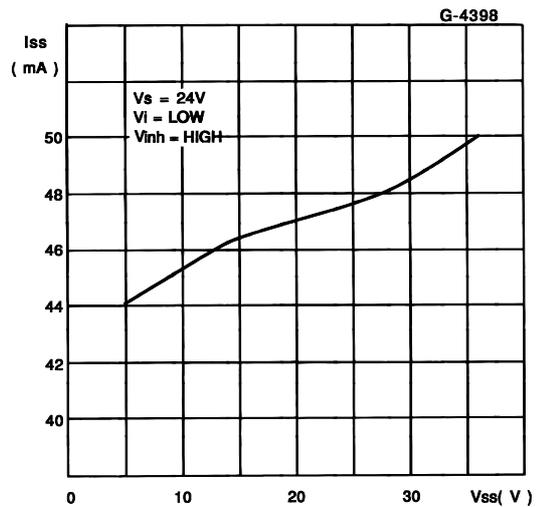


Figure 6. Output Voltage versus Input Voltage

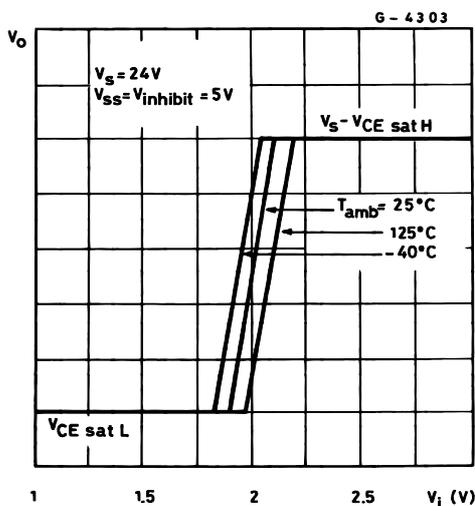
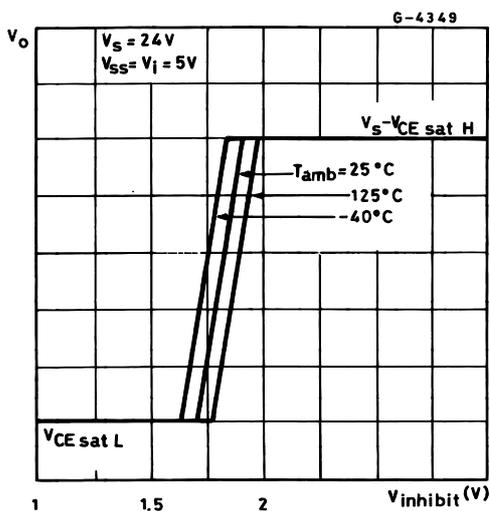
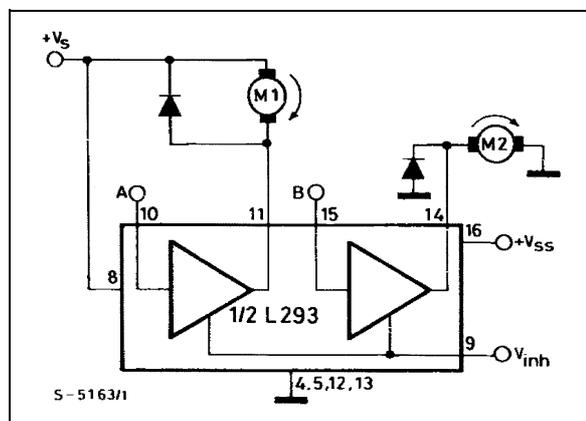


Figure 7. Output Voltage versus Inhibit Voltage



APPLICATION INFORMATION

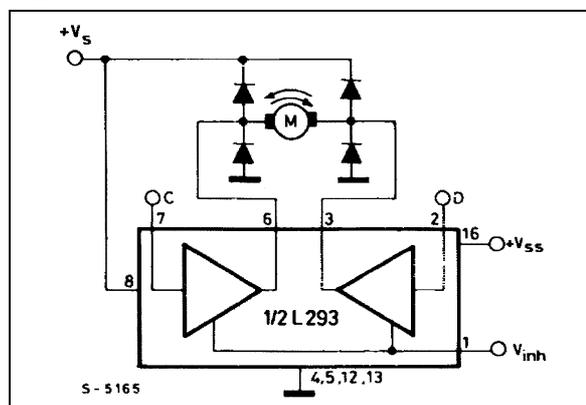
Figure 8. DC Motor Controls (with connection to ground and to the supply voltage)



V_{inh}	A	M1	B	M2
H	H	Fast Motor Stop	H	Run
H	L	Run	L	Fast Motor Stop
L	X	Free Running	X	Free Running
		Motor Stop		Motor Stop

L = Low H = High X = Don't Care

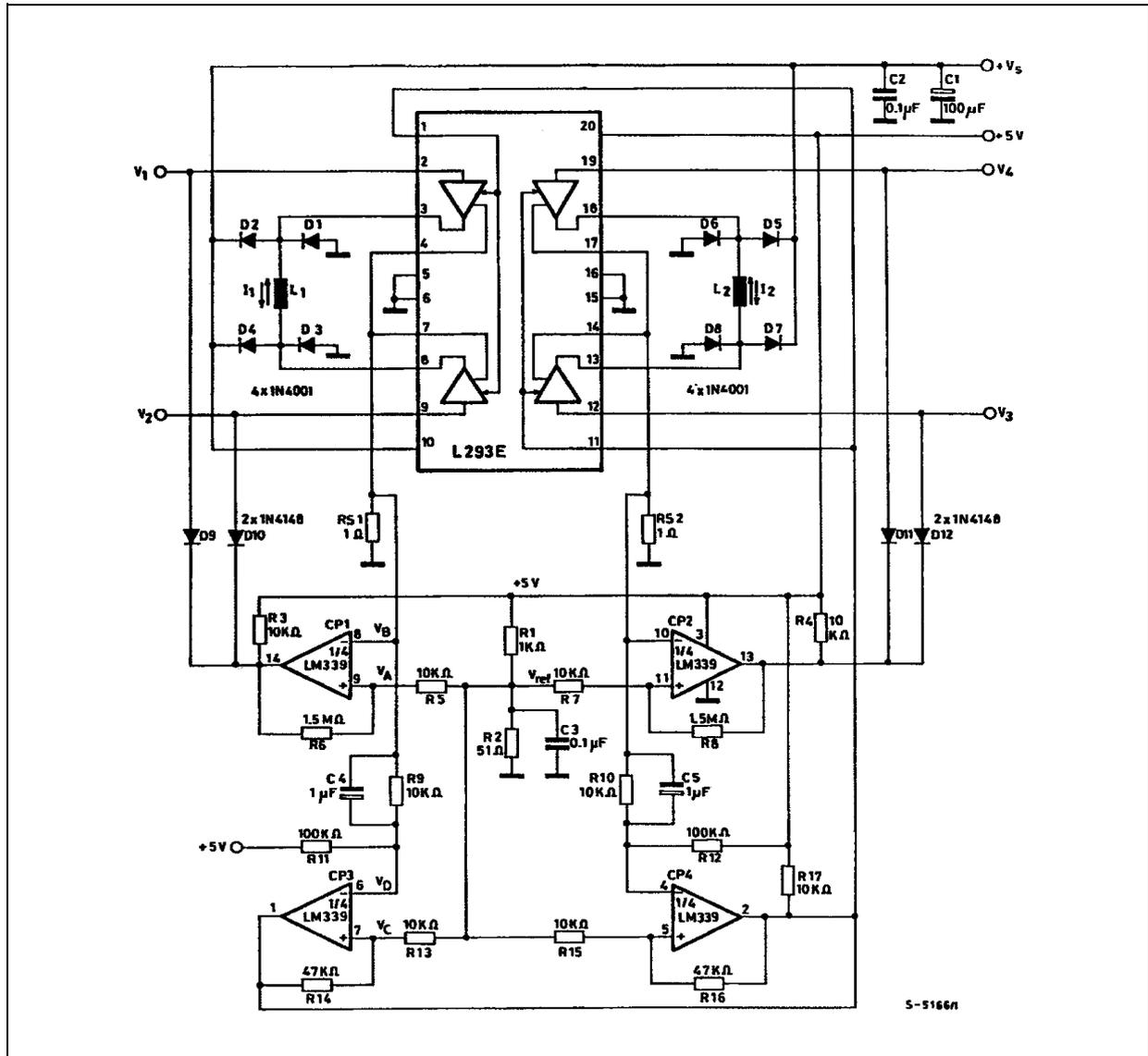
Figure 9. Bidirectional DC Motor Control



Inputs	Function	
$V_{inh} = H$	C = H ; D = L	Turn Right
	C = L ; D = H	Turn Left
	C = D	Fast Motor Stop
$V_{inh} = L$	C = X ; D = X	Free Running Motor Stop

L = Low H = High X = Don't Care

Figure 11. Stepping Motor Driver with Phase Current Control and Short Circuit Protection



MOUNTING INSTRUCTIONS

The $R_{thj-amb}$ of the L293B and the L293E can be reduced by soldering the GND pins to a suitable copper area of the printed circuit board as shown in figure 12 or to an external heatsink (figure 13).

During soldering the pins temperature must not exceed 260°C and the soldering time must not be longer than 12 seconds.

The external heatsink or printed circuit copper area must be connected to electrical ground.

Figure 12. Example of P.C. Board Copper Area which is Used as Heatsink

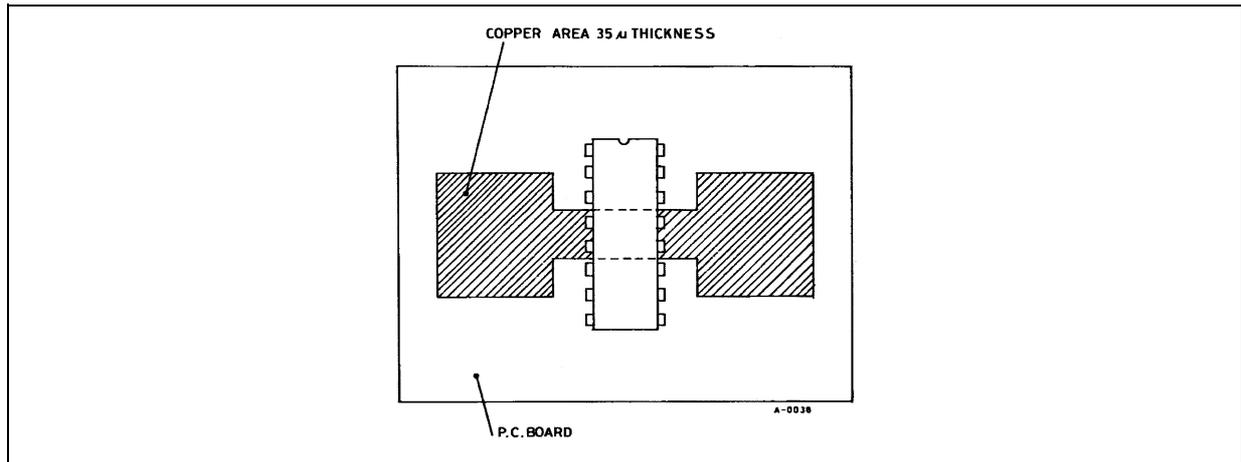
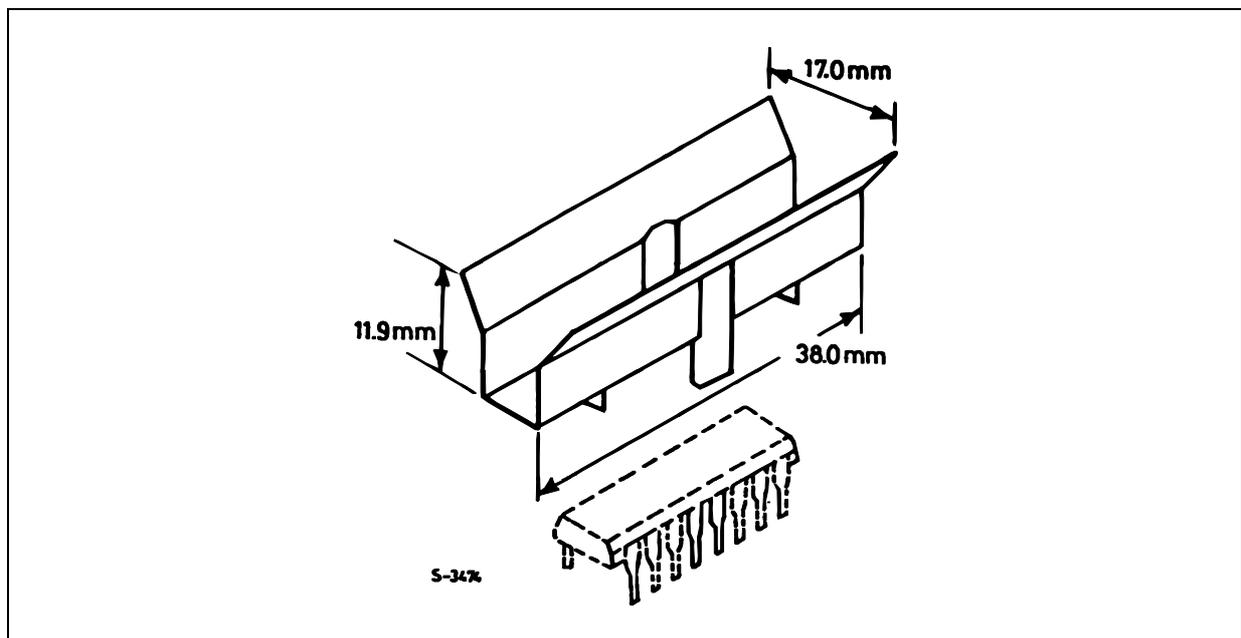
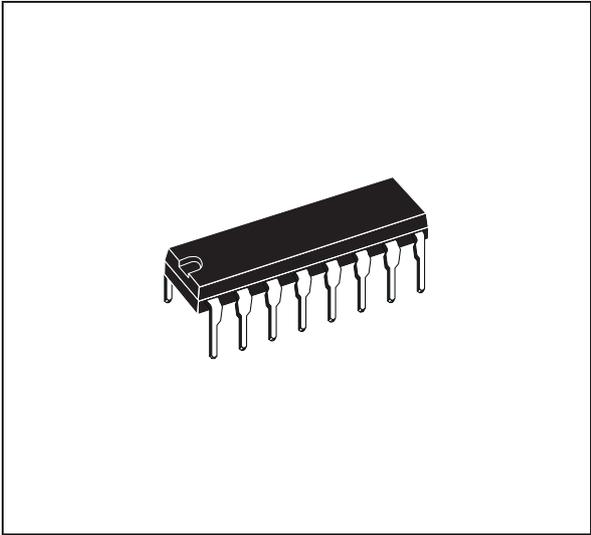


Figure 13. External Heatsink Mounting Example ($R_{th} = 30^{\circ}\text{C/W}$)

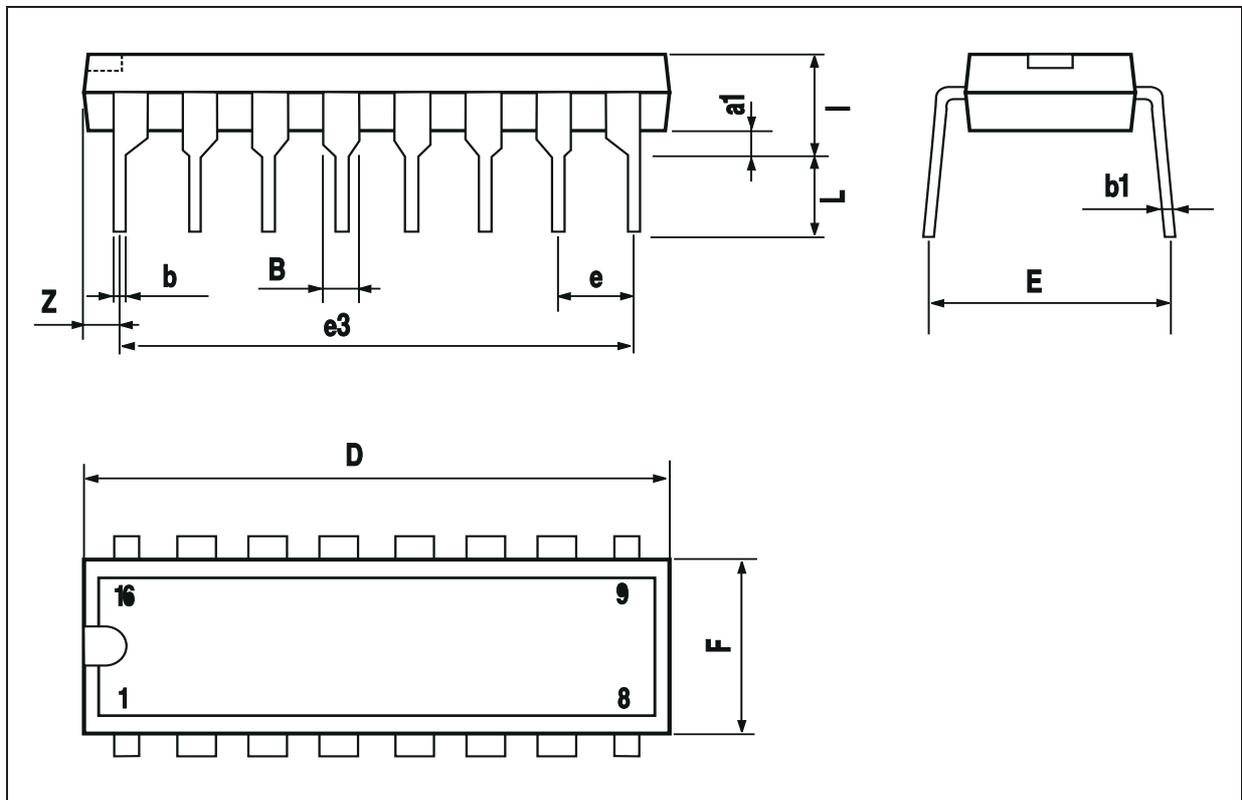


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.77		1.65	0.030		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
E		8.5			0.335	
e		2.54			0.100	
e3		17.78			0.700	
F			7.1			0.280
I			5.1			0.201
L		3.3			0.130	
Z			1.27			0.050

OUTLINE AND MECHANICAL DATA

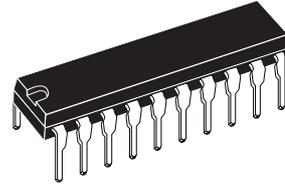


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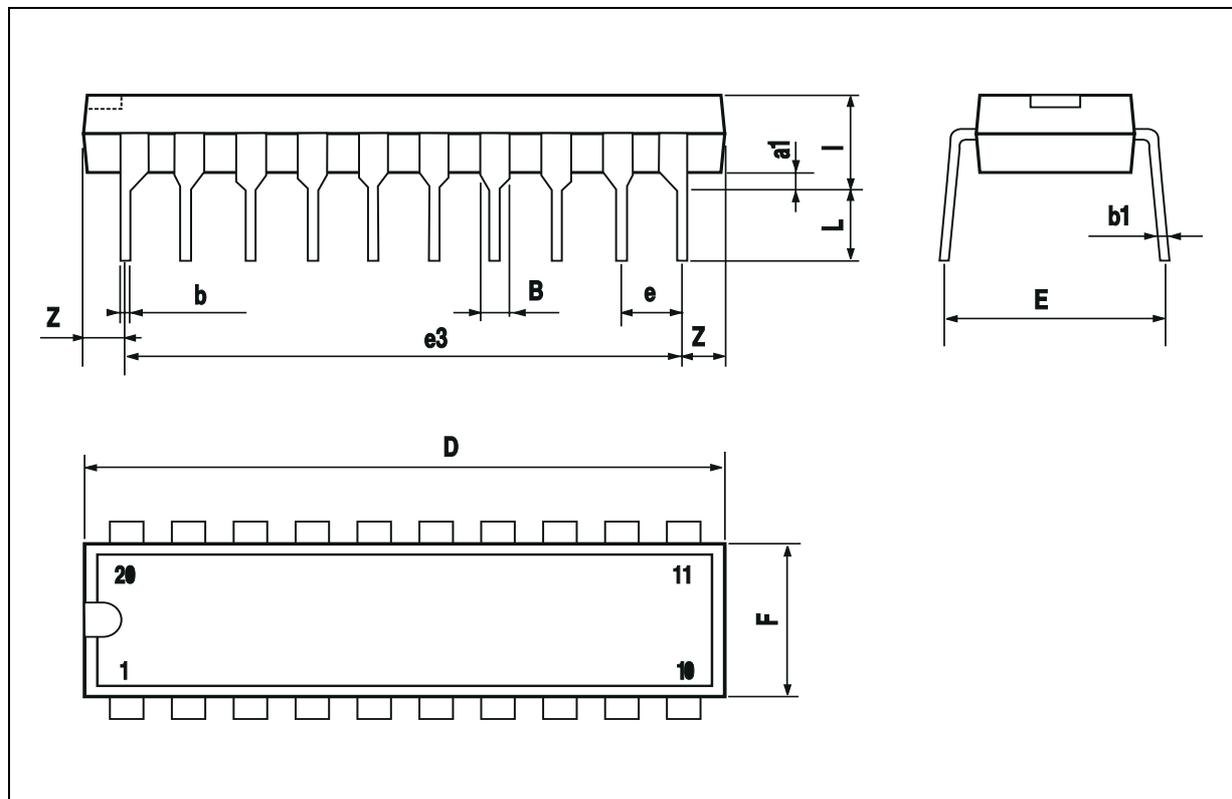


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.85		1.40	0.033		0.055
b		0.50			0.020	
b1	0.38		0.50	0.015		0.020
D			24.80			0.976
E		8.80			0.346	
e		2.54			0.100	
e3		22.86			0.900	
F			7.10			0.280
I			5.10			0.201
L		3.30			0.130	
Z			1.27			0.050

OUTLINE AND MECHANICAL DATA



Powerdip 20



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PUSH-PULL FOUR CHANNEL DRIVER WITH DIODES

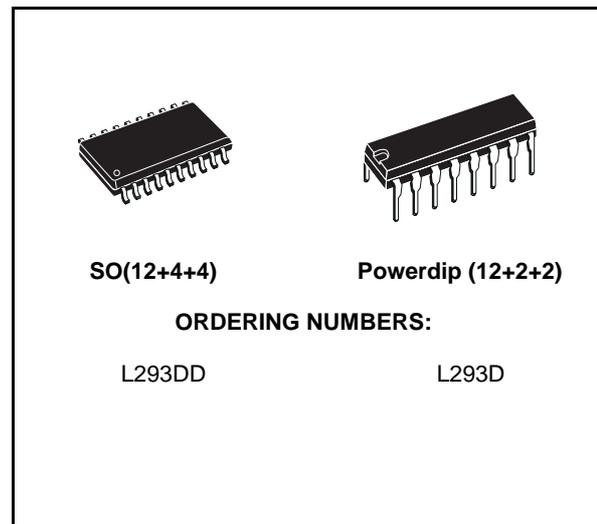
- 600mA OUTPUT CURRENT CAPABILITY PER CHANNEL
- 1.2A PEAK OUTPUT CURRENT (non repetitive) PER CHANNEL
- ENABLE FACILITY
- OVERTEMPERATURE PROTECTION
- LOGICAL "0" INPUT VOLTAGE UP TO 1.5 V (HIGH NOISE IMMUNITY)
- INTERNAL CLAMP DIODES

DESCRIPTION

The Device is a monolithic integrated high voltage, high current four channel driver designed to accept standard DTL or TTL logic levels and drive inductive loads (such as relays solenoids, DC and stepping motors) and switching power transistors.

To simplify use as two bridges each pair of channels is equipped with an enable input. A separate supply input is provided for the logic, allowing operation at a lower voltage and internal clamp diodes are included.

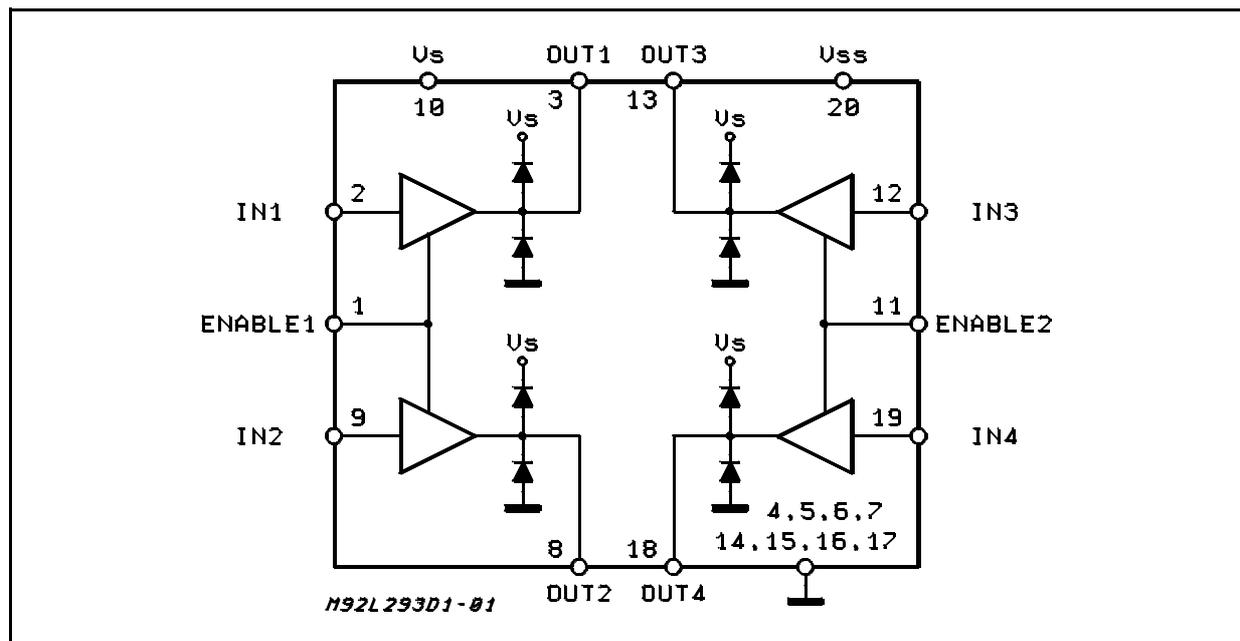
This device is suitable for use in switching applications at frequencies up to 5 kHz.



The L293D is assembled in a 16 lead plastic package which has 4 center pins connected together and used for heatsinking

The L293DD is assembled in a 20 lead surface mount which has 8 center pins connected together and used for heatsinking.

BLOCK DIAGRAM

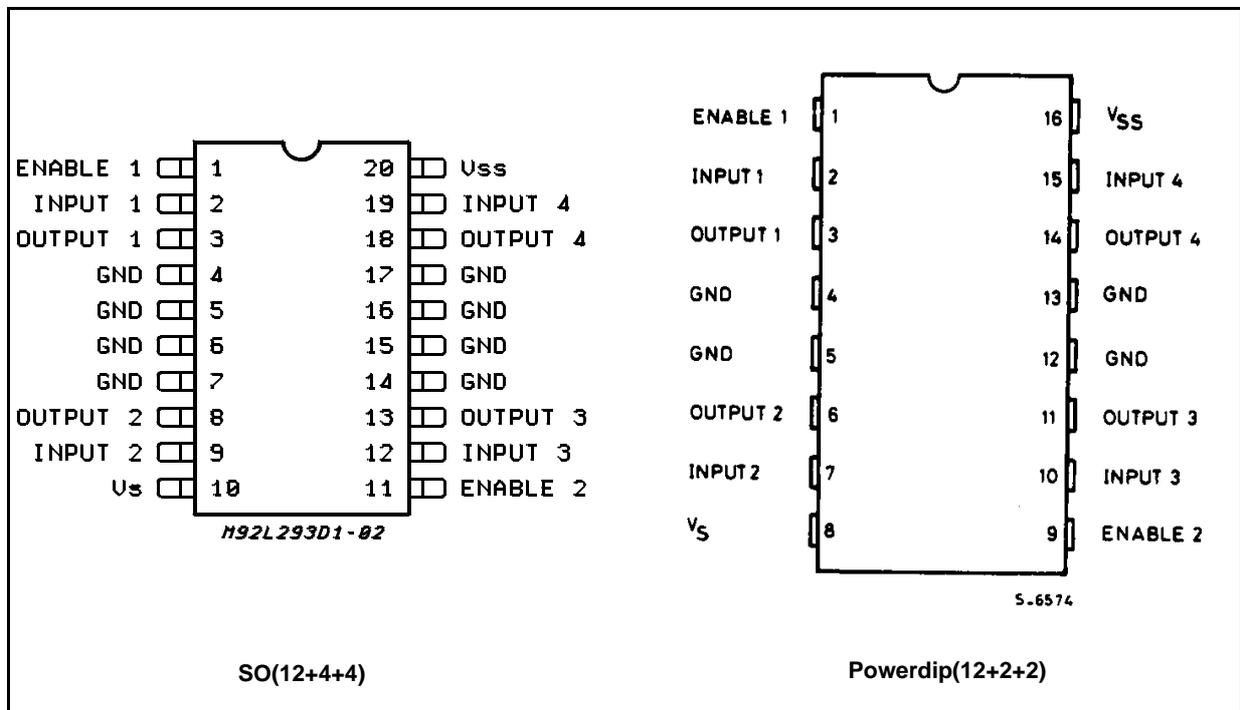


L293D - L293DD

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_S	Supply Voltage	36	V
V_{SS}	Logic Supply Voltage	36	V
V_i	Input Voltage	7	V
V_{en}	Enable Voltage	7	V
I_o	Peak Output Current (100 μ s non repetitive)	1.2	A
P_{tot}	Total Power Dissipation at $T_{pins} = 90$ °C	4	W
T_{stg}, T_j	Storage and Junction Temperature	- 40 to 150	°C

PIN CONNECTIONS (Top view)



THERMAL DATA

Symbol	Description	DIP	SO	Unit
$R_{th\ j-pins}$	Thermal Resistance Junction-pins	max.	14	°C/W
$R_{th\ j-amb}$	Thermal Resistance junction-ambient	max.	50 (*)	°C/W
$R_{th\ j-case}$	Thermal Resistance Junction-case	max.	-	

(*) With 6sq. cm on board heatsink.

ELECTRICAL CHARACTERISTICS (for each channel, $V_S = 24\text{ V}$, $V_{SS} = 5\text{ V}$, $T_{amb} = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_S	Supply Voltage (pin 10)		V_{SS}		36	V
V_{SS}	Logic Supply Voltage (pin 20)		4.5		36	V
I_S	Total Quiescent Supply Current (pin 10)	$V_i = L$; $I_O = 0$; $V_{en} = H$		2	6	mA
		$V_i = H$; $I_O = 0$; $V_{en} = H$		16	24	mA
		$V_{en} = L$			4	mA
I_{SS}	Total Quiescent Logic Supply Current (pin 20)	$V_i = L$; $I_O = 0$; $V_{en} = H$		44	60	mA
		$V_i = H$; $I_O = 0$; $V_{en} = H$		16	22	mA
		$V_{en} = L$		16	24	mA
V_{iL}	Input Low Voltage (pin 2, 9, 12, 19)		-0.3		1.5	V
V_{iH}	Input High Voltage (pin 2, 9, 12, 19)	$V_{SS} \leq 7\text{ V}$	2.3		V_{SS}	V
		$V_{SS} > 7\text{ V}$	2.3		7	V
I_{iL}	Low Voltage Input Current (pin 2, 9, 12, 19)	$V_{iL} = 1.5\text{ V}$			-10	μA
I_{iH}	High Voltage Input Current (pin 2, 9, 12, 19)	$2.3\text{ V} \leq V_{iH} \leq V_{SS} - 0.6\text{ V}$		30	100	μA
V_{enL}	Enable Low Voltage (pin 1, 11)		-0.3		1.5	V
V_{enH}	Enable High Voltage (pin 1, 11)	$V_{SS} \leq 7\text{ V}$	2.3		V_{SS}	V
		$V_{SS} > 7\text{ V}$	2.3		7	V
I_{enL}	Low Voltage Enable Current (pin 1, 11)	$V_{enL} = 1.5\text{ V}$		-30	-100	μA
I_{enH}	High Voltage Enable Current (pin 1, 11)	$2.3\text{ V} \leq V_{enH} \leq V_{SS} - 0.6\text{ V}$			± 10	μA
$V_{CE(sat)H}$	Source Output Saturation Voltage (pins 3, 8, 13, 18)	$I_O = -0.6\text{ A}$		1.4	1.8	V
$V_{CE(sat)L}$	Sink Output Saturation Voltage (pins 3, 8, 13, 18)	$I_O = +0.6\text{ A}$		1.2	1.8	V
V_F	Clamp Diode Forward Voltage	$I_O = 600\text{ nA}$		1.3		V
t_r	Rise Time (*)	0.1 to 0.9 V_O		250		ns
t_f	Fall Time (*)	0.9 to 0.1 V_O		250		ns
t_{on}	Turn-on Delay (*)	0.5 V_i to 0.5 V_O		750		ns
t_{off}	Turn-off Delay (*)	0.5 V_i to 0.5 V_O		200		ns

(*) See fig. 1.

TRUTH TABLE (one channel)

Input	Enable (*)	Output
H	H	H
L	H	L
H	L	Z
L	L	Z

Z = High output impedance

(*) Relative to the considered channel

Figure 1: Switching Times

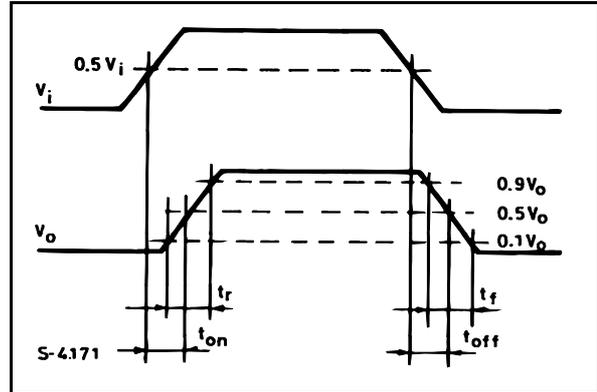
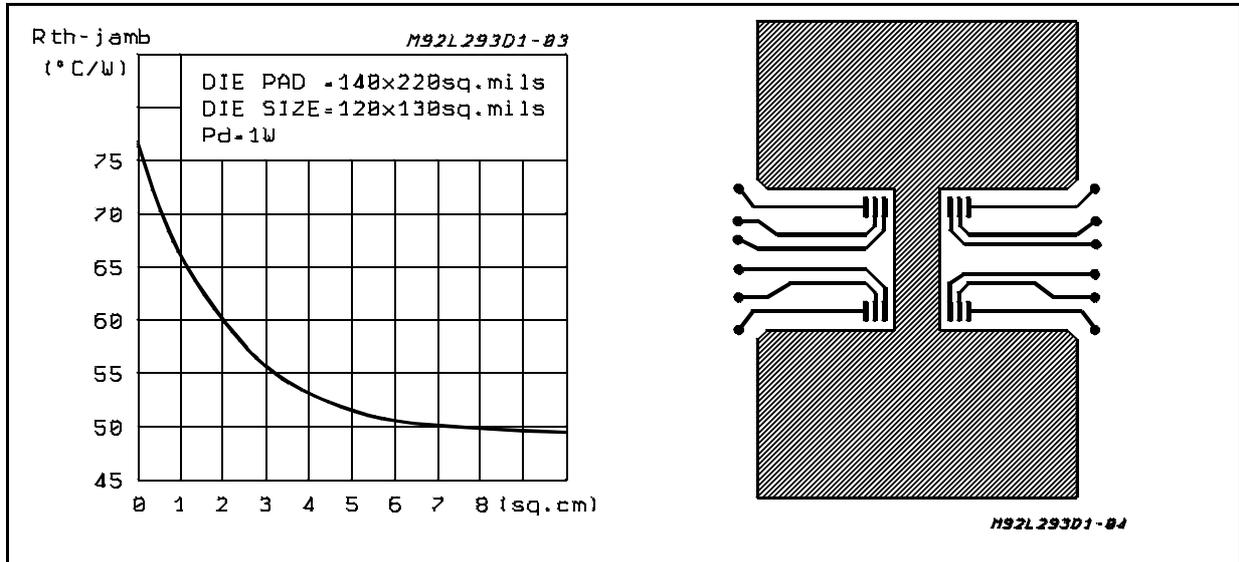
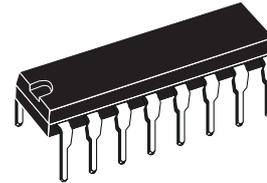


Figure 2: Junction to ambient thermal resistance vs. area on board heatsink (SO12+4+4 package)

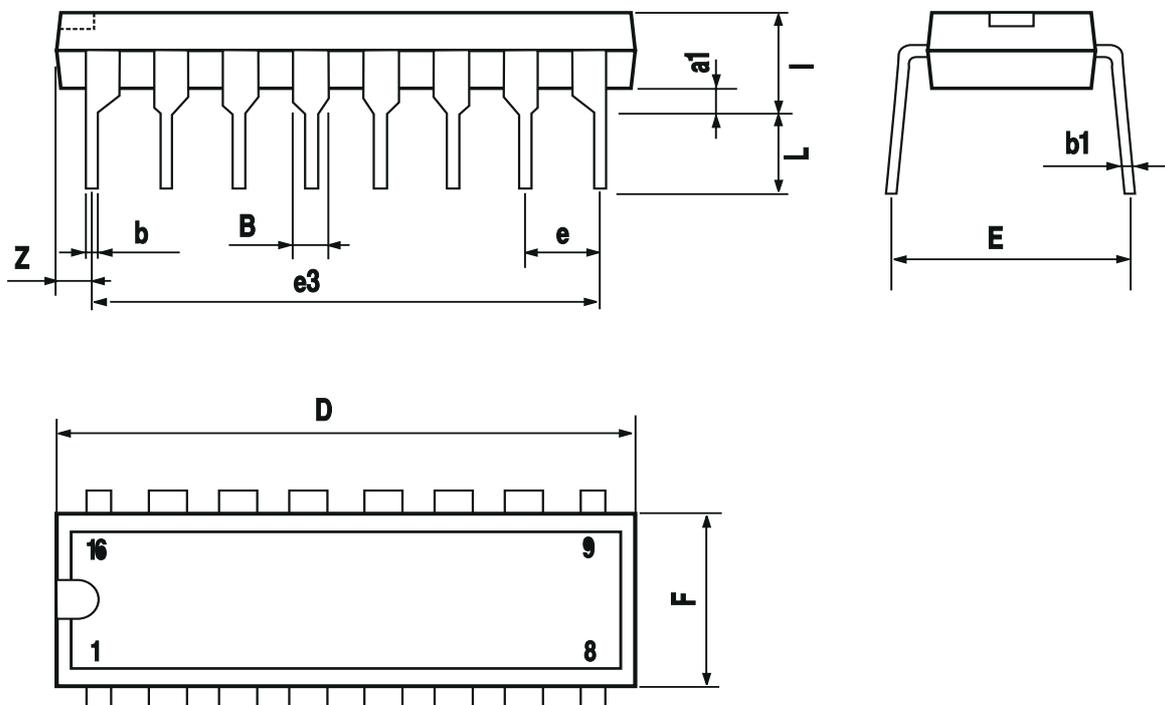


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.85		1.40	0.033		0.055
b		0.50			0.020	
b1	0.38		0.50	0.015		0.020
D			20.0			0.787
E		8.80			0.346	
e		2.54			0.100	
e3		17.78			0.700	
F			7.10			0.280
I			5.10			0.201
L		3.30			0.130	
Z			1.27			0.050

OUTLINE AND MECHANICAL DATA



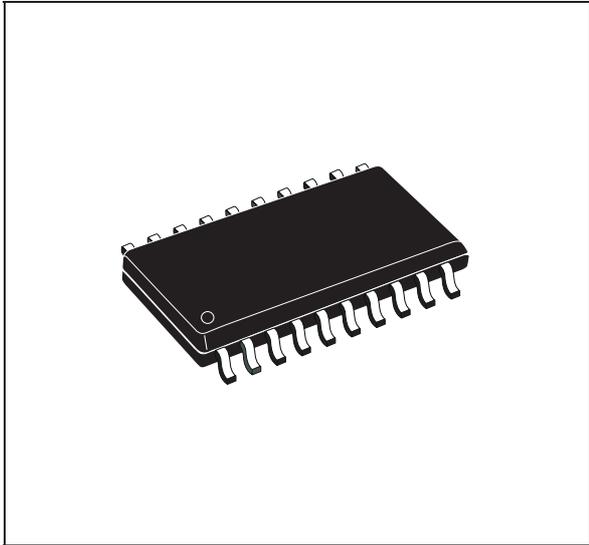
Powerdip 16



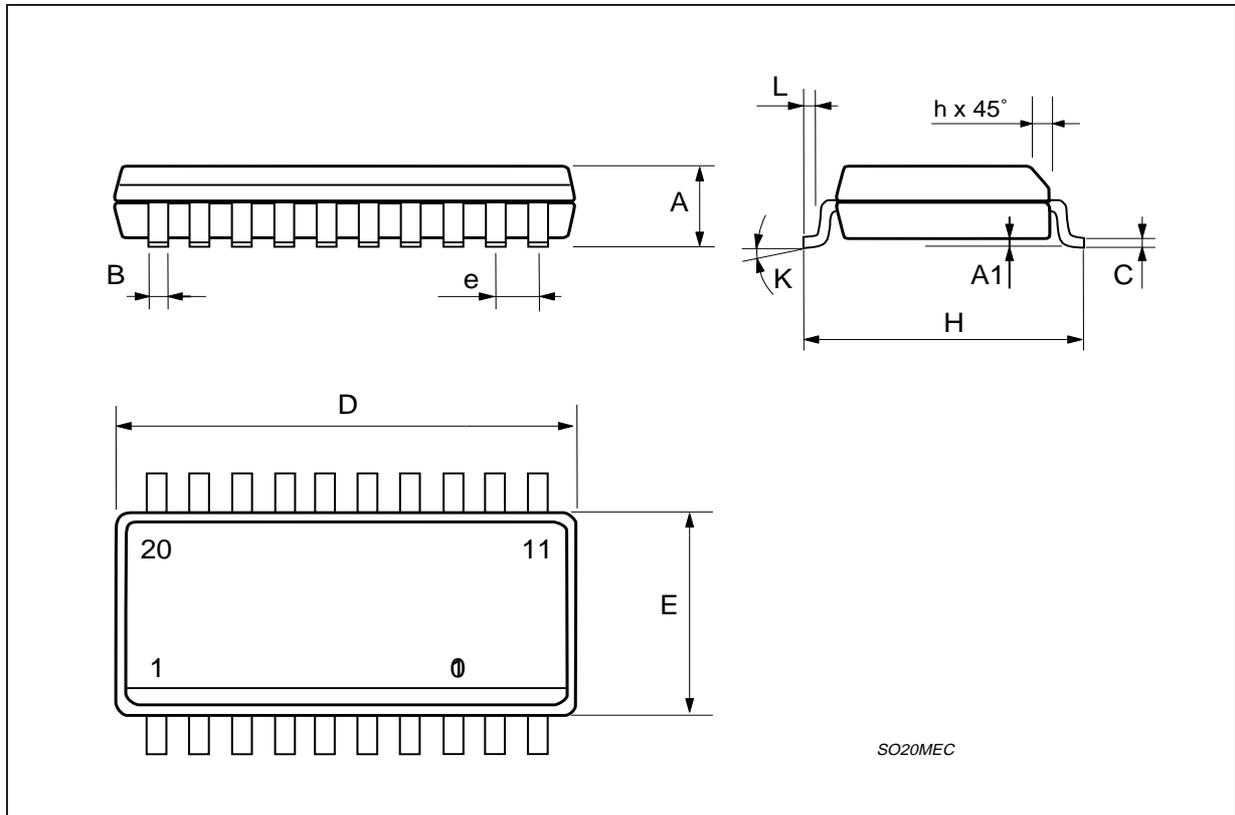
L293D - L293DD

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	2.35		2.65	0.093		0.104
A1	0.1		0.3	0.004		0.012
B	0.33		0.51	0.013		0.020
C	0.23		0.32	0.009		0.013
D	12.6		13	0.496		0.512
E	7.4		7.6	0.291		0.299
e		1.27			0.050	
H	10		10.65	0.394		0.419
h	0.25		0.75	0.010		0.030
L	0.4		1.27	0.016		0.050
K	0° (min.)8° (max.)					

OUTLINE AND MECHANICAL DATA



SO20



SO20MEC

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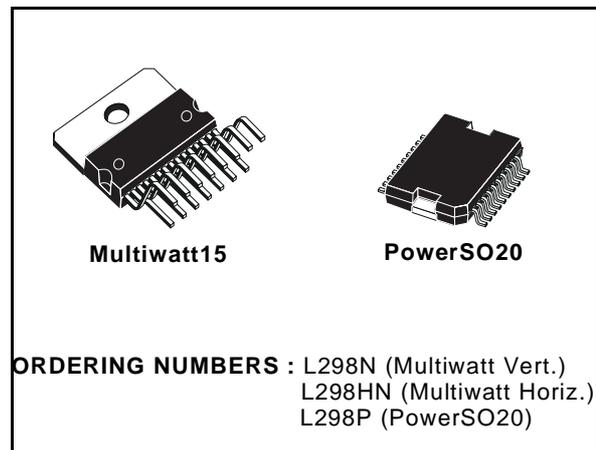
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DUAL FULL-BRIDGE DRIVER

- OPERATING SUPPLY VOLTAGE UP TO 46 V
- TOTAL DC CURRENT UP TO 4 A
- LOW SATURATION VOLTAGE
- OVERTEMPERATURE PROTECTION
- LOGICAL "0" INPUT VOLTAGE UP TO 1.5 V (HIGH NOISE IMMUNITY)

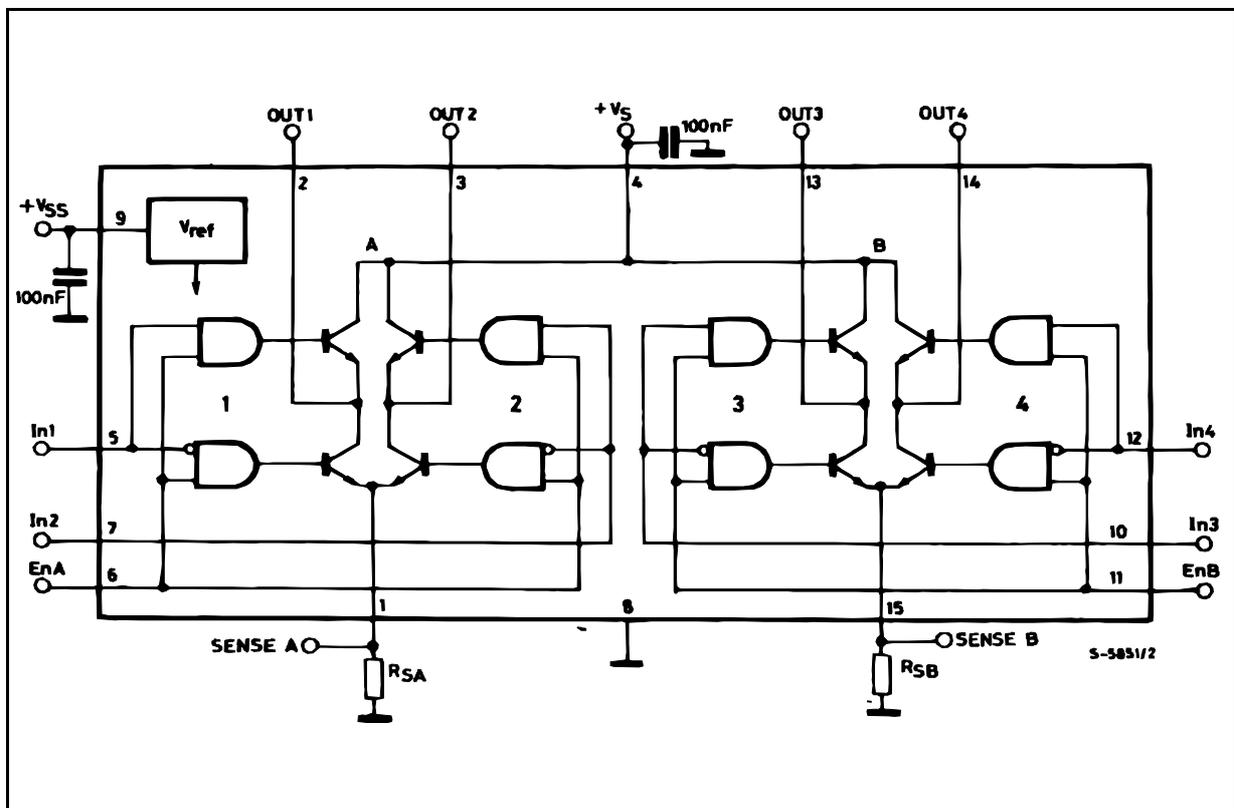
DESCRIPTION

The L298 is an integrated monolithic circuit in a 15-lead Multiwatt and PowerSO20 packages. It is a high voltage, high current dual full-bridge driver designed to accept standard TTL logic levels and drive inductive loads such as relays, solenoids, DC and stepping motors. Two enable inputs are provided to enable or disable the device independently of the input signals. The emitters of the lower transistors of each bridge are connected together and the corresponding external terminal can be used for the con-



nection of an external sensing resistor. An additional supply input is provided so that the logic works at a lower voltage.

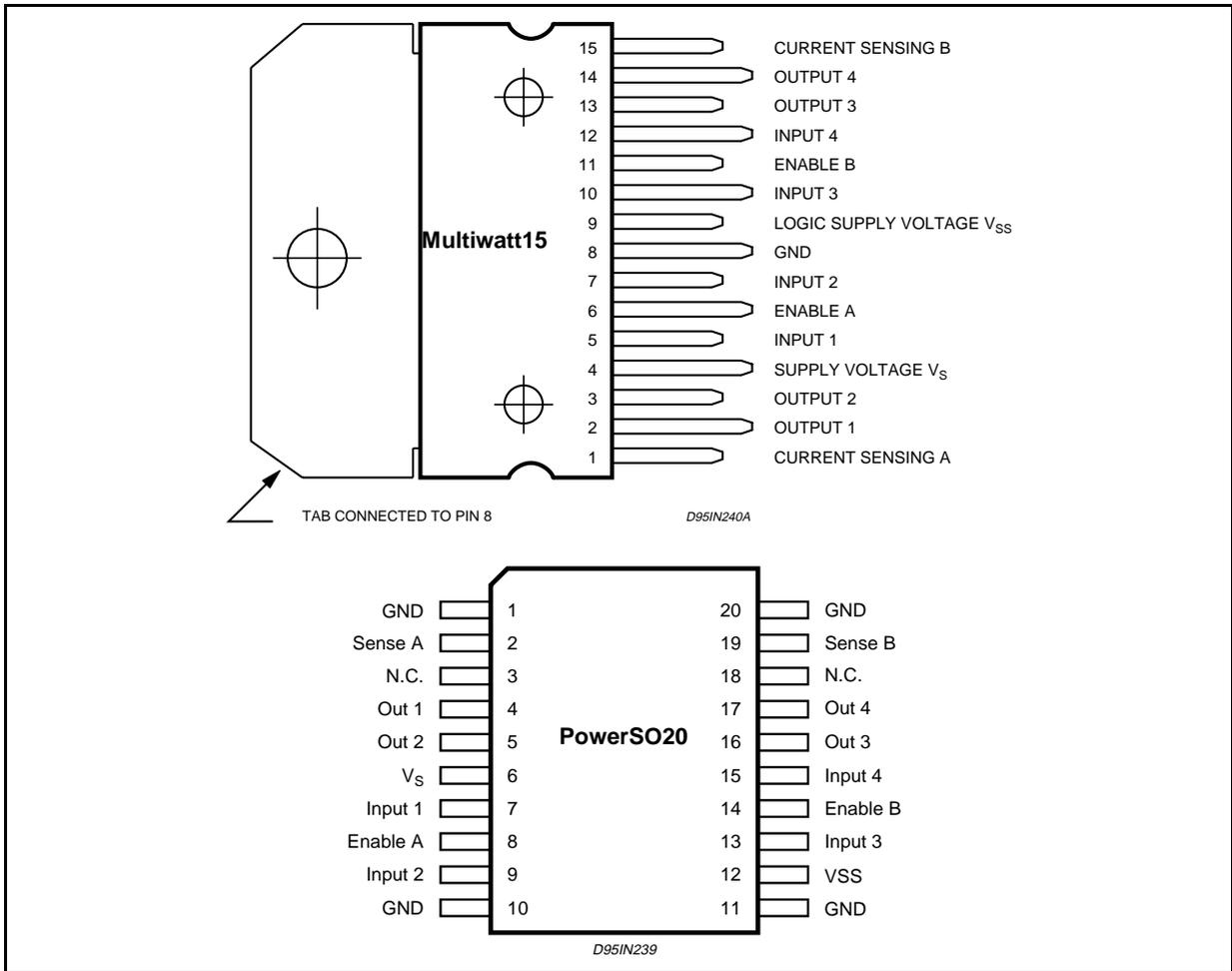
BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_S	Power Supply	50	V
V_{SS}	Logic Supply Voltage	7	V
V_I, V_{En}	Input and Enable Voltage	-0.3 to 7	V
I_O	Peak Output Current (each Channel)		
	- Non Repetitive ($t = 100\mu s$)	3	A
	- Repetitive (80% on -20% off; $t_{on} = 10ms$)	2.5	A
	-DC Operation	2	A
V_{sens}	Sensing Voltage	-1 to 2.3	V
P_{tot}	Total Power Dissipation ($T_{case} = 75^\circ C$)	25	W
T_{op}	Junction Operating Temperature	-25 to 130	$^\circ C$
T_{stg}, T_j	Storage and Junction Temperature	-40 to 150	$^\circ C$

PIN CONNECTIONS (top view)



THERMAL DATA

Symbol	Parameter		PowerSO20	Multiwatt15	Unit
$R_{th j-case}$	Thermal Resistance Junction-case	Max.	-	3	$^\circ C/W$
$R_{th j-amb}$	Thermal Resistance Junction-ambient	Max.	13 (*)	35	$^\circ C/W$

(*) Mounted on aluminum substrate



PIN FUNCTIONS (refer to the block diagram)

MW.15	PowerSO	Name	Function
1;15	2;19	Sense A; Sense B	Between this pin and ground is connected the sense resistor to control the current of the load.
2;3	4;5	Out 1; Out 2	Outputs of the Bridge A; the current that flows through the load connected between these two pins is monitored at pin 1.
4	6	V _s	Supply Voltage for the Power Output Stages. A non-inductive 100nF capacitor must be connected between this pin and ground.
5;7	7;9	Input 1; Input 2	TTL Compatible Inputs of the Bridge A.
6;11	8;14	Enable A; Enable B	TTL Compatible Enable Input: the L state disables the bridge A (enable A) and/or the bridge B (enable B).
8	1,10,11,20	GND	Ground.
9	12	V _{SS}	Supply Voltage for the Logic Blocks. A100nF capacitor must be connected between this pin and ground.
10; 12	13;15	Input 3; Input 4	TTL Compatible Inputs of the Bridge B.
13; 14	16;17	Out 3; Out 4	Outputs of the Bridge B. The current that flows through the load connected between these two pins is monitored at pin 15.
–	3;18	N.C.	Not Connected

ELECTRICAL CHARACTERISTICS (V_S = 42V; V_{SS} = 5V, T_j = 25°C; unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _S	Supply Voltage (pin 4)	Operative Condition	V _{IH} +2.5		46	V
V _{SS}	Logic Supply Voltage (pin 9)		4.5	5	7	V
I _S	Quiescent Supply Current (pin 4)	V _{en} = H; I _L = 0 V _i = L V _i = H		13 50	22 70	mA mA
		V _{en} = L V _i = X			4	mA
I _{SS}	Quiescent Current from V _{SS} (pin 9)	V _{en} = H; I _L = 0 V _i = L V _i = H		24 7	36 12	mA mA
		V _{en} = L V _i = X			6	mA
V _{iL}	Input Low Voltage (pins 5, 7, 10, 12)		–0.3		1.5	V
V _{iH}	Input High Voltage (pins 5, 7, 10, 12)		2.3		V _{SS}	V
I _{iL}	Low Voltage Input Current (pins 5, 7, 10, 12)	V _i = L			–10	μA
I _{iH}	High Voltage Input Current (pins 5, 7, 10, 12)	V _i = H ≤ V _{SS} – 0.6V		30	100	μA
V _{en} = L	Enable Low Voltage (pins 6, 11)		–0.3		1.5	V
V _{en} = H	Enable High Voltage (pins 6, 11)		2.3		V _{SS}	V
I _{en} = L	Low Voltage Enable Current (pins 6, 11)	V _{en} = L			–10	μA
I _{en} = H	High Voltage Enable Current (pins 6, 11)	V _{en} = H ≤ V _{SS} – 0.6V		30	100	μA
V _{CEsat} (H)	Source Saturation Voltage	I _L = 1A I _L = 2A	0.95	1.35 2	1.7 2.7	V V
V _{CEsat} (L)	Sink Saturation Voltage	I _L = 1A (5) I _L = 2A (5)	0.85	1.2 1.7	1.6 2.3	V V
V _{CEsat}	Total Drop	I _L = 1A (5) I _L = 2A (5)	1.80		3.2 4.9	V V
V _{sens}	Sensing Voltage (pins 1, 15)		–1 (1)		2	V

ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
T ₁ (V _i)	Source Current Turn-off Delay	0.5 V _i to 0.9 I _L (2); (4)		1.5		μs
T ₂ (V _i)	Source Current Fall Time	0.9 I _L to 0.1 I _L (2); (4)		0.2		μs
T ₃ (V _i)	Source Current Turn-on Delay	0.5 V _i to 0.1 I _L (2); (4)		2		μs
T ₄ (V _i)	Source Current Rise Time	0.1 I _L to 0.9 I _L (2); (4)		0.7		μs
T ₅ (V _i)	Sink Current Turn-off Delay	0.5 V _i to 0.9 I _L (3); (4)		0.7		μs
T ₆ (V _i)	Sink Current Fall Time	0.9 I _L to 0.1 I _L (3); (4)		0.25		μs
T ₇ (V _i)	Sink Current Turn-on Delay	0.5 V _i to 0.9 I _L (3); (4)		1.6		μs
T ₈ (V _i)	Sink Current Rise Time	0.1 I _L to 0.9 I _L (3); (4)		0.2		μs
f _c (V _i)	Commutation Frequency	I _L = 2A		25	40	KHz
T ₁ (V _{en})	Source Current Turn-off Delay	0.5 V _{en} to 0.9 I _L (2); (4)		3		μs
T ₂ (V _{en})	Source Current Fall Time	0.9 I _L to 0.1 I _L (2); (4)		1		μs
T ₃ (V _{en})	Source Current Turn-on Delay	0.5 V _{en} to 0.1 I _L (2); (4)		0.3		μs
T ₄ (V _{en})	Source Current Rise Time	0.1 I _L to 0.9 I _L (2); (4)		0.4		μs
T ₅ (V _{en})	Sink Current Turn-off Delay	0.5 V _{en} to 0.9 I _L (3); (4)		2.2		μs
T ₆ (V _{en})	Sink Current Fall Time	0.9 I _L to 0.1 I _L (3); (4)		0.35		μs
T ₇ (V _{en})	Sink Current Turn-on Delay	0.5 V _{en} to 0.9 I _L (3); (4)		0.25		μs
T ₈ (V _{en})	Sink Current Rise Time	0.1 I _L to 0.9 I _L (3); (4)		0.1		μs

- 1) Sensing voltage can be -1 V for t ≤ 50 μsec; in steady state V_{sens} min ≥ -0.5 V.
- 2) See fig. 2.
- 3) See fig. 4.
- 4) The load must be a pure resistor.

Figure 1 : Typical Saturation Voltage vs. Output Current.

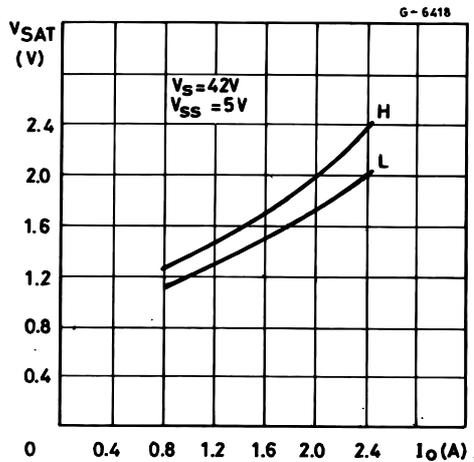
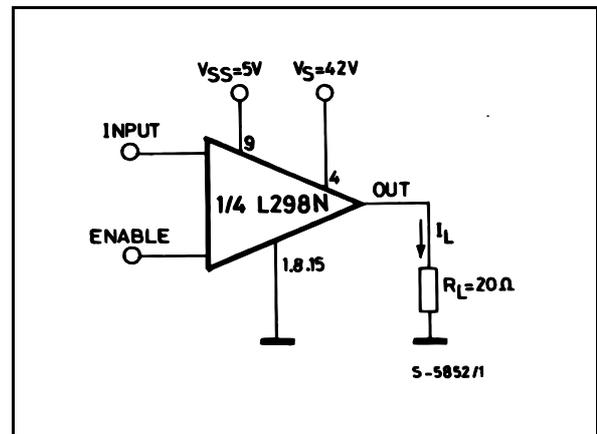


Figure 2 : Switching Times Test Circuits.



Note : For INPUT Switching, set EN = H
For ENABLE Switching, set IN = H

Figure 3 : Source Current Delay Times vs. Input or Enable Switching.

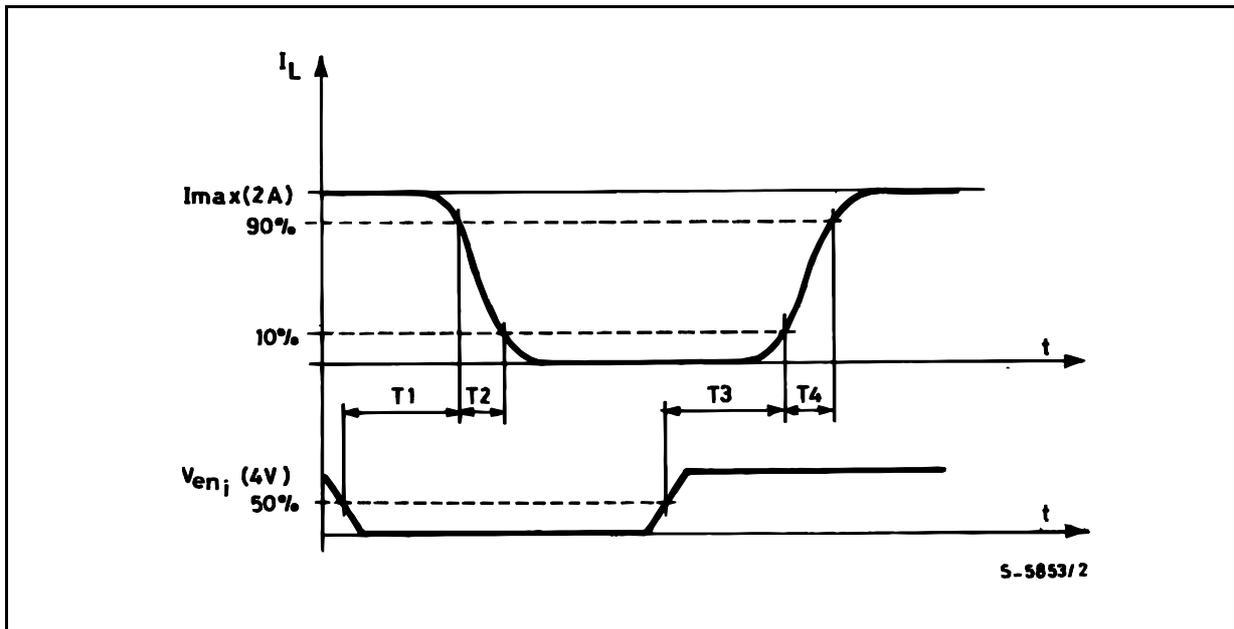
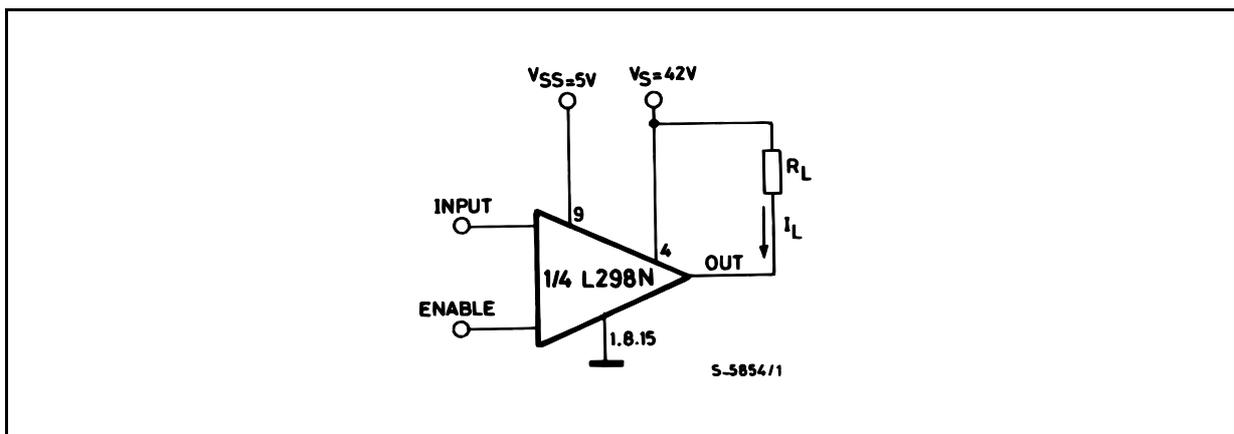


Figure 4 : Switching Times Test Circuits.



Note : For INPUT Switching, set EN = H
For ENABLE Switching, set IN = L

Figure 5 : Sink Current Delay Times vs. Input 0 V Enable Switching.

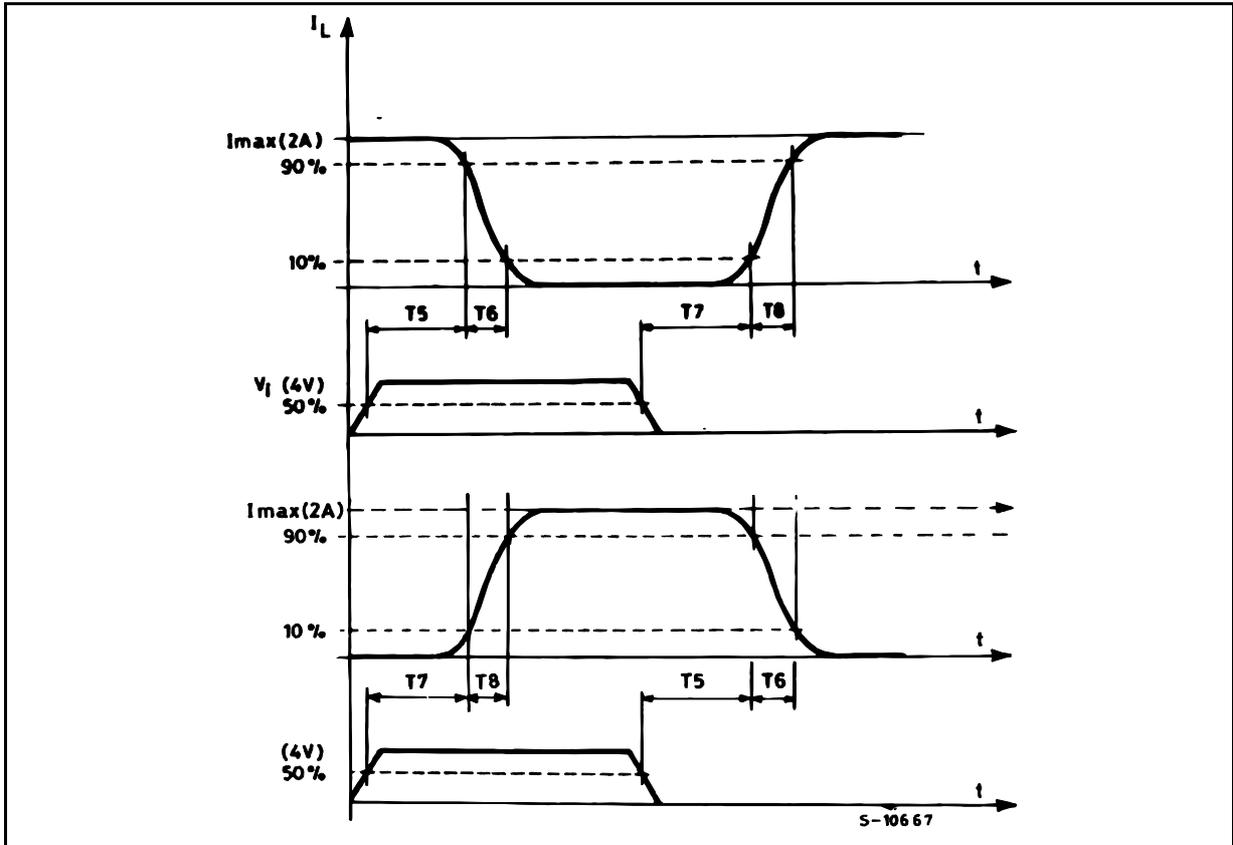


Figure 6 : Bidirectional DC Motor Control.

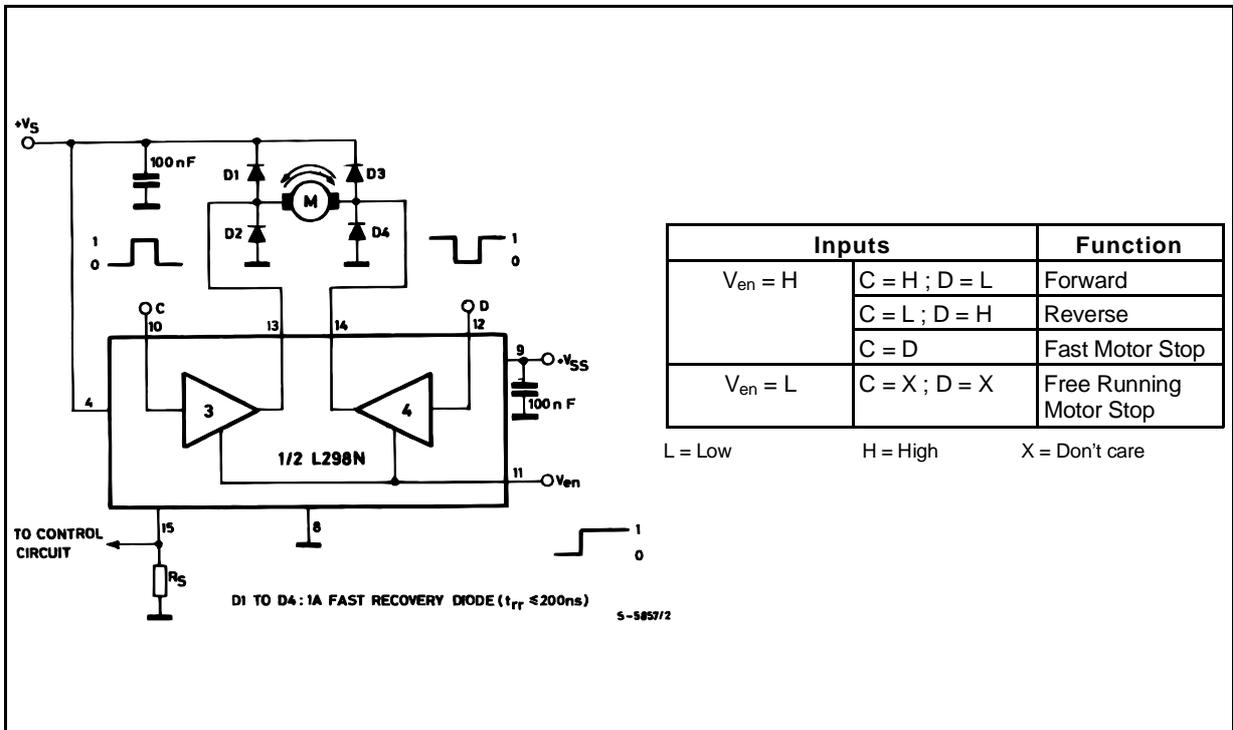
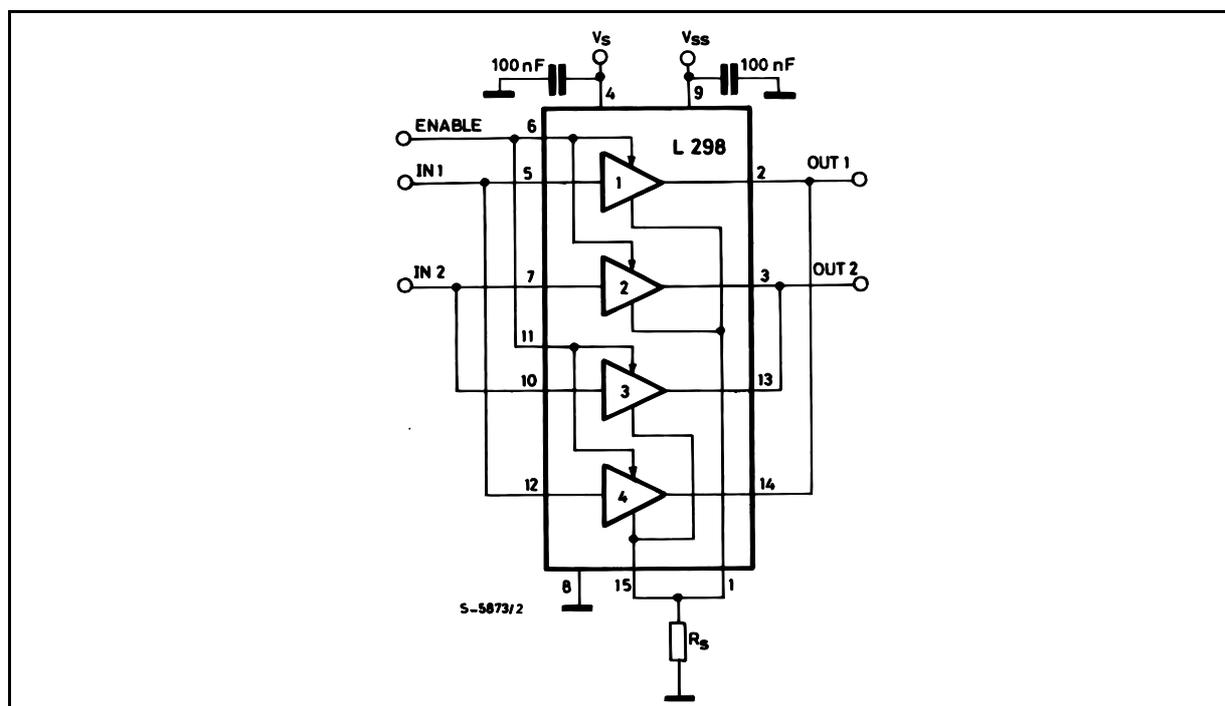


Figure 7 : For higher currents, outputs can be paralleled. Take care to parallel channel 1 with channel 4 and channel 2 with channel 3.



APPLICATION INFORMATION (Refer to the block diagram)

1.1. POWER OUTPUT STAGE

The L298 integrates two power output stages (A ; B). The power output stage is a bridge configuration and its outputs can drive an inductive load in common or differenzial mode, depending on the state of the inputs. The current that flows through the load comes out from the bridge at the sense output : an external resistor (R_{SA} ; R_{SB} .) allows to detect the intensity of this current.

1.2. INPUT STAGE

Each bridge is driven by means of four gates the input of which are In1 ; In2 ; EnA and In3 ; In4 ; EnB. The In inputs set the bridge state when The En input is high ; a low state of the En input inhibits the bridge. All the inputs are TTL compatible.

2. SUGGESTIONS

A non inductive capacitor, usually of 100 nF, must be foreseen between both Vs and Vss, to ground, as near as possible to GND pin. When the large capacitor of the power supply is too far from the IC, a second smaller one must be foreseen near the L298.

The sense resistor, not of a wire wound type, must be grounded near the negative pole of Vs that must be near the GND pin of the I.C.

Each input must be connected to the source of the driving signals by means of a very short path.

Turn-On and Turn-Off : Before to Turn-ON the Supply Voltage and before to Turn it OFF, the Enable input must be driven to the Low state.

3. APPLICATIONS

Fig 6 shows a bidirectional DC motor control Schematic Diagram for which only one bridge is needed. The external bridge of diodes D1 to D4 is made by four fast recovery elements ($t_{tr} \leq 200$ nsec) that must be chosen of a VF as low as possible at the worst case of the load current.

The sense output voltage can be used to control the current amplitude by chopping the inputs, or to provide overcurrent protection by switching low the enable input.

The brake function (Fast motor stop) requires that the Absolute Maximum Rating of 2 Amps must never be overcome.

When the repetitive peak current needed from the load is higher than 2 Amps, a paralleled configuration can be chosen (See Fig.7).

An external bridge of diodes are required when inductive loads are driven and when the inputs of the IC are chopped ; Schottky diodes would be preferred.

This solution can drive until 3 Amps In DC operation and until 3.5 Amps of a repetitive peak current.

On Fig 8 it is shown the driving of a two phase bipolar stepper motor ; the needed signals to drive the inputs of the L298 are generated, in this example, from the IC L297.

Fig 9 shows an example of P.C.B. designed for the application of Fig 8.

Figure 8 : Two Phase Bipolar Stepper Motor Circuit.

This circuit drives bipolar stepper motors with winding currents up to 2 A. The diodes are fast 2 A types.

Fig 10 shows a second two phase bipolar stepper motor control circuit where the current is controlled by the I.C. L6506.

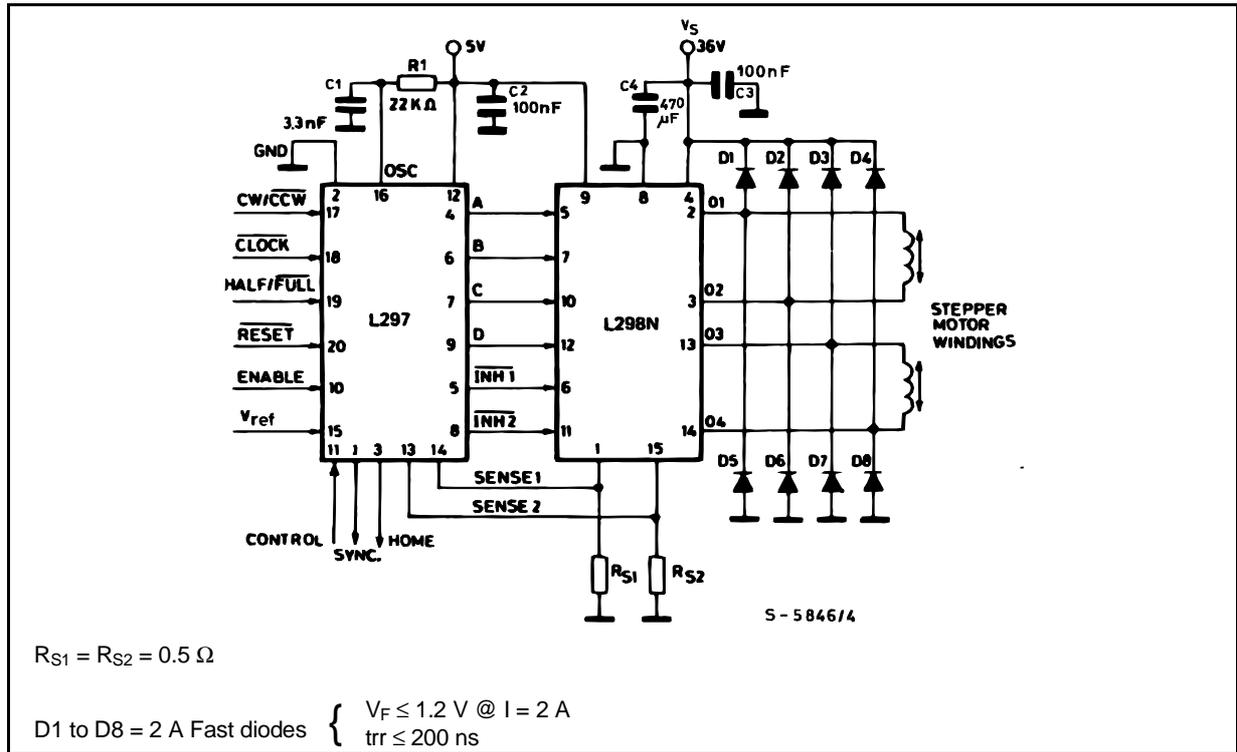


Figure 9 : Suggested Printed Circuit Board Layout for the Circuit of fig. 8 (1:1 scale).

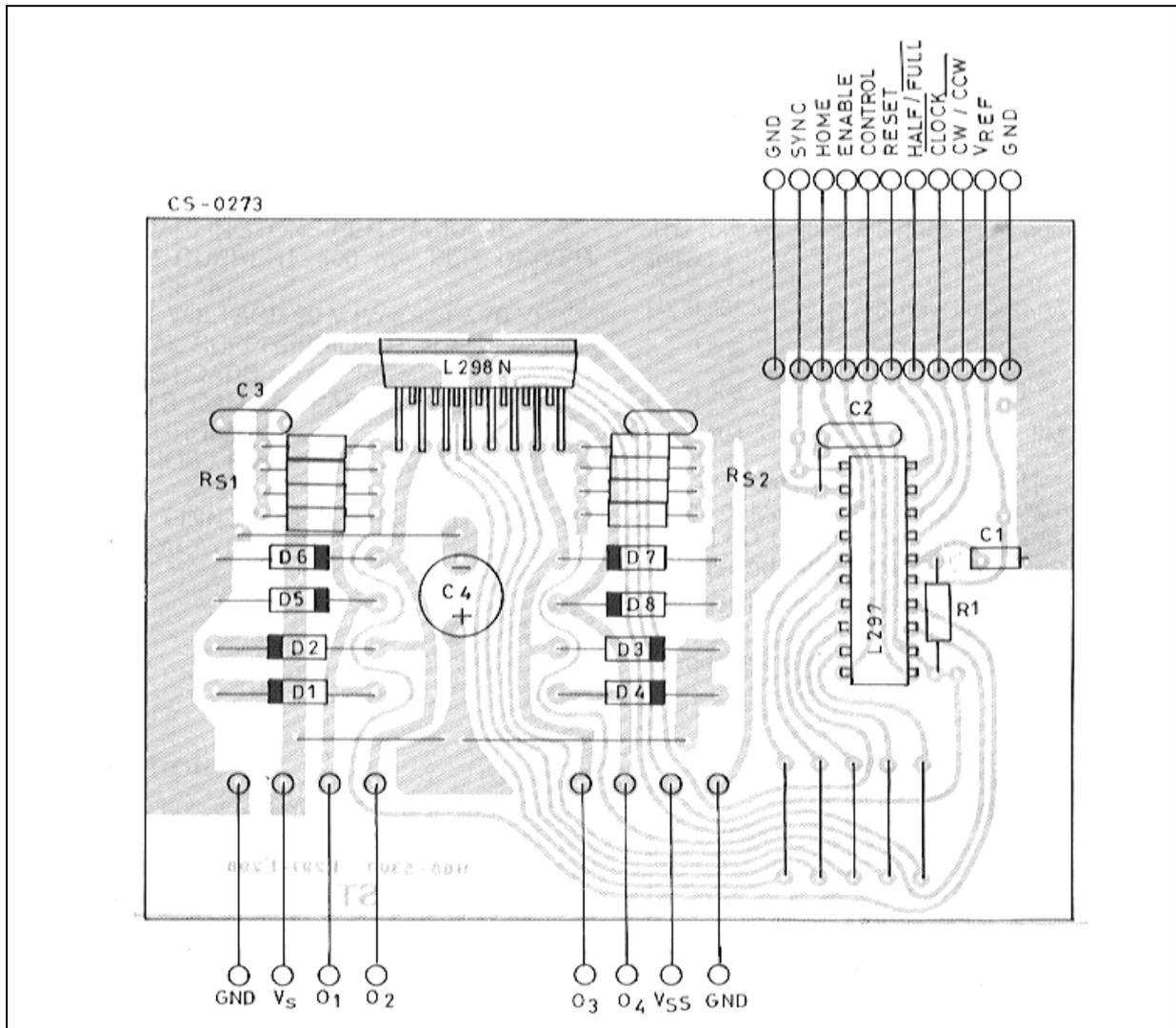
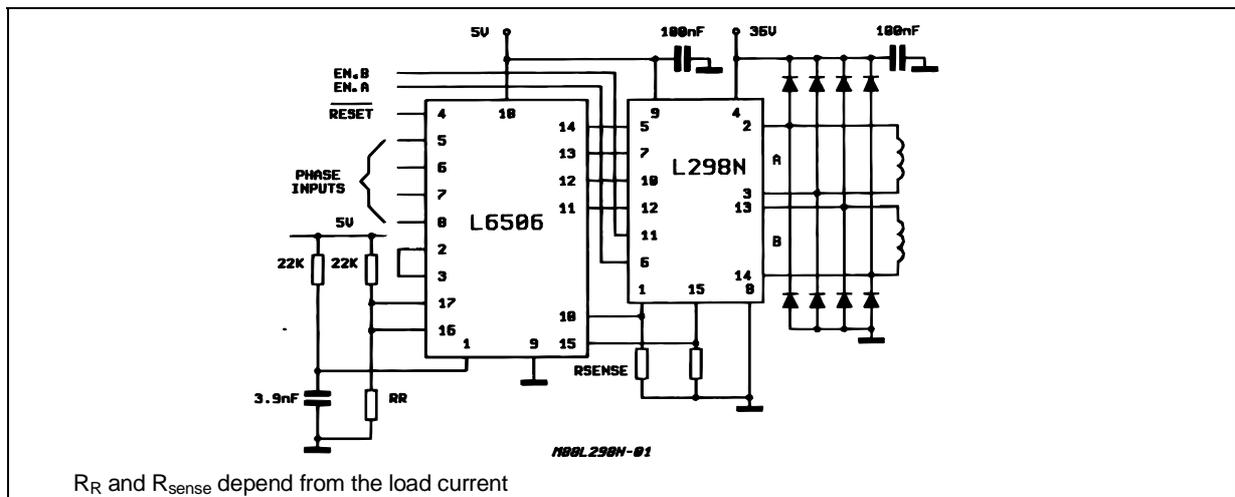
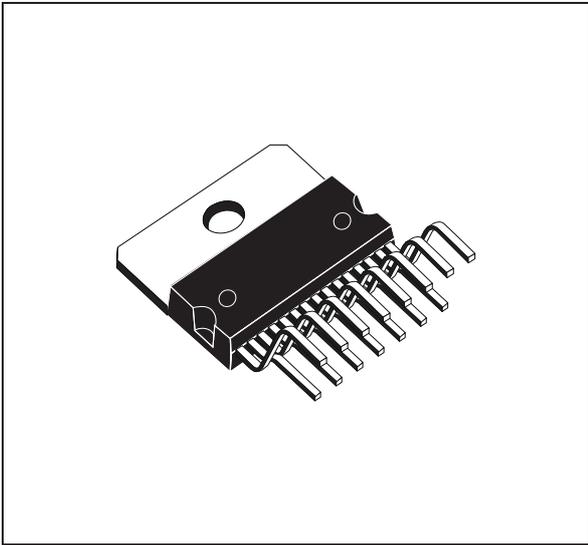


Figure 10 : Two Phase Bipolar Stepper Motor Control Circuit by Using the Current Controller L6506.

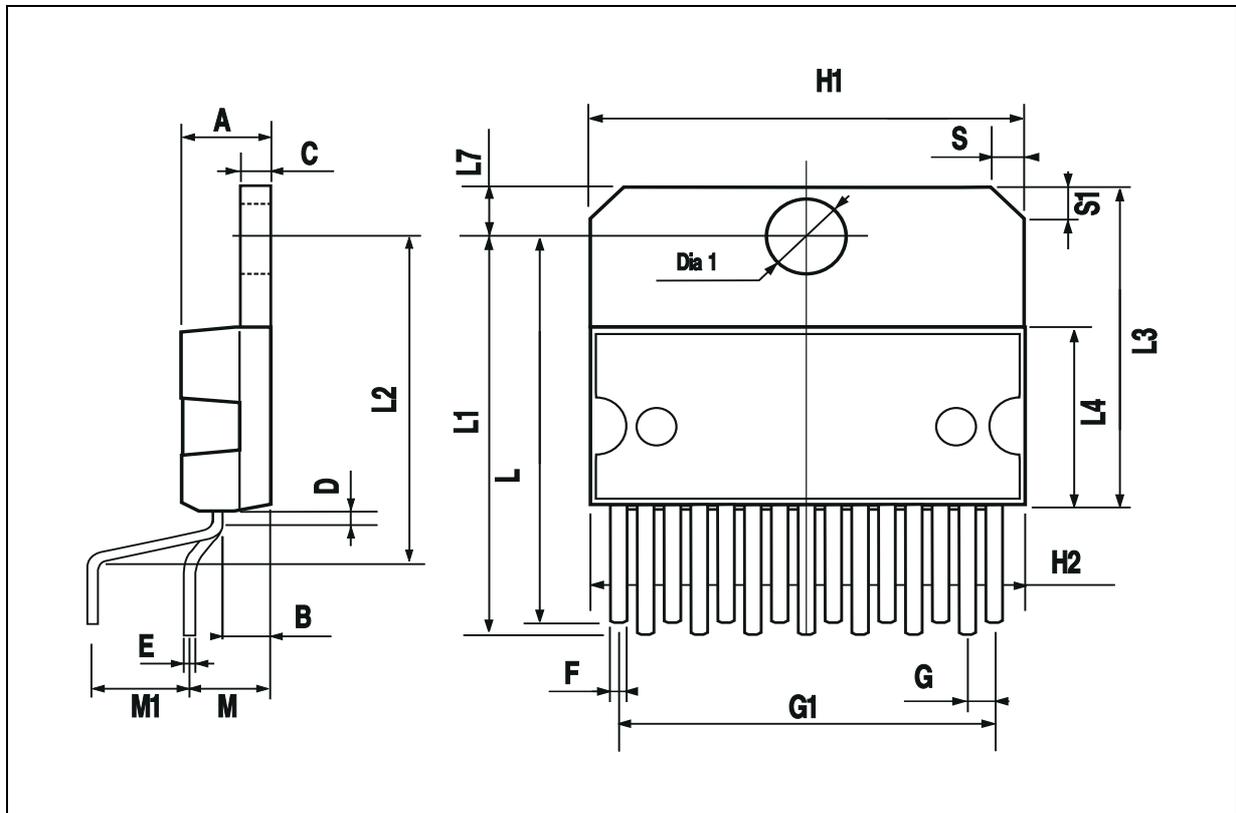


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			5			0.197
B			2.65			0.104
C			1.6			0.063
D		1			0.039	
E	0.49		0.55	0.019		0.022
F	0.66		0.75	0.026		0.030
G	1.02	1.27	1.52	0.040	0.050	0.060
G1	17.53	17.78	18.03	0.690	0.700	0.710
H1	19.6			0.772		
H2			20.2			0.795
L	21.9	22.2	22.5	0.862	0.874	0.886
L1	21.7	22.1	22.5	0.854	0.870	0.886
L2	17.65		18.1	0.695		0.713
L3	17.25	17.5	17.75	0.679	0.689	0.699
L4	10.3	10.7	10.9	0.406	0.421	0.429
L7	2.65		2.9	0.104		0.114
M	4.25	4.55	4.85	0.167	0.179	0.191
M1	4.63	5.08	5.53	0.182	0.200	0.218
S	1.9		2.6	0.075		0.102
S1	1.9		2.6	0.075		0.102
Dia1	3.65		3.85	0.144		0.152

OUTLINE AND MECHANICAL DATA

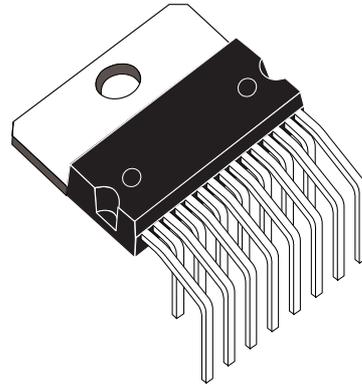


Multiwatt15 V

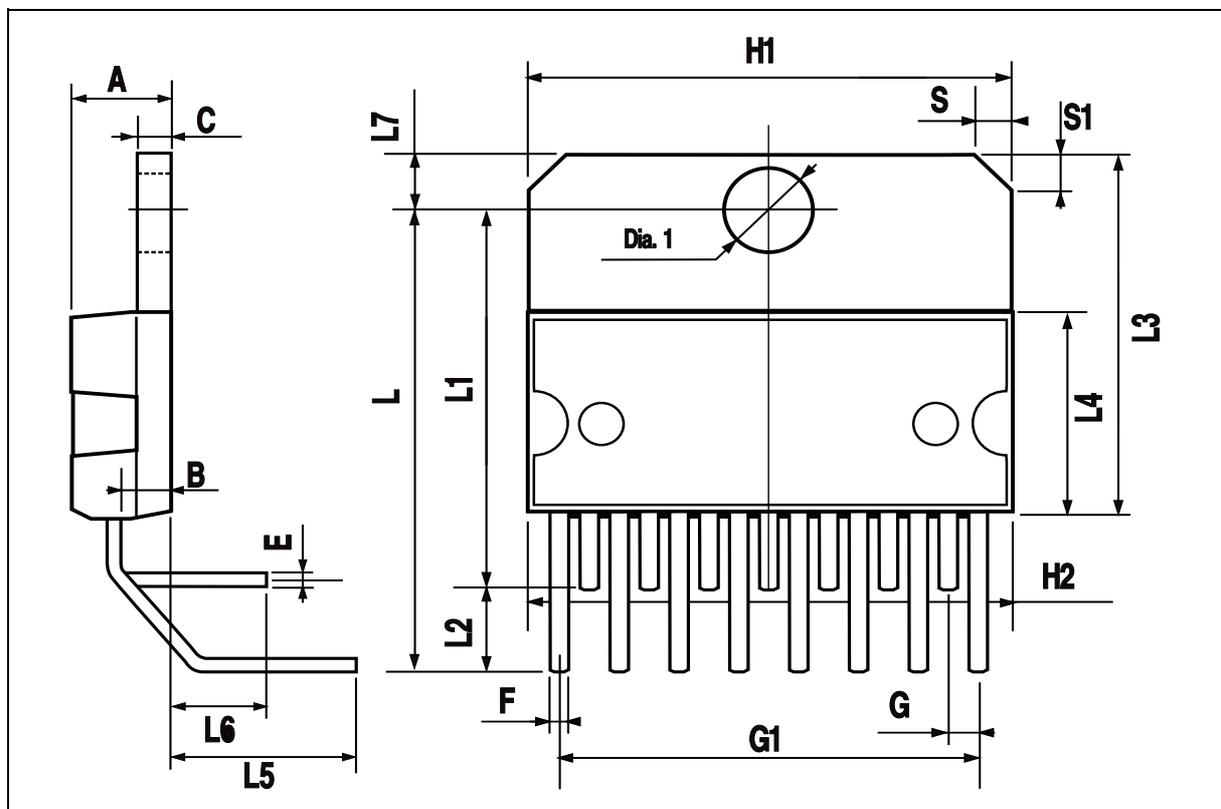


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			5			0.197
B			2.65			0.104
C			1.6			0.063
E	0.49		0.55	0.019		0.022
F	0.66		0.75	0.026		0.030
G	1.14	1.27	1.4	0.045	0.050	0.055
G1	17.57	17.78	17.91	0.692	0.700	0.705
H1	19.6			0.772		
H2			20.2			0.795
L		20.57			0.810	
L1		18.03			0.710	
L2		2.54			0.100	
L3	17.25	17.5	17.75	0.679	0.689	0.699
L4	10.3	10.7	10.9	0.406	0.421	0.429
L5		5.28			0.208	
L6		2.38			0.094	
L7	2.65		2.9	0.104		0.114
S	1.9		2.6	0.075		0.102
S1	1.9		2.6	0.075		0.102
Dia1	3.65		3.85	0.144		0.152

OUTLINE AND MECHANICAL DATA



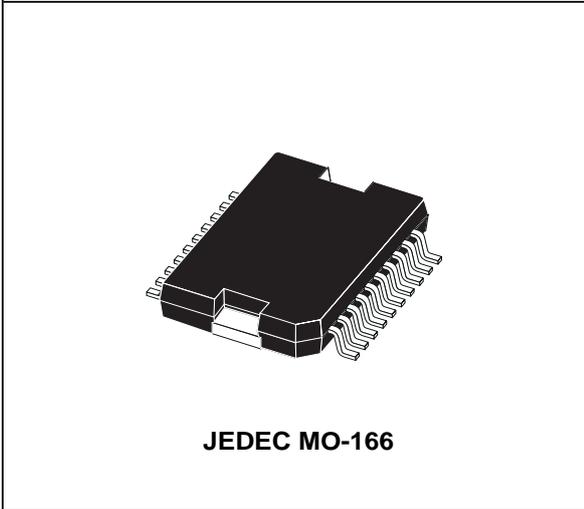
Multiwatt15 H



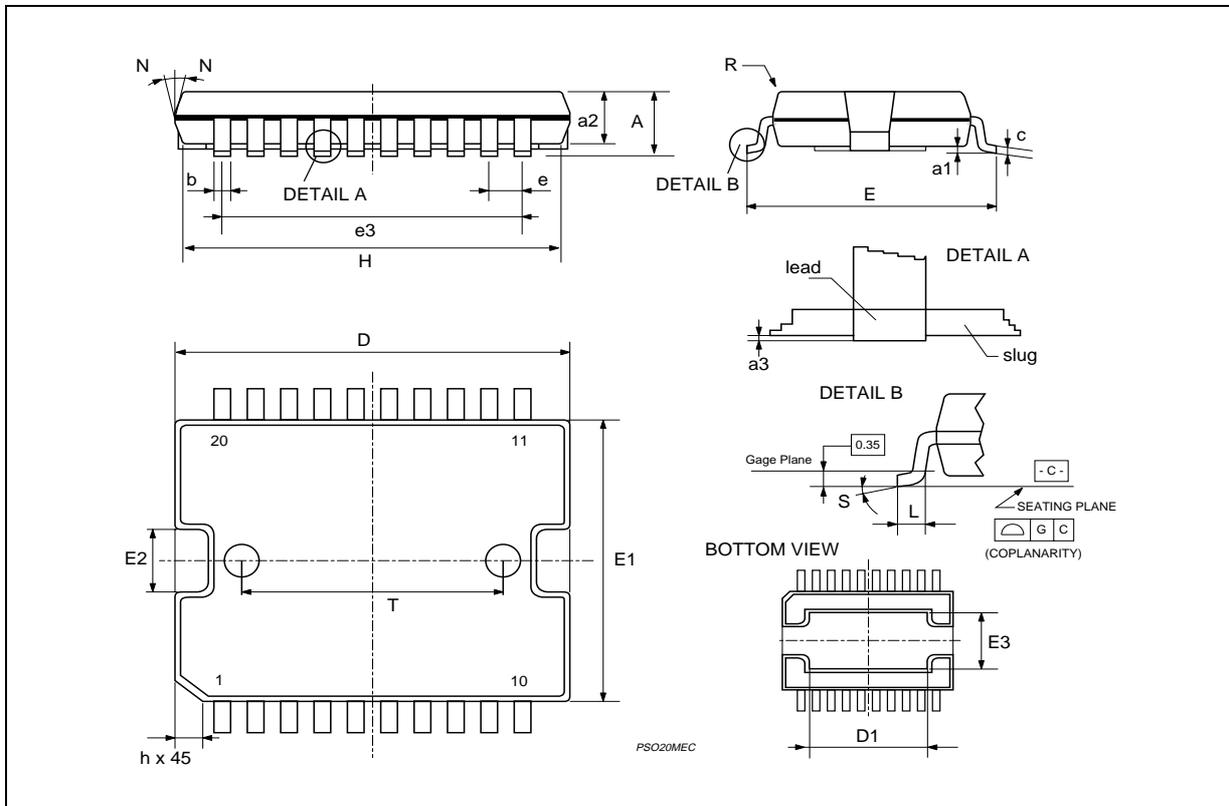
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			3.6			0.142
a1	0.1		0.3	0.004		0.012
a2			3.3			0.130
a3	0		0.1	0.000		0.004
b	0.4		0.53	0.016		0.021
c	0.23		0.32	0.009		0.013
D (1)	15.8		16	0.622		0.630
D1	9.4		9.8	0.370		0.386
E	13.9		14.5	0.547		0.570
e		1.27			0.050	
e3		11.43			0.450	
E1 (1)	10.9		11.1	0.429		0.437
E2			2.9			0.114
E3	5.8		6.2	0.228		0.244
G	0		0.1	0.000		0.004
H	15.5		15.9	0.610		0.626
h			1.1			0.043
L	0.8		1.1	0.031		0.043
N	10° (max.)					
S	8° (max.)					
T		10			0.394	

(1) "D and F" do not include mold flash or protrusions.
 - Mold flash or protrusions shall not exceed 0.15 mm (0.006").
 - Critical dimensions: "E", "G" and "a3"

OUTLINE AND MECHANICAL DATA



PowerSO20



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IR2101(S)/IR2102(S) & (PbF)

HIGH AND LOW SIDE DRIVER Product Summary

V_{OFFSET}	600V max.
$I_{\text{O}+/-}$	130 mA / 270 mA
V_{OUT}	10 - 20V
$t_{\text{on/off}}$ (typ.)	160 & 150 ns
Delay Matching	50 ns

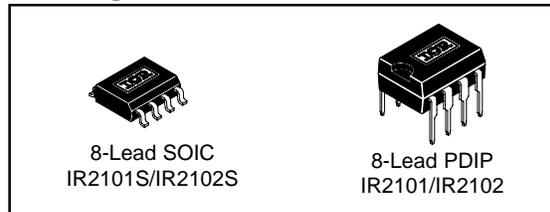
Features

- Floating channel designed for bootstrap operation
Fully operational to +600V
Tolerant to negative transient voltage
dV/dt immune
- Gate drive supply range from 10 to 20V
- Undervoltage lockout
- 3.3V, 5V, and 15V logic input compatible
- Matched propagation delay for both channels
- Outputs in phase with inputs (IR2101) or out of phase with inputs (IR2102)
- Also available LEAD-FREE

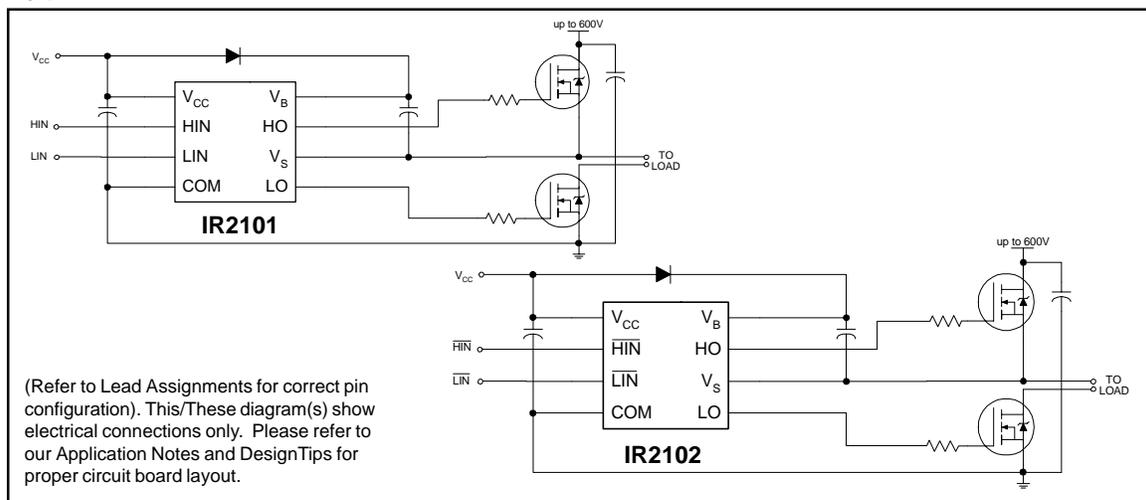
Description

The IR2101(S)/IR2102(S) are high voltage, high speed power MOSFET and IGBT drivers with independent high and low side referenced output channels. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard CMOS or LSTTL output, down to 3.3V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 600 volts.

Packages



Typical Connection



IR2101(S)/IR2102(S) & (PbF)

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units	
V _B	High side floating supply voltage	-0.3	625	V	
V _S	High side floating supply offset voltage	V _B - 25	V _B + 0.3		
V _{HO}	High side floating output voltage	V _S - 0.3	V _B + 0.3		
V _{CC}	Low side and logic fixed supply voltage	-0.3	25		
V _{LO}	Low side output voltage	-0.3	V _{CC} + 0.3		
V _{IN}	Logic input voltage (HIN & LIN)	-0.3	V _{CC} + 0.3		
dV _S /dt	Allowable offset supply voltage transient	—	50	V/ns	
P _D	Package power dissipation @ T _A ≤ +25°C	(8 lead PDIP)	—	1.0	W
		(8 lead SOIC)	—	0.625	
R _{thJA}	Thermal resistance, junction to ambient	(8 lead PDIP)	—	125	°C/W
		(8 lead SOIC)	—	200	
T _J	Junction temperature	—	150	°C	
T _S	Storage temperature	-55	150		
T _L	Lead temperature (soldering, 10 seconds)	—	300		

Recommended Operating Conditions

The input/output logic timing diagram is shown in figure 1. For proper operation the device should be used within the recommended conditions. The V_S offset rating is tested with all supplies biased at 15V differential.

Symbol	Definition	Min.	Max.	Units
V _B	High side floating supply absolute voltage	V _S + 10	V _S + 20	V
V _S	High side floating supply offset voltage	Note 1	600	
V _{HO}	High side floating output voltage	V _S	V _B	
V _{CC}	Low side and logic fixed supply voltage	10	20	
V _{LO}	Low side output voltage	0	V _{CC}	
V _{IN}	Logic input voltage (HIN & LIN) (IR2101) & ($\overline{\text{HIN}}$ & $\overline{\text{LIN}}$) (IR2102)	0	V _{CC}	
T _A	Ambient temperature	-40	125	°C

Note 1: Logic operational for V_S of -5 to +600V. Logic state held for V_S of -5V to -V_{BS}. (Please refer to the Design Tip DT97-3 for more details).

Dynamic Electrical Characteristics

V_{BIAS} (V_{CC}, V_{BS}) = 15V, C_L = 1000 pF and T_A = 25°C unless otherwise specified.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
t _{on}	Turn-on propagation delay	—	160	220	ns	V _S = 0V
t _{off}	Turn-off propagation delay	—	150	220		V _S = 600V
t _r	Turn-on rise time	—	100	170		
t _f	Turn-off fall time	—	50	90		
MT	Delay matching, HS & LS turn-on/off	—	—	50		

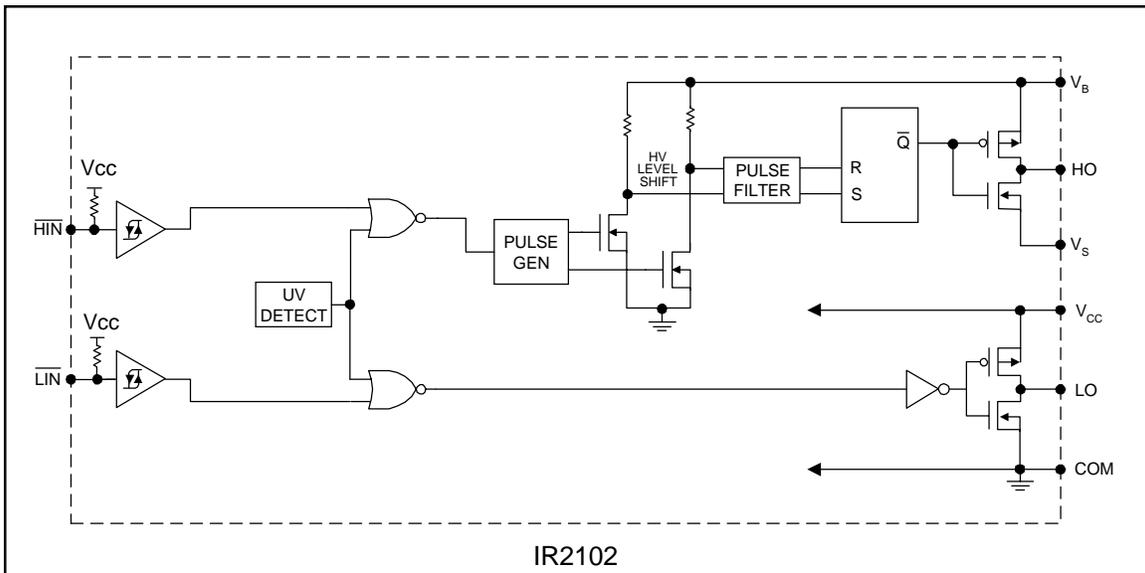
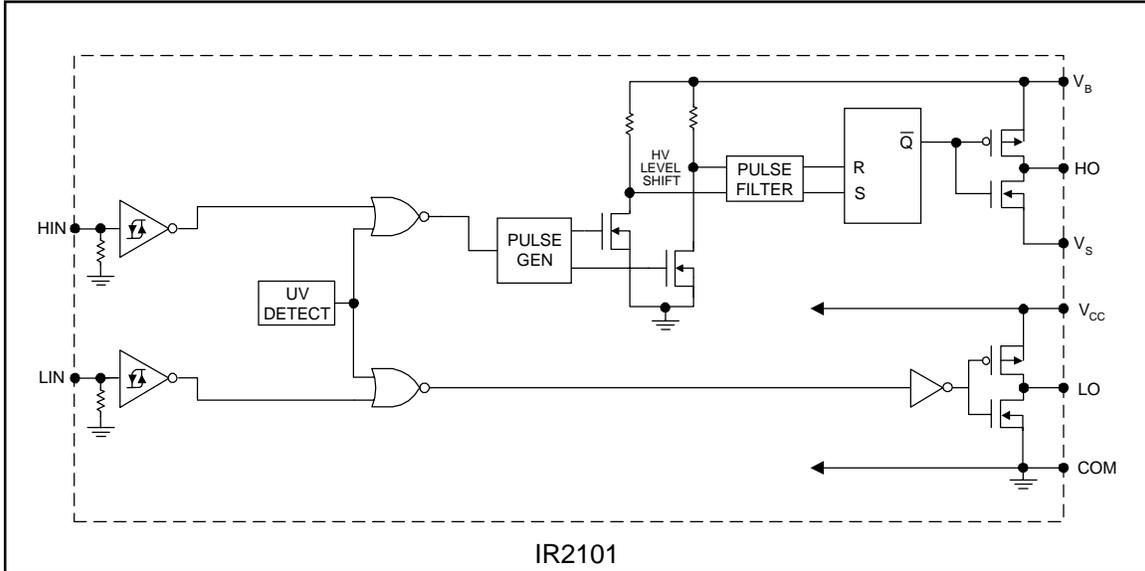
Static Electrical Characteristics

V_{BIAS} (V_{CC}, V_{BS}) = 15V and T_A = 25°C unless otherwise specified. The V_{IN}, V_{TH} and I_{IN} parameters are referenced to COM. The V_O and I_O parameters are referenced to COM and are applicable to the respective output leads: HO or LO.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
V _{IH}	Logic "1" input voltage (IR2101) Logic "0" input voltage (IR2102)	3	—	—	V	V _{CC} = 10V to 20V
V _{IL}	Logic "0" input voltage (IR2101) Logic "1" input voltage (IR2102)	—	—	0.8		V _{CC} = 10V to 20V
V _{OH}	High level output voltage, V _{BIAS} - V _O	—	—	100	mV	I _O = 0A
V _{OL}	Low level output voltage, V _O	—	—	100		I _O = 0A
I _{LK}	Offset supply leakage current	—	—	50	μA	V _B = V _S = 600V
I _{QBS}	Quiescent V _{BS} supply current	—	30	55		V _{IN} = 0V or 5V
I _{QCC}	Quiescent V _{CC} supply current	—	150	270		V _{IN} = 0V or 5V
I _{IN+}	Logic "1" input bias current	—	3	10		V _{IN} = 5V (IR2101) V _{IN} = 0V (IR2102)
I _{IN-}	Logic "0" input bias current	—	—	1	V _{IN} = 0V (IR2101) V _{IN} = 5V (IR2102)	
V _{CCUV+}	V _{CC} supply undervoltage positive going threshold	8	8.9	9.8	V	
V _{CCUV-}	V _{CC} supply undervoltage negative going threshold	7.4	8.2	9		
I _{O+}	Output high short circuit pulsed current	130	210	—	mA	V _O = 0V V _{IN} = Logic "1" PW ≤ 10 μs
I _{O-}	Output low short circuit pulsed current	270	360	—		V _O = 15V V _{IN} = Logic "0" PW ≤ 10 μs

IR2101(S)/IR2102(S) & (PbF)

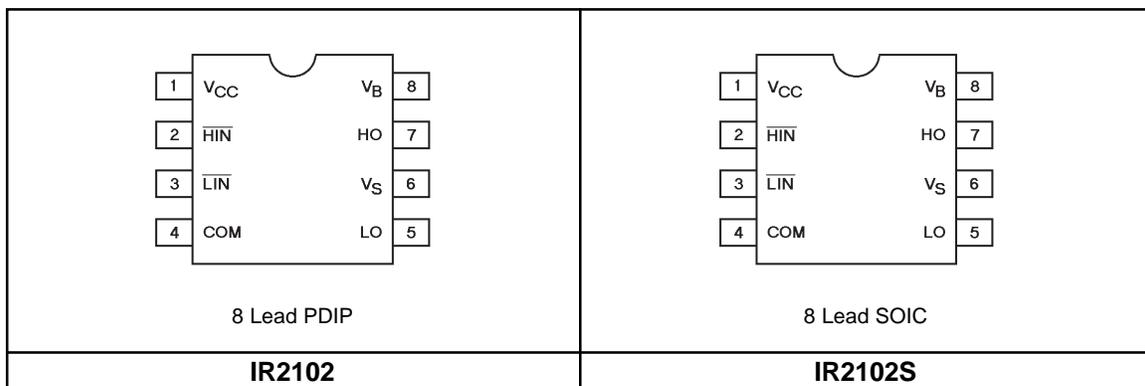
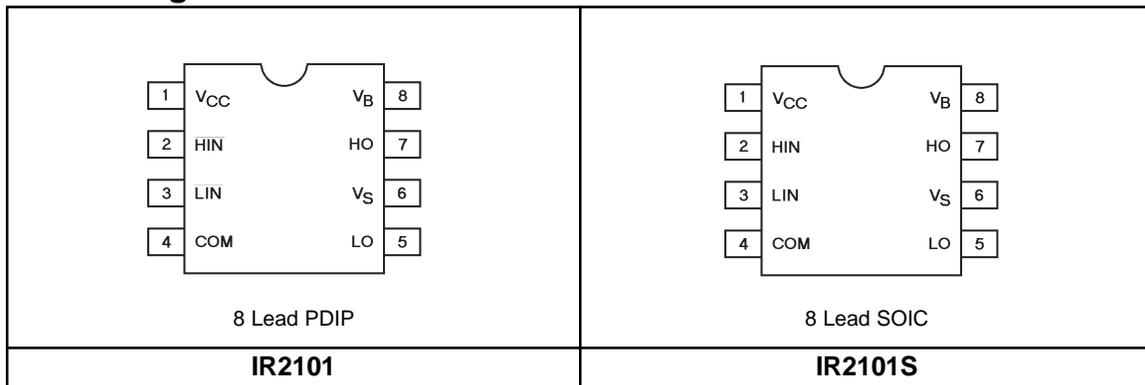
Functional Block Diagram



Lead Definitions

Symbol	Description
HIN	Logic input for high side gate driver output (HO), in phase (IR2101)
$\overline{\text{HIN}}$	Logic input for high side gate driver output (HO), out of phase (IR2102)
LIN	Logic input for low side gate driver output (LO), in phase (IR2101)
$\overline{\text{LIN}}$	Logic input for low side gate driver output (LO), out of phase (IR2102)
V_B	High side floating supply
HO	High side gate drive output
V_S	High side floating supply return
V_{CC}	Low side and logic fixed supply
LO	Low side gate drive output
COM	Low side return

Lead Assignments



IR2101(S)/IR2102(S) & (PbF)

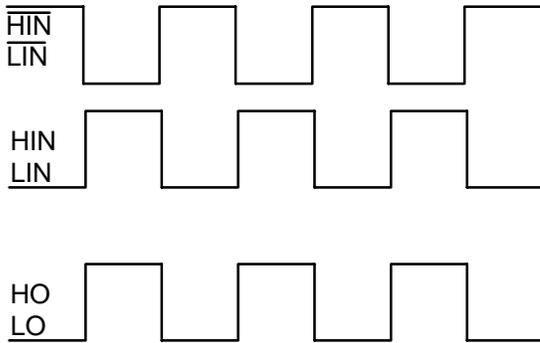


Figure 1. Input/Output Timing Diagram

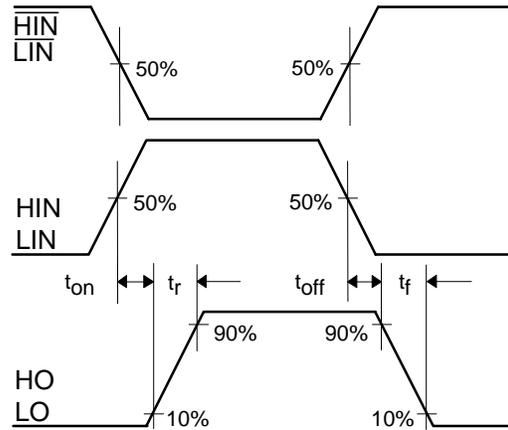


Figure 2. Switching Time Waveform Definitions

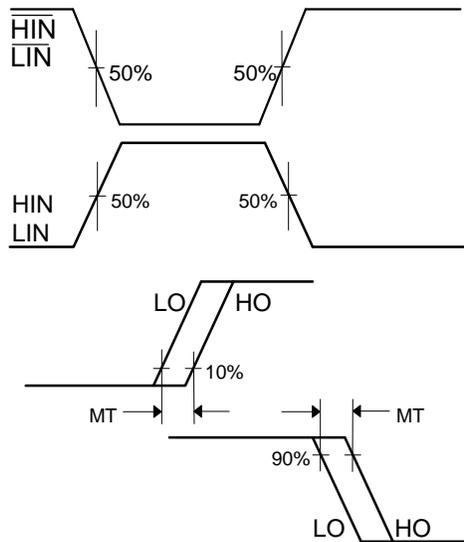


Figure 3. Delay Matching Waveform Definitions

IR2101(S)/IR2102(S) & (PbF)

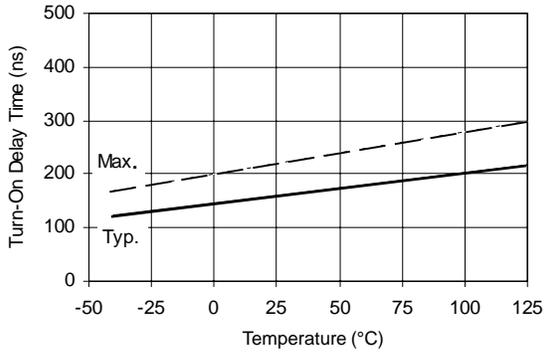


Figure 6A. Turn-On Time vs Temperature

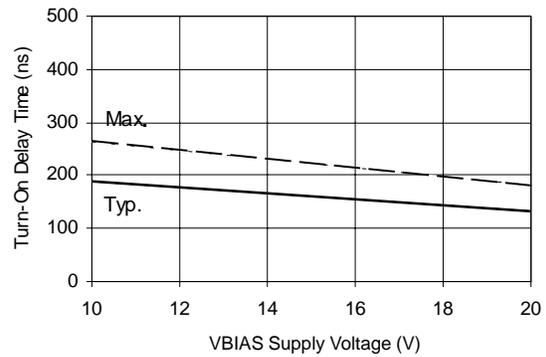


Figure 6B. Turn-On Time vs Supply Voltage

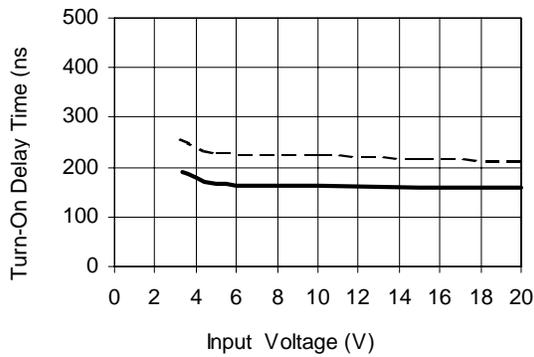


Figure 6C. Turn-On Time vs Input Voltage

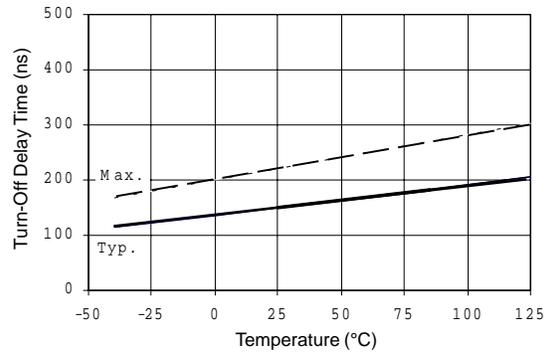


Figure 7A. Turn-Off Time vs Temperature

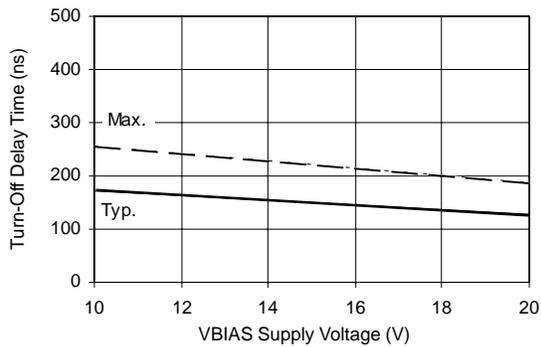


Figure 7B. Turn-Off Time vs Supply Voltage

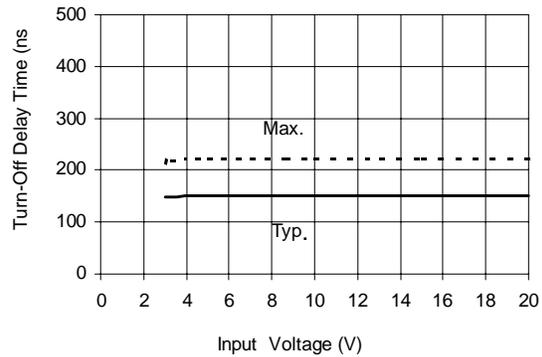


Figure 7C. Turn-Off Time vs Input Voltage

IR2101(S)/IR2102(S) & (PbF)

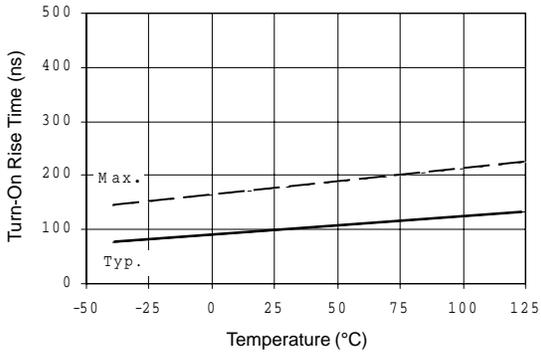


Figure 9A. Turn-On Rise Time vs Temperature

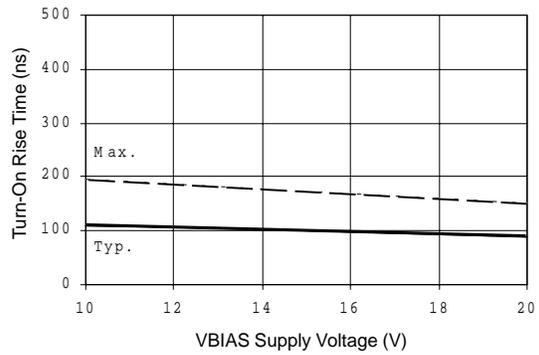


Figure 9B. Turn-On Rise Time vs Voltage

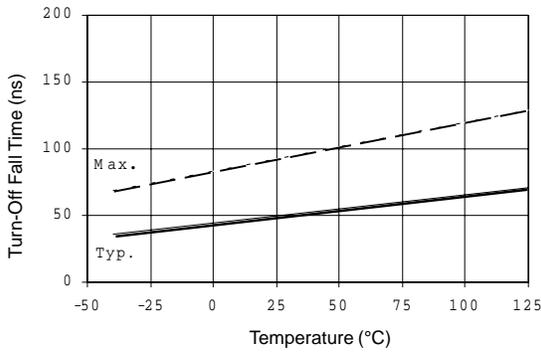


Figure 10A. Turn-Off Fall Time vs Temperature

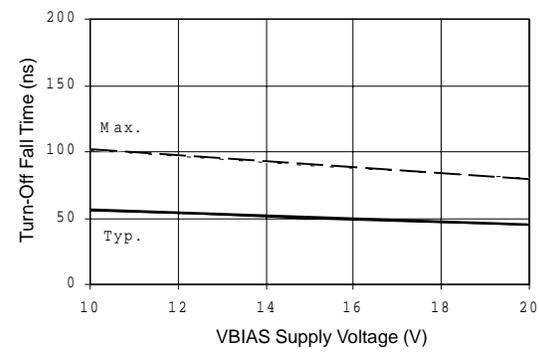
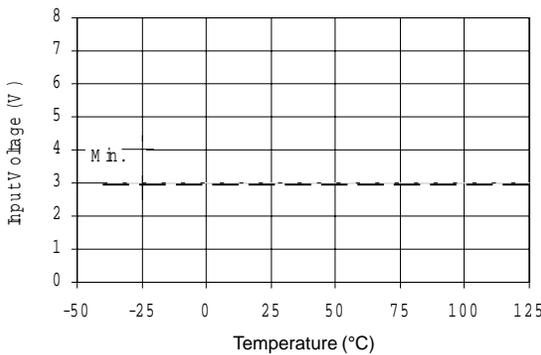
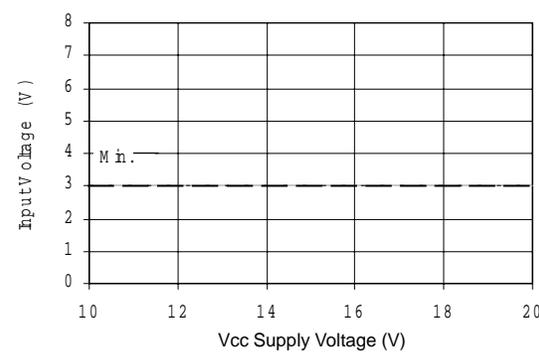


Figure 10B. Turn-Off Fall Time vs Voltage

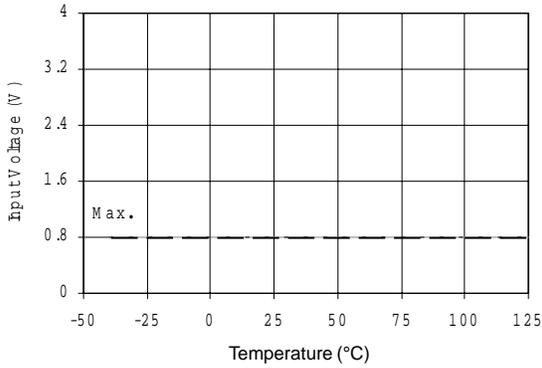


**Figure 12A. Logic "1" Input Voltage (IR2101)
Logic "0" Input Voltage (IR2102)
vs Temperature**

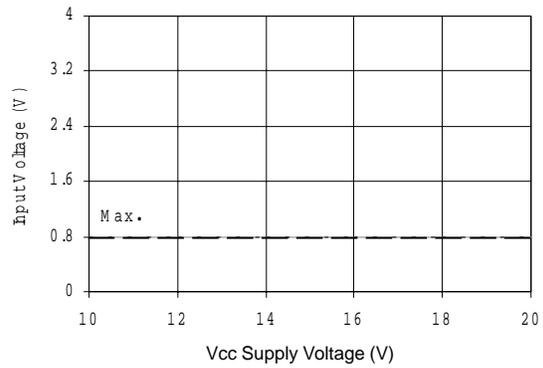


**Figure 12B. Logic "1" Input Voltage (IR2101)
Logic "0" Input Voltage (IR2102)
vs Voltage**

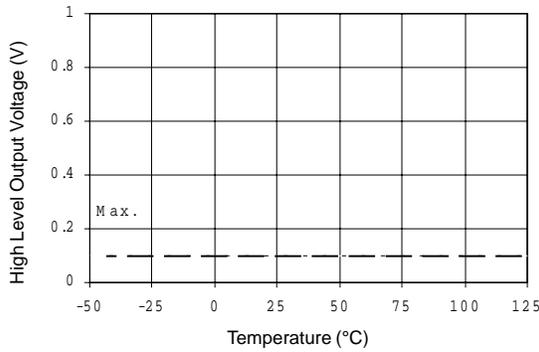
IR2101(S)/IR2102(S) & (PbF)



**Figure 13A. Logic "0" Input Voltage (IR2101)
 Logic "1" Input Voltage (IR2102)
 vs Temperature**



**Figure 13B. Logic "0" Input Voltage (IR2101)
 Logic "1" Input Voltage (IR2102)
 vs Voltage**



**Figure 14A. High Level Output
 vs Temperature**

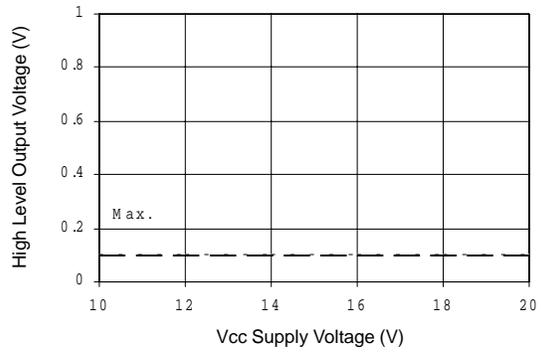
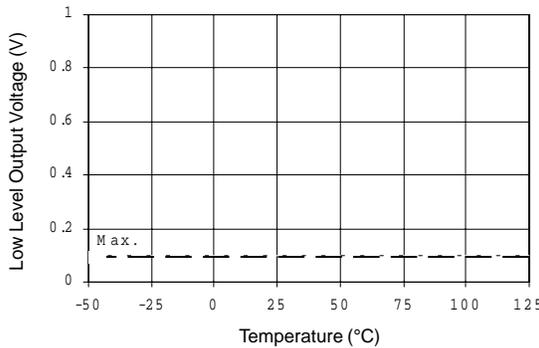


Figure 14B. High Level Output vs Voltage



**Figure 15A. Low Level Output
 vs Temperature**

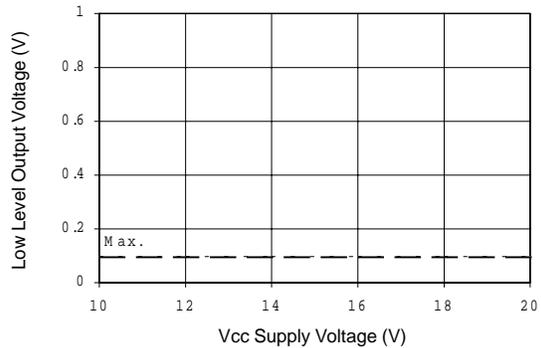


Figure 15B. Low level Output vs Voltage

IR2101(S)/IR2102(S) & (PbF)

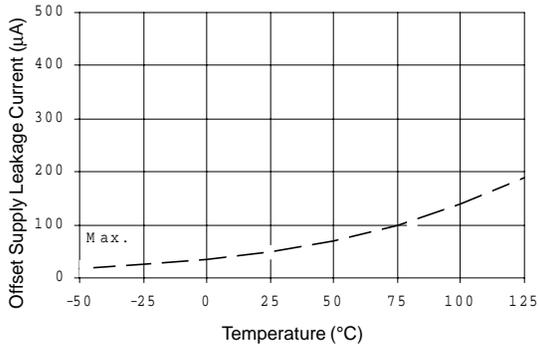


Figure 16A. Offset Supply Current vs Temperature

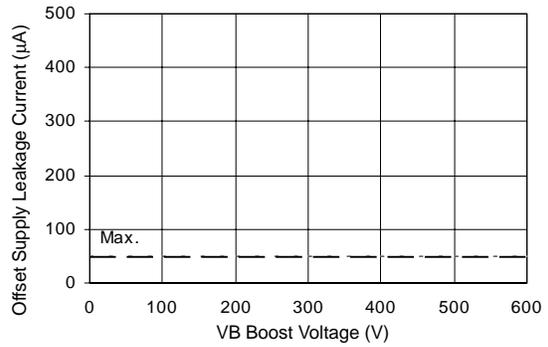


Figure 16B. Offset Supply Current vs Voltage

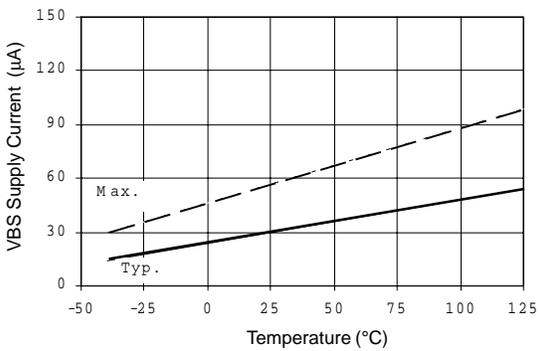


Figure 17A. V_{BS} Supply Current vs Temperature

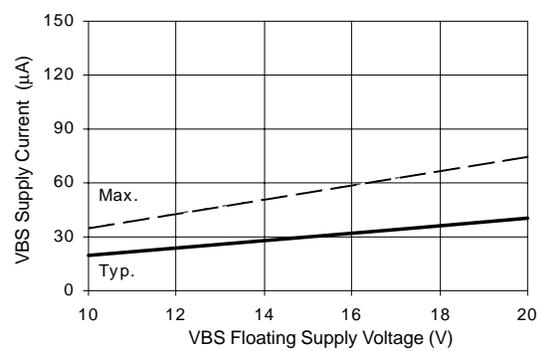


Figure 17B. V_{BS} Supply Current vs Voltage

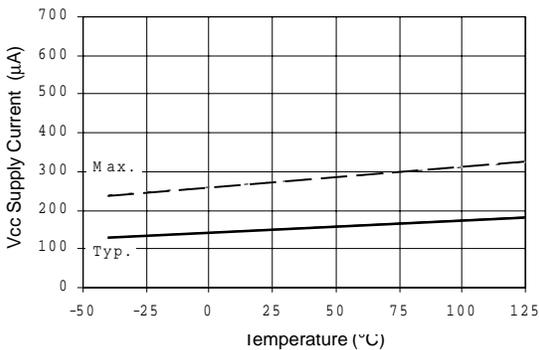


Figure 18A. V_{CC} Supply Current vs Temperature

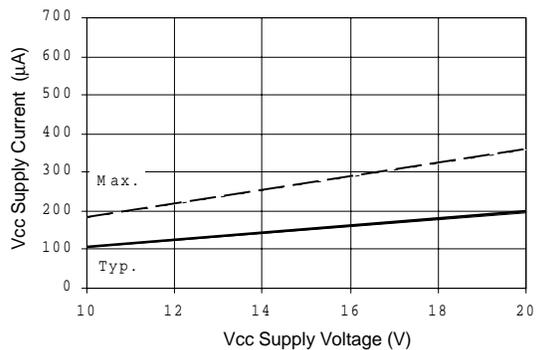


Figure 18B. V_{CC} Supply Current vs Voltage

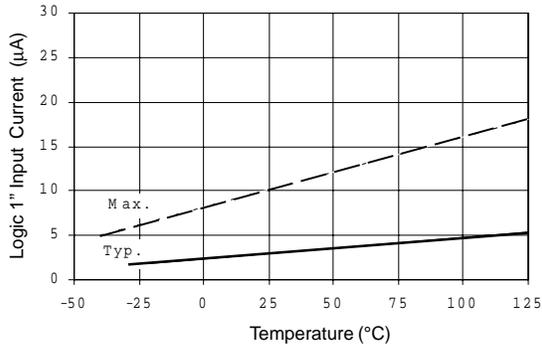


Figure 19A. Logic "1" Input Current vs Temperature

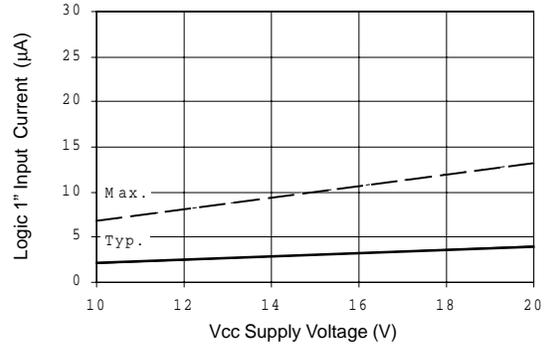


Figure 19B. Logic "1" Input Current vs Voltage

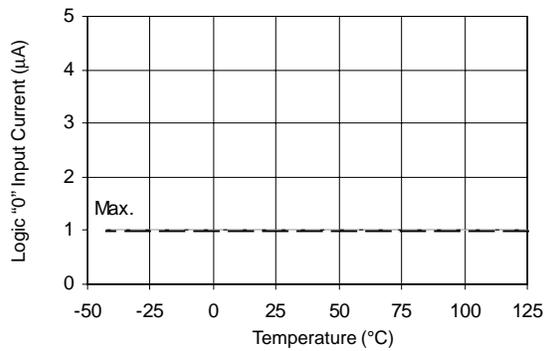


Figure 20A. Logic "0" Input Current vs Temperature

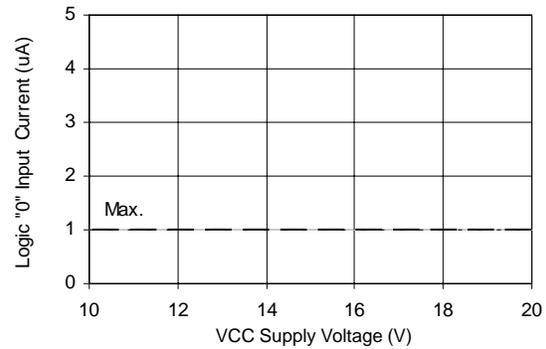


Figure 20B. Logic "0" Input Current vs Voltage

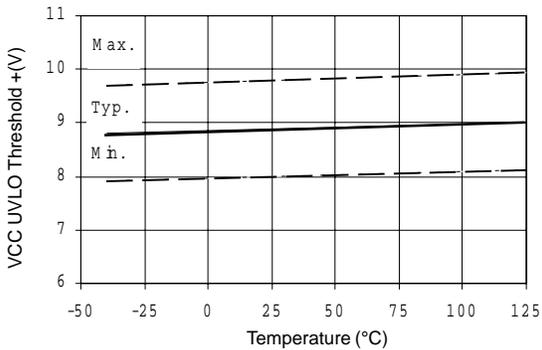


Figure 21A. Vcc Undervoltage Threshold(+) vs Temperature

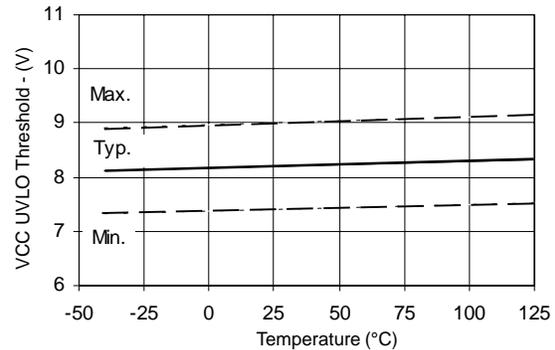


Figure 21B. Vcc Undervoltage Threshold(-) vs Temperature

IR2101(S)/IR2102(S) & (PbF)

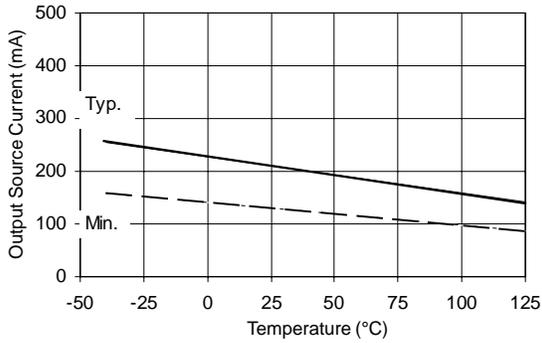


Figure 22A. Output Source Current vs Temperature

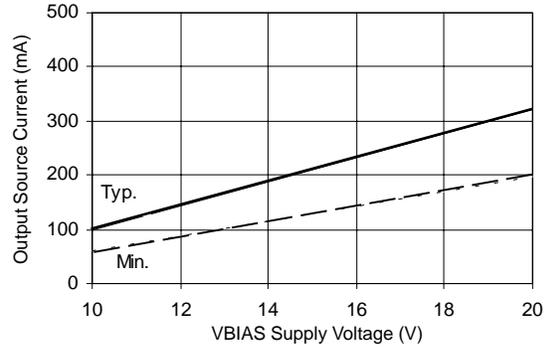


Figure 22B. Output Source Current vs Voltage

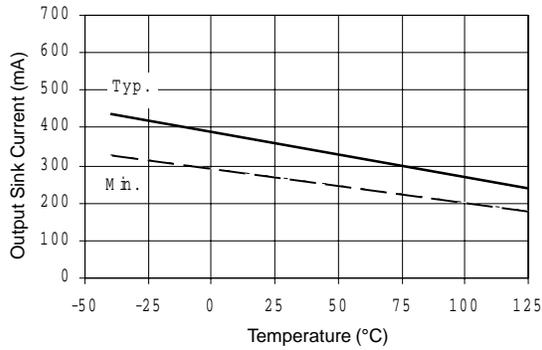


Figure 23A. Output Sink Current vs Temperature

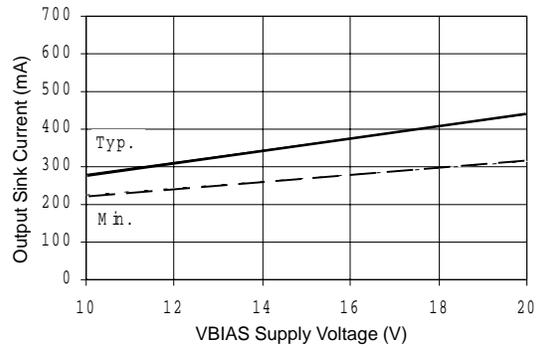
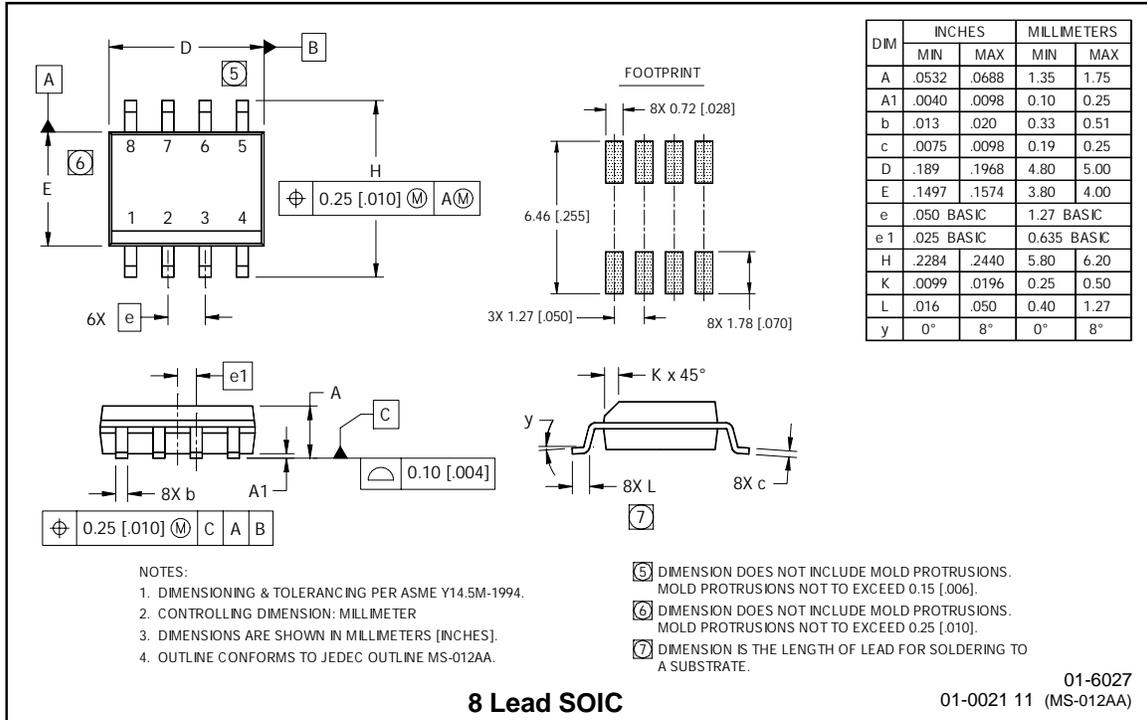
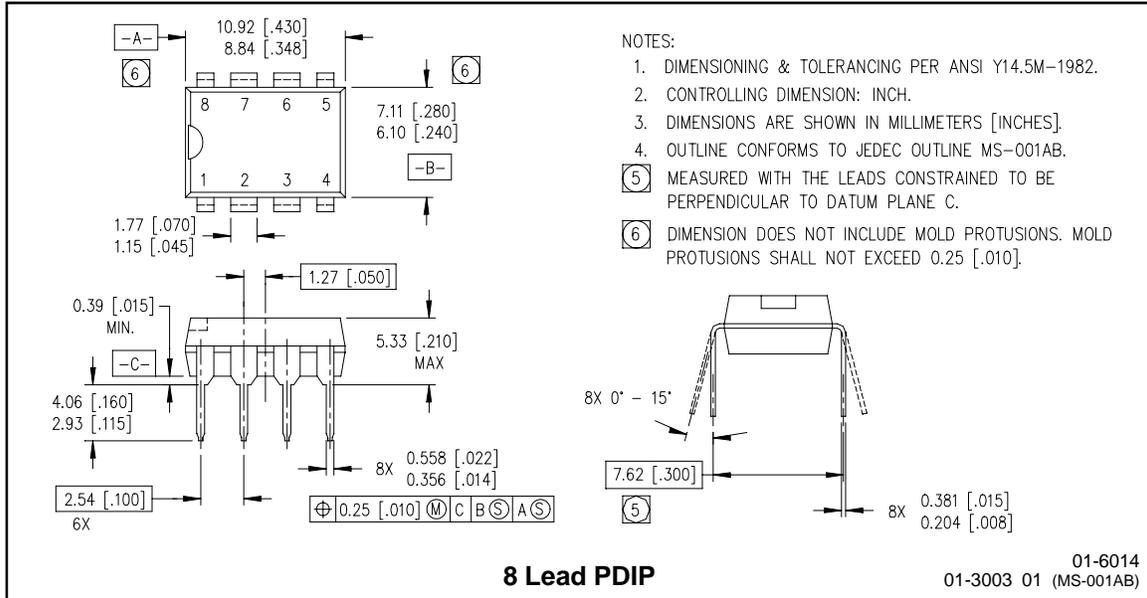


Figure 23B. Output Sink Current vs Voltage

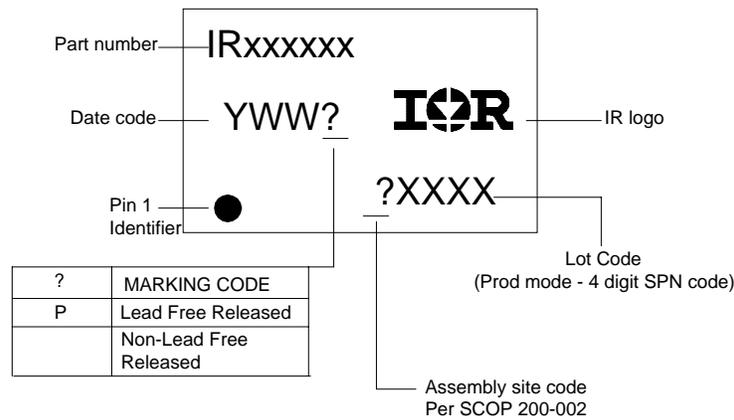
IR2101(S)/IR2102(S) & (PbF)

Case outlines



IR2101(S)/IR2102(S) & (PbF)

LEADFREE PART MARKING INFORMATION



ORDER INFORMATION

Basic Part (Non-Lead Free)

8-Lead PDIP IR2101 order IR2101
 8-Lead SOIC IR2101S order IR2101S
 8-Lead PDIP IR2102 order IR2102
 8-Lead SOIC IR2102S order IR2102S

Leadfree Part

8-Lead PDIP IR2101 order IR2101PbF
 8-Lead SOIC IR2101S order IR2101SPbF
 8-Lead PDIP IR2102 order IR2102PbF
 8-Lead SOIC IR2102S order IR2102SPbF

IR2103(S) & (PbF)

HALF-BRIDGE DRIVER

Features

- Floating channel designed for bootstrap operation
Fully operational to +600V
Tolerant to negative transient voltage
dV/dt immune
- Gate drive supply range from 10 to 20V
- Undervoltage lockout
- 3.3V, 5V and 15V logic compatible
- Cross-conduction prevention logic
- Matched propagation delay for both channels
- Internal set deadtime
- High side output in phase with HIN input
- Low side output out of phase with LIN input
- Also available LEAD-FREE

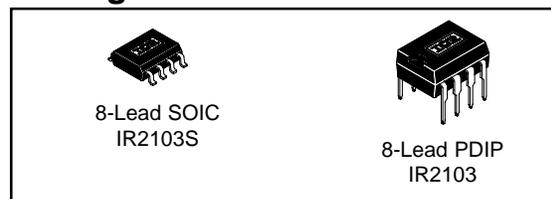
Description

The IR2103(S) are high voltage, high speed power MOSFET and IGBT drivers with dependent high and low side referenced output channels. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard CMOS or LSTTL output, down to 3.3V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 600 volts.

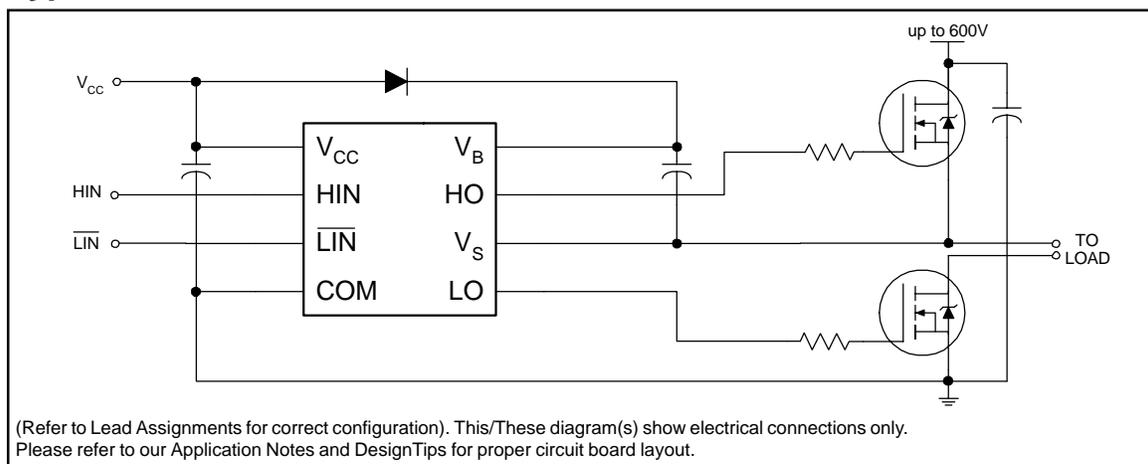
Product Summary

V_{OFFSET}	600V max.
$I_{\text{O+/-}}$	130 mA / 270 mA
V_{OUT}	10 - 20V
$t_{\text{on/off (typ.)}}$	680 & 150 ns
Deadtime (typ.)	520 ns

Packages



Typical Connection



IR2103(S) & (PbF)

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units	
V _B	High side floating absolute voltage	-0.3	625	V	
V _S	High side floating supply offset voltage	V _B - 25	V _B + 0.3		
V _{HO}	High side floating output voltage	V _S - 0.3	V _B + 0.3		
V _{CC}	Low side and logic fixed supply voltage	-0.3	25		
V _{LO}	Low side output voltage	-0.3	V _{CC} + 0.3		
V _{IN}	Logic input voltage (HIN & LIN)	-0.3	V _{CC} + 0.3		
dV _S /dt	Allowable offset supply voltage transient	—	50	V/ns	
P _D	Package power dissipation @ T _A ≤ +25°C	(8 Lead PDIP)	—	1.0	W
		(8 Lead SOIC)	—	0.625	
R _{thJA}	Thermal resistance, junction to ambient	(8 Lead PDIP)	—	125	°C/W
		(8 Lead SOIC)	—	200	
T _J	Junction temperature	—	150	°C	
T _S	Storage temperature	-55	150		
T _L	Lead temperature (soldering, 10 seconds)	—	300		

Recommended Operating Conditions

The input/output logic timing diagram is shown in figure 1. For proper operation the device should be used within the recommended conditions. The V_S offset rating is tested with all supplies biased at 15V differential.

Symbol	Definition	Min.	Max.	Units
V _B	High side floating supply absolute voltage	V _S + 10	V _S + 20	V
V _S	High side floating supply offset voltage	Note 1	600	
V _{HO}	High side floating output voltage	V _S	V _B	
V _{CC}	Low side and logic fixed supply voltage	10	20	
V _{LO}	Low side output voltage	0	V _{CC}	
V _{IN}	Logic input voltage (HIN & LIN)	0	V _{CC}	
T _A	Ambient temperature	-40	125	°C

Note 1: Logic operational for V_S of -5 to +600V. Logic state held for V_S of -5V to -V_{BS}. (Please refer to the Design Tip DT97-3 for more details).

Dynamic Electrical Characteristics

V_{BIAS} (V_{CC} , V_{BS}) = 15V, C_L = 1000 pF and T_A = 25°C unless otherwise specified.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
t_{on}	Turn-on propagation delay	—	680	820	ns	$V_S = 0V$
t_{off}	Turn-off propagation delay	—	150	220		$V_S = 600V$
t_r	Turn-on rise time	—	100	170		
t_f	Turn-off fall time	—	50	90		
DT	Deadtime, LS turn-off to HS turn-on & HS turn-on to LS turn-off	400	520	650		
MT	Delay matching, HS & LS turn-on/off	—	—	60		

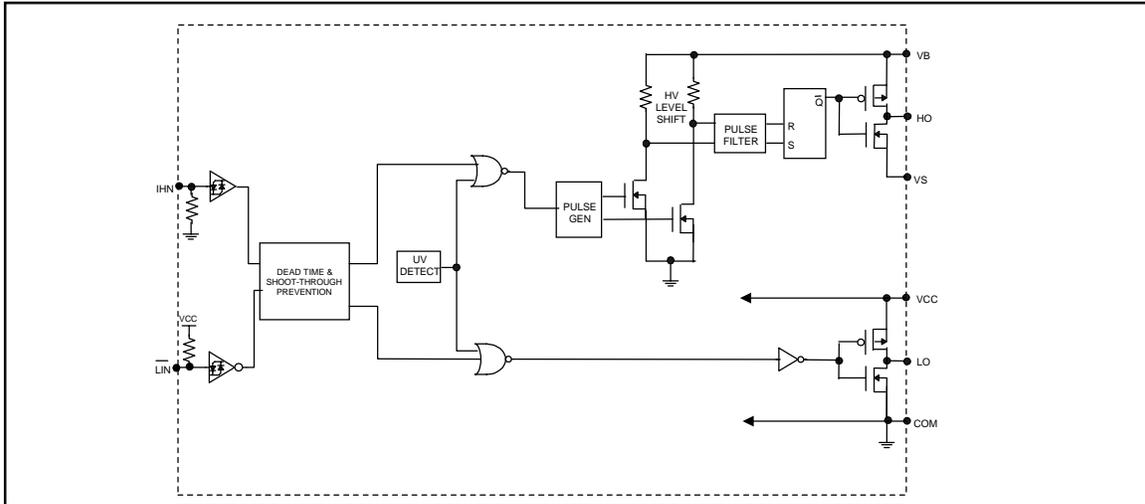
Static Electrical Characteristics

V_{BIAS} (V_{CC} , V_{BS}) = 15V and T_A = 25°C unless otherwise specified. The V_{IN} , V_{TH} and I_{IN} parameters are referenced to COM. The V_O and I_O parameters are referenced to COM and are applicable to the respective output leads: HO or LO.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
V_{IH}	Logic "1" (HIN) & Logic "0" (\overline{LIN}) input voltage	3	—	—	V	$V_{CC} = 10V$ to 20V
V_{IL}	Logic "0" (HIN) & Logic "1" (\overline{LIN}) input voltage	—	—	0.8		$V_{CC} = 10V$ to 20V
V_{OH}	High level output voltage, $V_{BIAS} - V_O$	—	—	100	mV	$I_O = 0A$
V_{OL}	Low level output voltage, V_O	—	—	100		$I_O = 0A$
I_{LK}	Offset supply leakage current	—	—	50	μA	$V_B = V_S = 600V$
I_{QBS}	Quiescent V_{BS} supply current	—	30	55		$V_{IN} = 0V$ or 5V
I_{QCC}	Quiescent V_{CC} supply current	—	150	270		$V_{IN} = 0V$ or 5V
I_{IN+}	Logic "1" input bias current	—	3	10		HIN = 5V, $\overline{LIN} = 0V$
I_{IN-}	Logic "0" input bias current	—	—	1		HIN = 0V, $\overline{LIN} = 5V$
V_{CCUV+}	V_{CC} supply undervoltage positive going threshold	8	8.9	9.8	V	
V_{CCUV-}	V_{CC} supply undervoltage negative going threshold	7.4	8.2	9		
I_{O+}	Output high short circuit pulsed current	130	210	—	mA	$V_O = 0V$, $V_{IN} = V_{IH}$ $PW \leq 10 \mu s$
I_{O-}	Output low short circuit pulsed current	270	360	—		$V_O = 15V$, $V_{IN} = V_{IL}$ $PW \leq 10 \mu s$

IR2103(S) & (PbF)

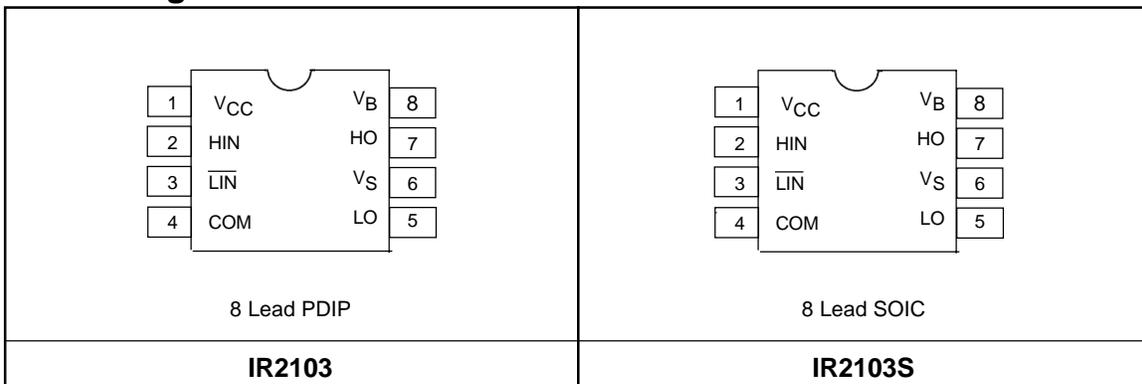
Functional Block Diagram



Lead Definitions

Symbol	Description
HIN	Logic input for high side gate driver output (HO), in phase
$\overline{\text{LIN}}$	Logic input for low side gate driver output (LO), out of phase
VB	High side floating supply
HO	High side gate drive output
VS	High side floating supply return
VCC	Low side and logic fixed supply
LO	Low side gate drive output
COM	Low side return

Lead Assignments



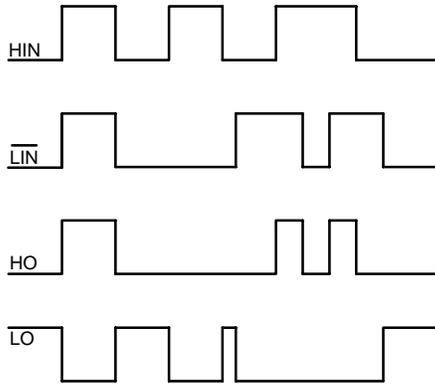


Figure 1. Input/Output Timing Diagram

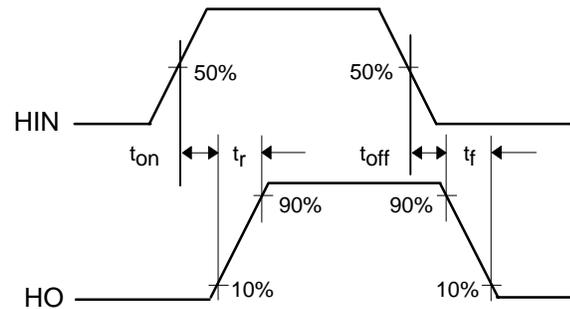
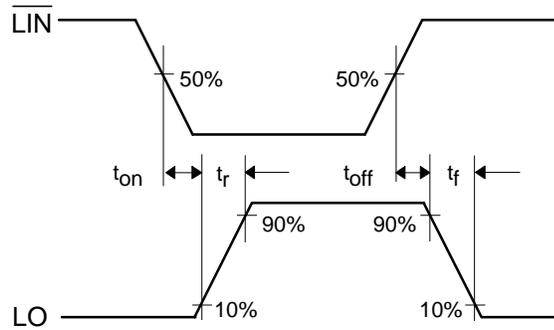


Figure 2. Switching Time Waveform Definitions

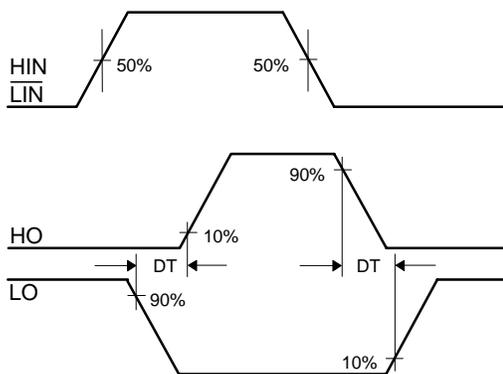


Figure 4. Deadtime Waveform Definitions

IR2103(S) & (PbF)

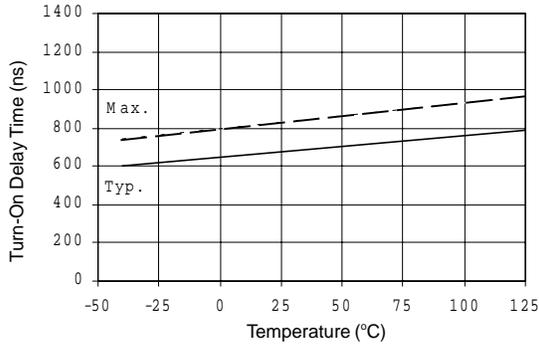


Figure 6A. Turn-On Time vs Temperature

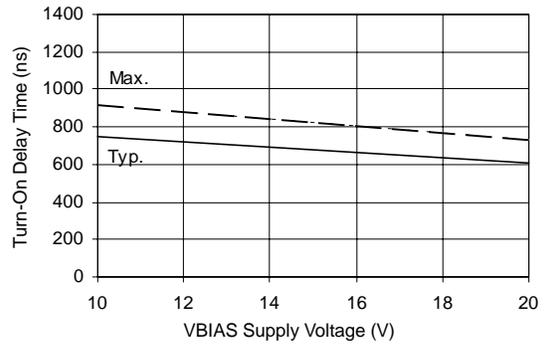


Figure 6B. Turn-On Time vs Supply Voltage

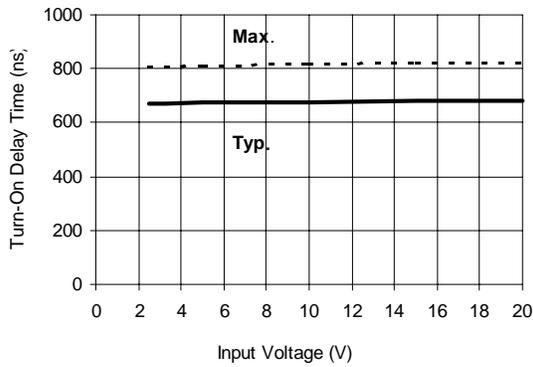


Figure 6C. Turn-On Time vs Input Voltage

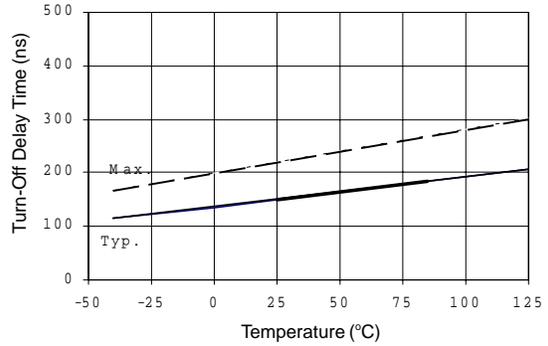


Figure 7A. Turn-Off Time vs Temperature

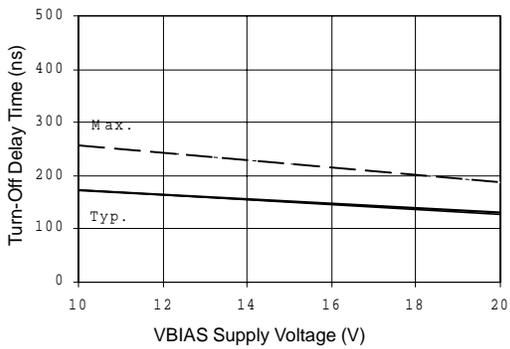


Figure 7B. Turn-Off Time vs Supply Voltage

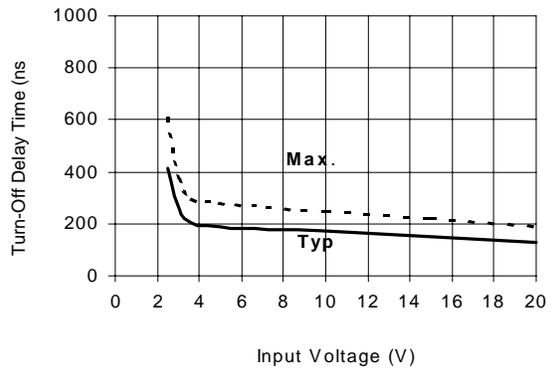


Figure 7C. Turn-Off Time vs Input Voltage

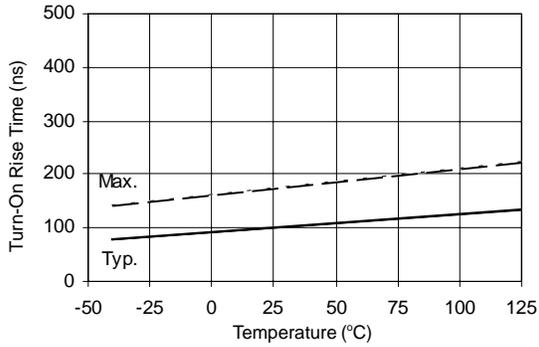


Figure 9A. Turn-On Rise Time vs Temperature

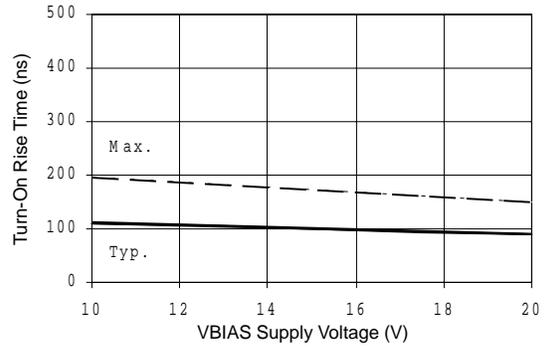


Figure 9B. Turn-On Rise Time vs Voltage

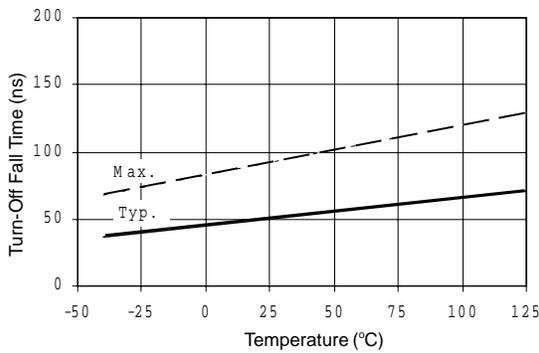


Figure 10A. Turn Off Fall Time vs Temperature

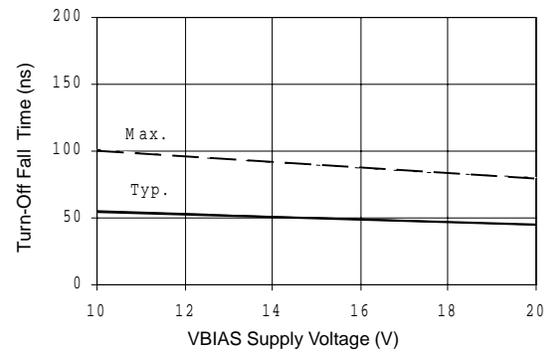


Figure 10B. Turn Off Fall Time vs Voltage

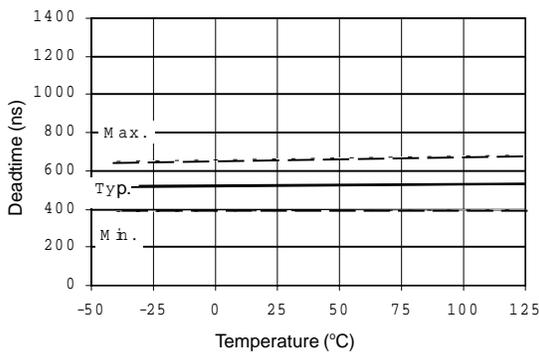


Figure 11A. Deadtime vs Temperature

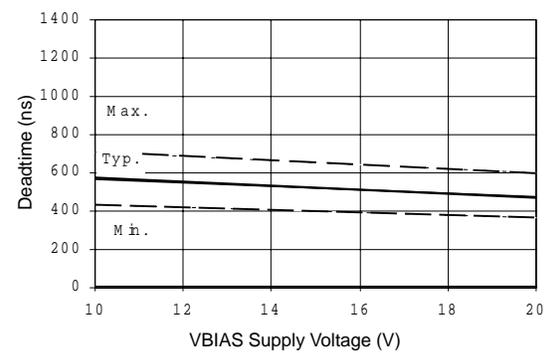


Figure 11B. Deadtime vs Voltage

IR2103(S) & (PbF)

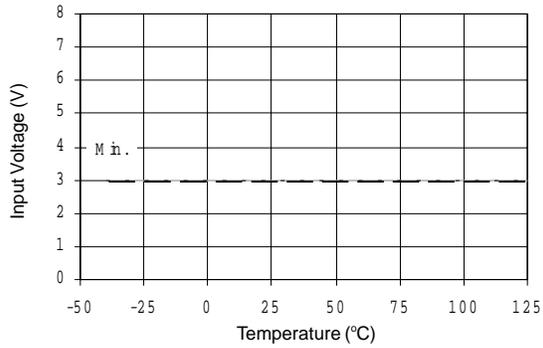


Figure 12A. Logic "1" (\overline{HIN}) & Logic "0" (\overline{LIN}) Input Voltage vs Temperature

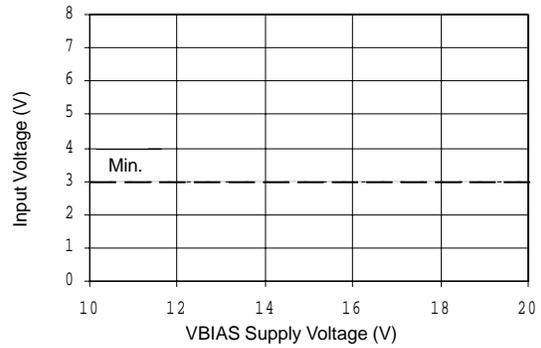


Figure 12B. Logic "1" (\overline{HIN}) & Logic "0" (\overline{LIN}) Input Voltage vs Voltage

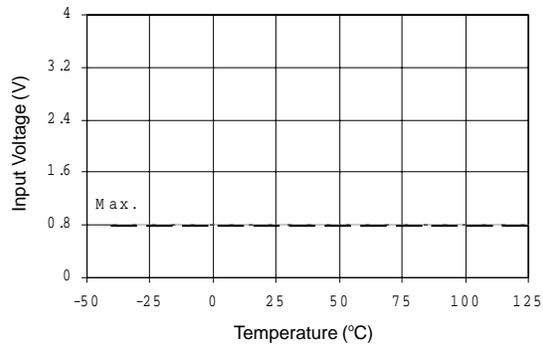


Figure 13A. Logic "0" (\overline{HIN}) & Logic "1" (\overline{LIN}) Input Voltage vs Temperature

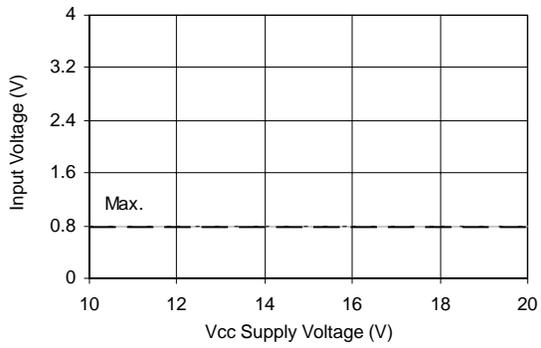


Figure 13B. Logic "0" (\overline{HIN}) & Logic "1" (\overline{LIN}) Input Voltage vs Voltage

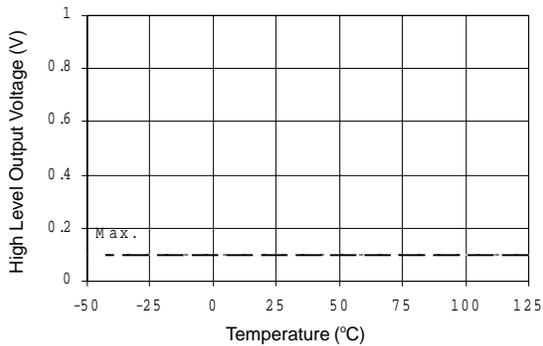


Figure 14A. High Level Output vs Temperature

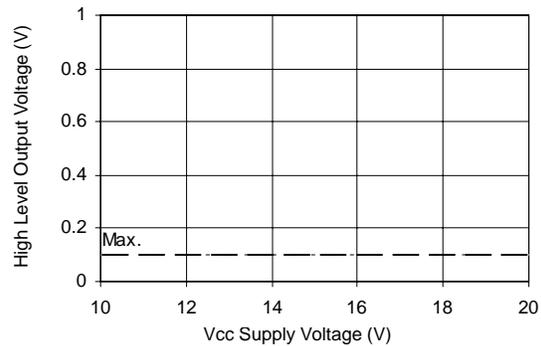


Figure 14B. High Level Output vs Voltage

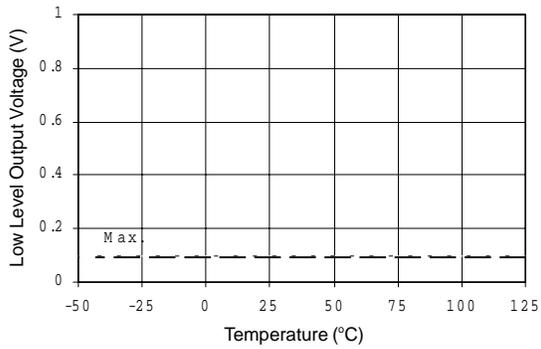


Figure 15A. Low Level Output vs Temperature

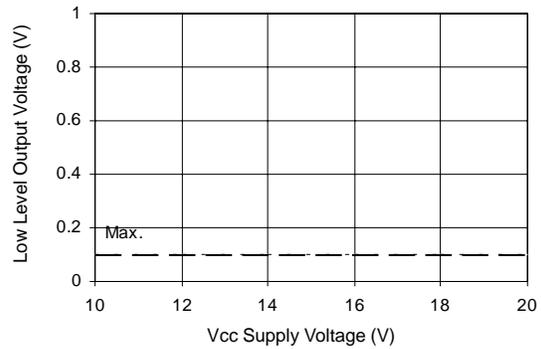


Figure 15B. Low Level Output vs Voltage

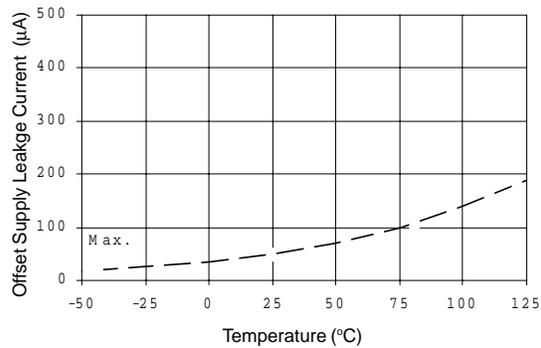


Figure 16A. Offset Supply Current vs Temperature

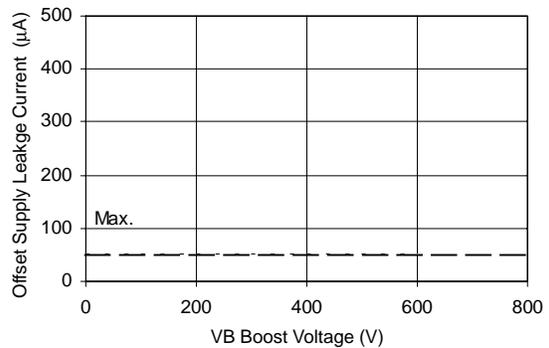


Figure 16B. Offset Supply Current vs Voltage

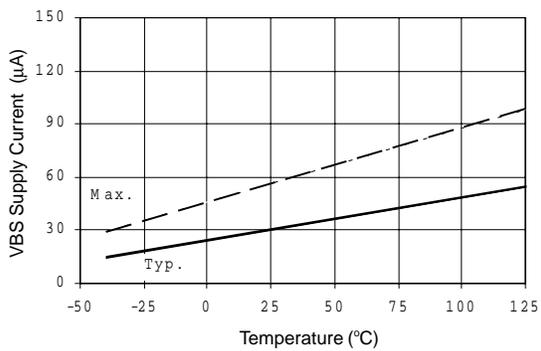


Figure 17A. VBS Supply Current vs Temperature

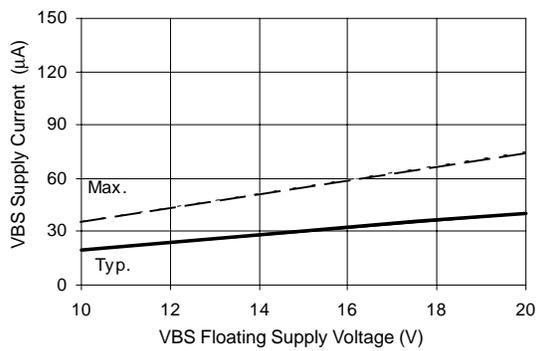


Figure 17B. VBS Supply Current vs Voltage

IR2103(S) & (PbF)

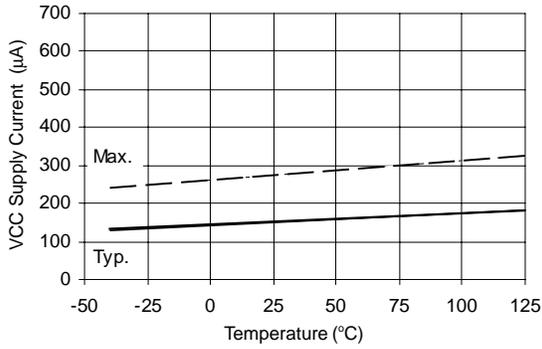


Figure 18A. Vcc Supply Current vs Temperature

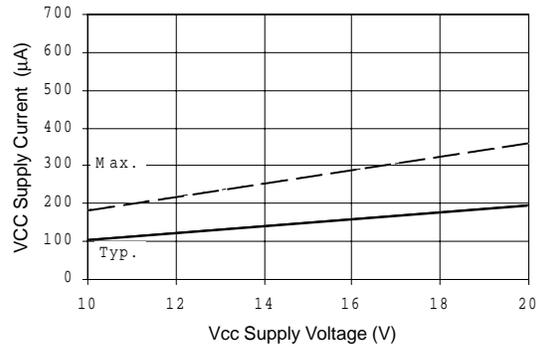


Figure 18B. Vcc Supply Current vs Voltage

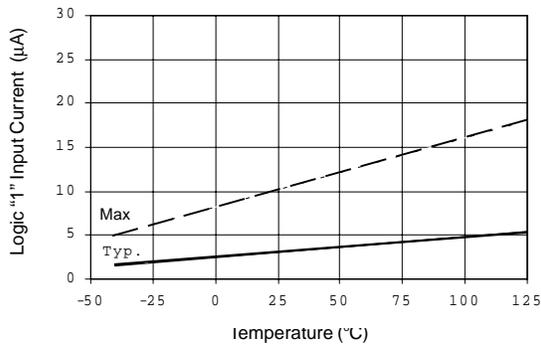


Figure 19A. Logic "1" Input Current vs Temperature

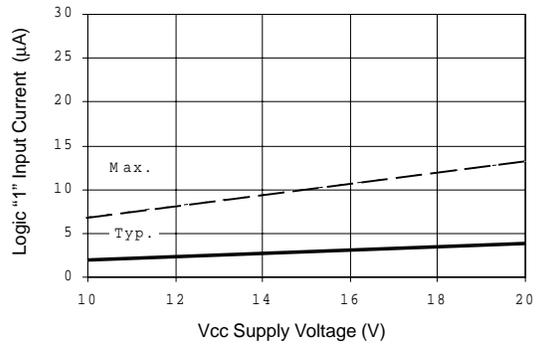


Figure 19B. Logic "1" Input Current vs Voltage

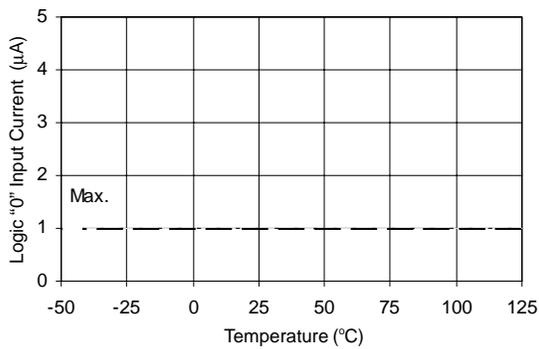


Figure 20A. Logic "0" Input Current vs Temperature

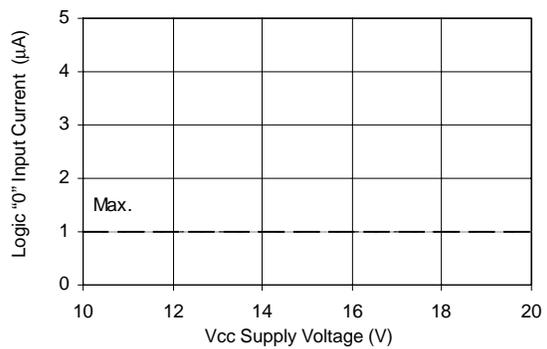


Figure 20B. Logic "0" Input Current vs Voltage

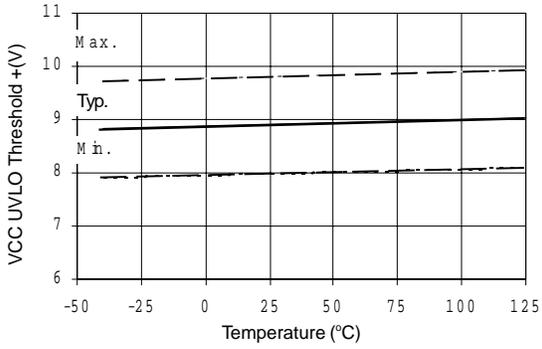


Figure 21A. Vcc Undervoltage Threshold(+) vs Temperature

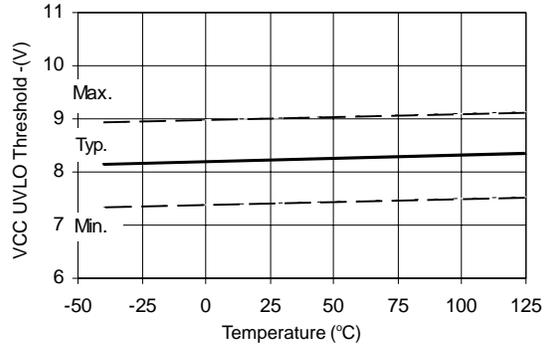


Figure 21B. Vcc Undervoltage Threshold (-) vs Temperature

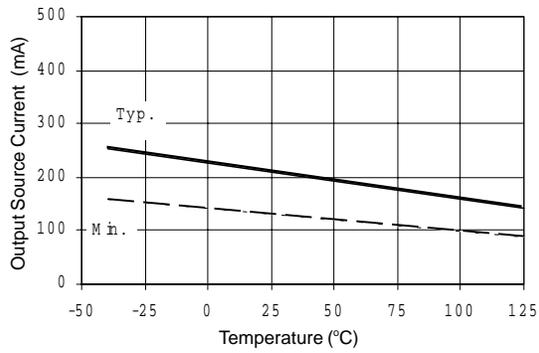


Figure 22A. Output Source Current vs Temperature

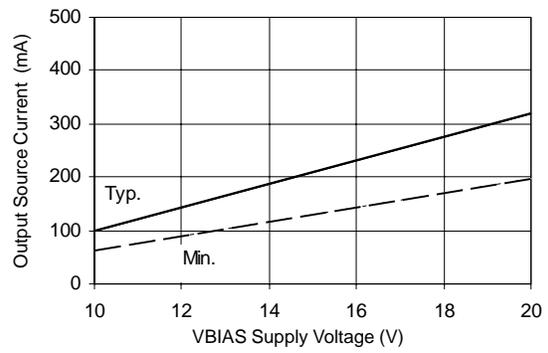


Figure 22B. Output Source Current vs Voltage

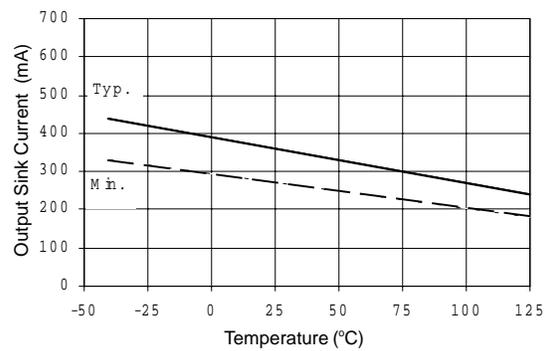


Figure 23A. Output Sink Current vs Temperature

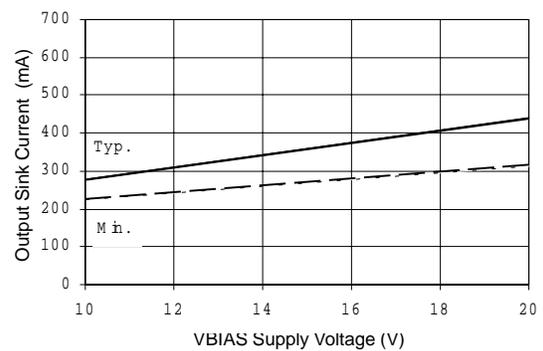
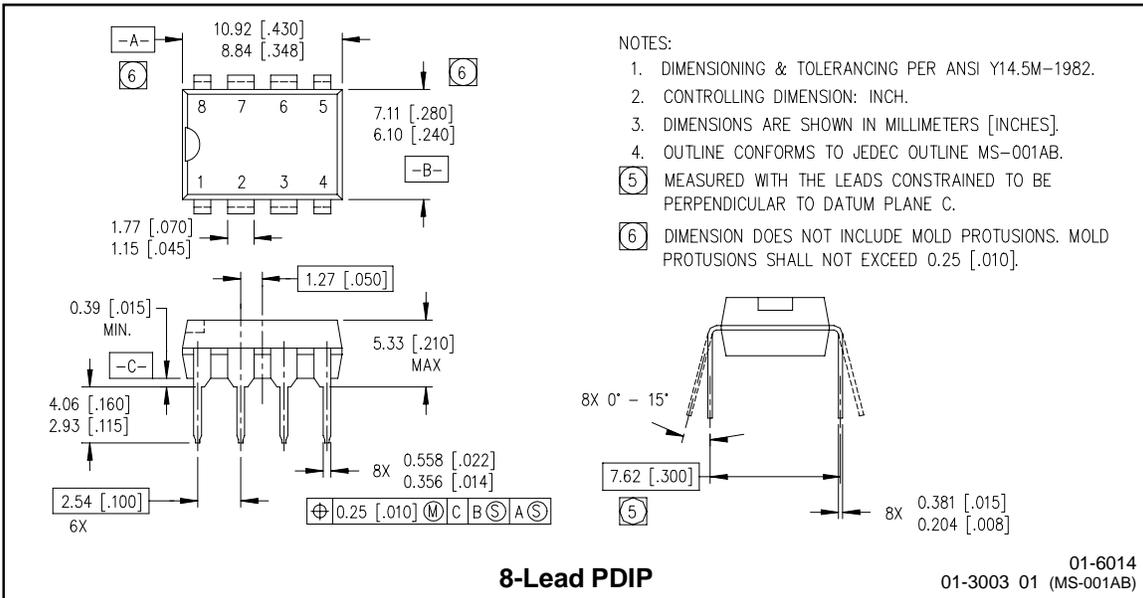


Figure 23B. Output Sink Current vs Voltage

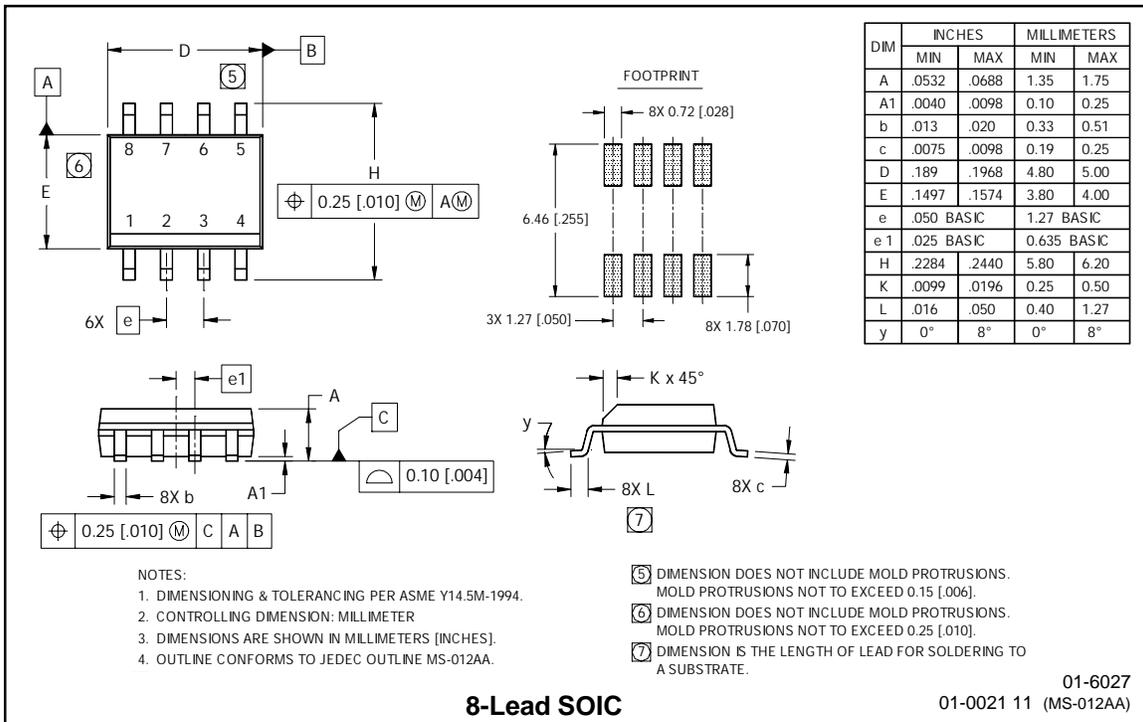
IR2103(S) & (PbF)

International
IR Rectifier

Case outlines

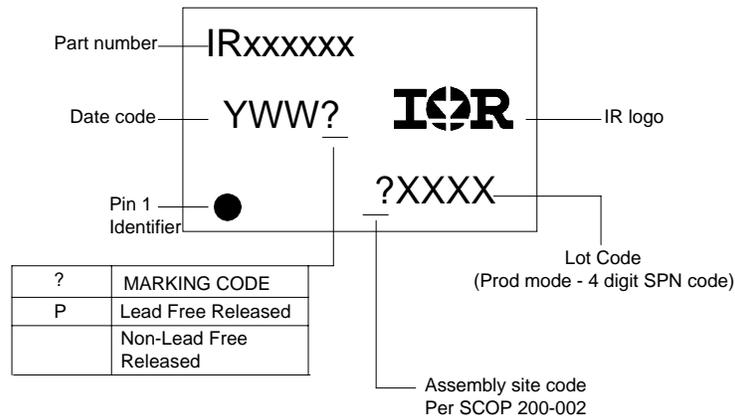


8-Lead PDIP



8-Lead SOIC

LEADFREE PART MARKING INFORMATION



ORDER INFORMATION

Basic Part (Non-Lead Free)

8-Lead PDIP IR2103 order IR2103
 8-Lead SOIC IR2103S order IR2103S

Leadfree Part

8-Lead PDIP IR2103 order IR2103PbF
 8-Lead SOIC IR2103S order IR2103SPbF

IR2104(S)&(PbF)

HALF-BRIDGE DRIVER

Features

- Floating channel designed for bootstrap operation
Fully operational to +600V
Tolerant to negative transient voltage
dV/dt immune
- Gate drive supply range from 10 to 20V
- Undervoltage lockout
- 3.3V, 5V and 15V input logic compatible
- Cross-conduction prevention logic
- Internally set deadtime
- High side output in phase with input
- Shut down input turns off both channels
- Matched propagation delay for both channels
- Also available LEAD-FREE

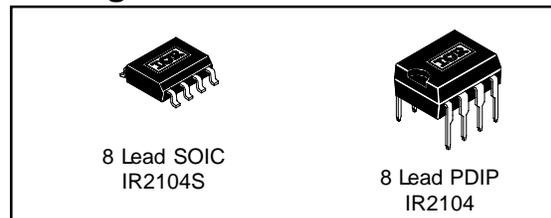
Description

The IR2104(S) are high voltage, high speed power MOSFET and IGBT drivers with dependent high and low side referenced output channels. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard CMOS or LSTTL output, down to 3.3V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates from 10 to 600 volts.

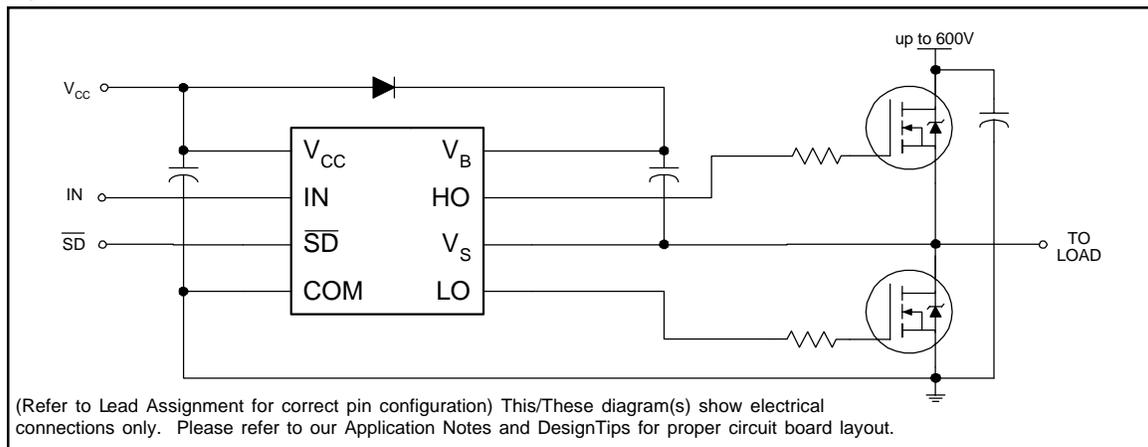
Product Summary

V_{OFFSET}	600V max.
$I_{\text{O}+/-}$	130 mA / 270 mA
V_{OUT}	10 - 20V
$t_{\text{on/off (typ.)}}$	680 & 150 ns
Deadtime (typ.)	520 ns

Packages



Typical Connection



IR2104(S)&(PbF)

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units	
V _B	High side floating absolute voltage	-0.3	625	V	
V _S	High side floating supply offset voltage	V _B - 25	V _B + 0.3		
V _{HO}	High side floating output voltage	V _S - 0.3	V _B + 0.3		
V _{CC}	Low side and logic fixed supply voltage	-0.3	25		
V _{LO}	Low side output voltage	-0.3	V _{CC} + 0.3		
V _{IN}	Logic input voltage (IN & \overline{SD})	-0.3	V _{CC} + 0.3		
dV _S /dt	Allowable offset supply voltage transient	—	50	V/ns	
P _D	Package power dissipation @ T _A ≤ +25°C	(8 lead PDIP)	—	1.0	W
		(8 lead SOIC)	—	0.625	
R _{thJA}	Thermal resistance, junction to ambient	(8 lead PDIP)	—	125	°C/W
		(8 lead SOIC)	—	200	
T _J	Junction temperature	—	150	°C	
T _S	Storage temperature	-55	150		
T _L	Lead temperature (soldering, 10 seconds)	—	300		

Recommended Operating Conditions

The Input/Output logic timing diagram is shown in Figure 1. For proper operation the device should be used within the recommended conditions. The V_S offset rating is tested with all supplies biased at 15V differential.

Symbol	Definition	Min.	Max.	Units
V _B	High side floating supply absolute voltage	V _S + 10	V _S + 20	V
V _S	High side floating supply offset voltage	Note 1	600	
V _{HO}	High side floating output voltage	V _S	V _B	
V _{CC}	Low side and logic fixed supply voltage	10	20	
V _{LO}	Low side output voltage	0	V _{CC}	
V _{IN}	Logic input voltage (IN & \overline{SD})	0	V _{CC}	
T _A	Ambient temperature	-40	125	°C

Note 1: Logic operational for V_S of -5 to +600V. Logic state held for V_S of -5V to -V_{BS}. (Please refer to the Design Tip DT97-3 for more details).

Dynamic Electrical Characteristics

V_{BIAS} (V_{CC} , V_{BS}) = 15V, C_L = 1000 pF and T_A = 25°C unless otherwise specified.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
t_{on}	Turn-on propagation delay	—	680	820	ns	$V_S = 0V$
t_{off}	Turn-off propagation delay	—	150	220		$V_S = 600V$
t_{sd}	Shutdown propagation delay	—	160	220		
t_r	Turn-on rise time	—	100	170		
t_f	Turn-off fall time	—	50	90		
DT	Deadtime, LS turn-off to HS turn-on & HS turn-on to LS turn-off	400	520	650		
MT	Delay matching, HS & LS turn-on/off	—	—	60		

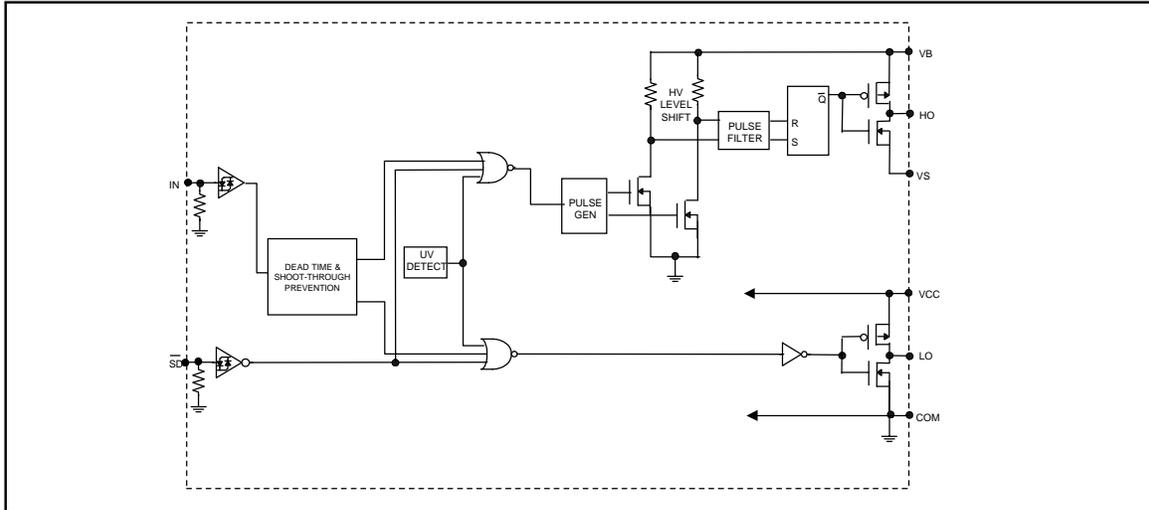
Static Electrical Characteristics

V_{BIAS} (V_{CC} , V_{BS}) = 15V and T_A = 25°C unless otherwise specified. The V_{IN} , V_{TH} and I_{IN} parameters are referenced to COM. The V_O and I_O parameters are referenced to COM and are applicable to the respective output leads: HO or LO.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
V_{IH}	Logic "1" (HO) & Logic "0" (LO) input voltage	3	—	—	V	$V_{CC} = 10V$ to 20V
V_{IL}	Logic "0" (HO) & Logic "1" (LO) input voltage	—	—	0.8		$V_{CC} = 10V$ to 20V
$V_{SD,TH+}$	SD input positive going threshold	3	—	—		$V_{CC} = 10V$ to 20V
$V_{SD,TH-}$	SD input negative going threshold	—	—	0.8		$V_{CC} = 10V$ to 20V
V_{OH}	High level output voltage, $V_{BIAS} - V_O$	—	—	100	mV	$I_O = 0A$
V_{OL}	Low level output voltage, V_O	—	—	100		$I_O = 0A$
I_{LK}	Offset supply leakage current	—	—	50	μA	$V_B = V_S = 600V$
I_{QBS}	Quiescent V_{BS} supply current	—	30	55		$V_{IN} = 0V$ or 5V
I_{QCC}	Quiescent V_{CC} supply current	—	150	270		$V_{IN} = 0V$ or 5V
I_{IN+}	Logic "1" input bias current	—	3	10		$V_{IN} = 5V$
I_{IN-}	Logic "0" input bias current	—	—	1		$V_{IN} = 0V$
V_{CCUV+}	V_{CC} supply undervoltage positive going threshold	8	8.9	9.8	V	
V_{CCUV-}	V_{CC} supply undervoltage negative going threshold	7.4	8.2	9		
I_{O+}	Output high short circuit pulsed current	130	210	—	mA	$V_O = 0V$ $PW \leq 10 \mu s$
I_{O-}	Output low short circuit pulsed current	270	360	—		$V_O = 15V$ $PW \leq 10 \mu s$

IR2104(S)&(PbF)

Functional Block Diagram



Lead Definitions

Symbol	Description
IN	Logic input for high and low side gate driver outputs (HO and LO), in phase with HO
\overline{SD}	Logic input for shutdown
V_B	High side floating supply
HO	High side gate drive output
V_S	High side floating supply return
V_{CC}	Low side and logic fixed supply
LO	Low side gate drive output
COM	Low side return

Lead Assignments

<p>8 Lead PDIP</p>	<p>8 Lead SOIC</p>
IR2104	IR2104S

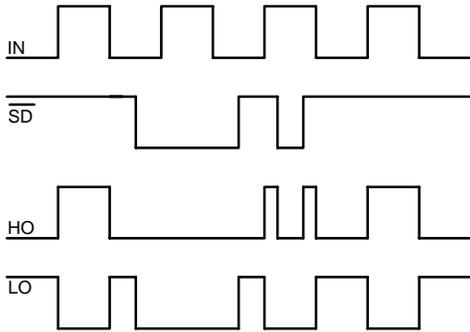


Figure 1. Input/Output Timing Diagram

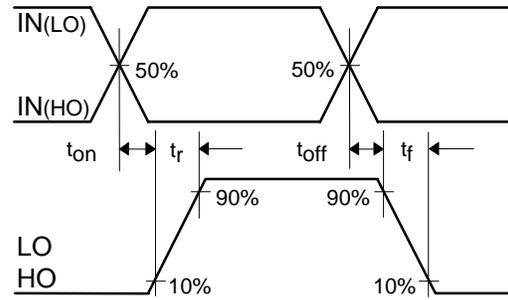


Figure 2. Switching Time Waveform Definitions

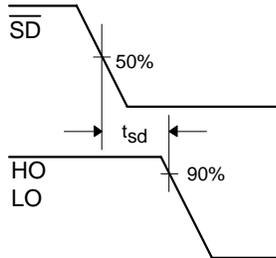


Figure 3. Shutdown Waveform Definitions

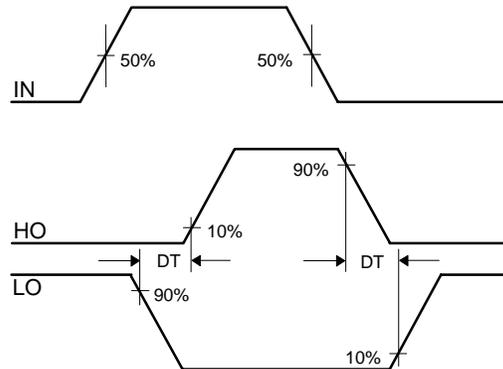


Figure 4. Deadtime Waveform Definitions

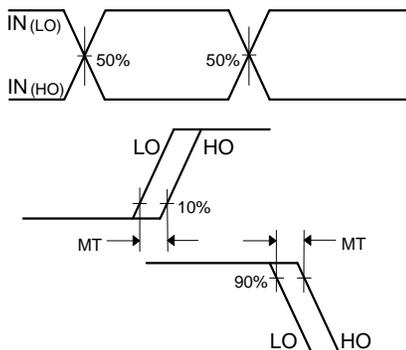


Figure 5. Delay Matching Waveform Definitions

IR2104(S)&(PbF)

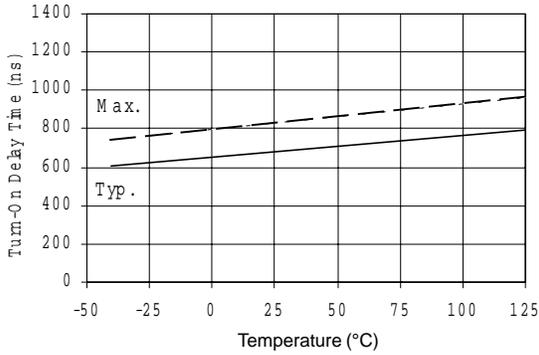


Figure 6A. Turn-On Time vs Temperature

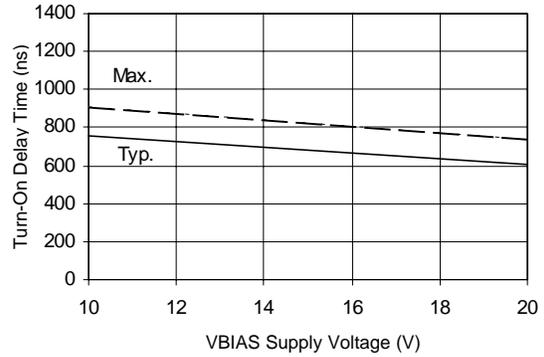


Figure 6B. Turn-On Time vs Supply Voltage

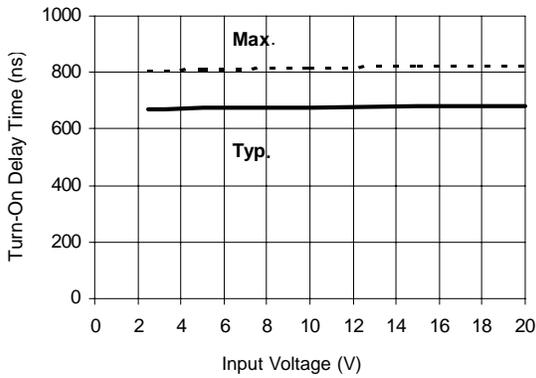


Figure 6C. Turn-On Time vs Input Voltage

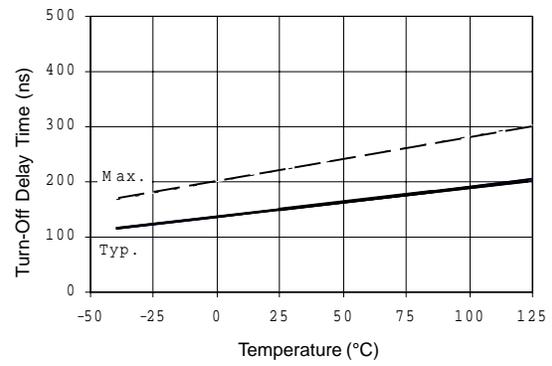


Figure 7A. Turn-Off Time vs Temperature

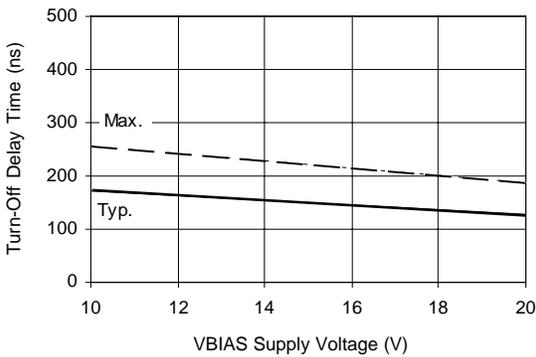


Figure 7B. Turn-Off Time vs Supply Voltage

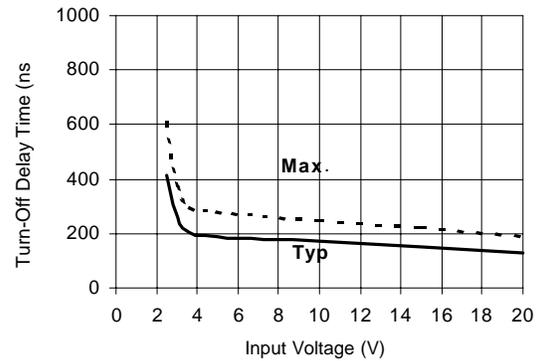


Figure 7C. Turn-Off Time vs Input Voltage

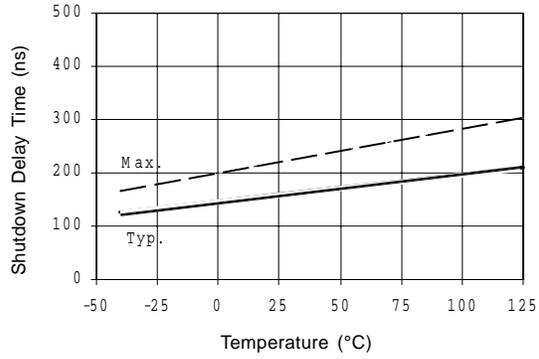


Figure 8A. Shutdown Time vs Temperature

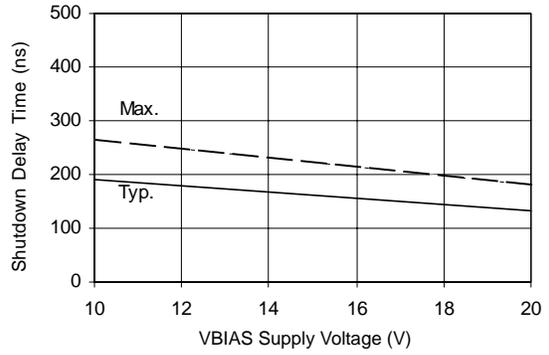


Figure 8B. Shutdown Time vs Voltage

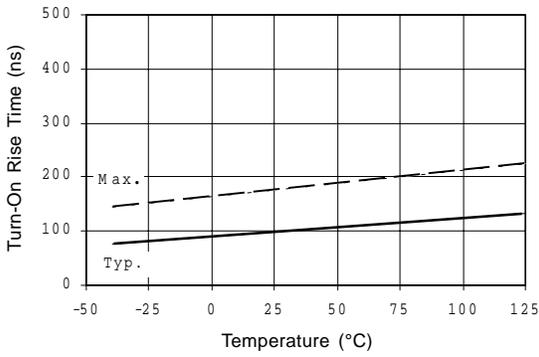


Figure 9A. Turn-On Rise Time vs Temperature

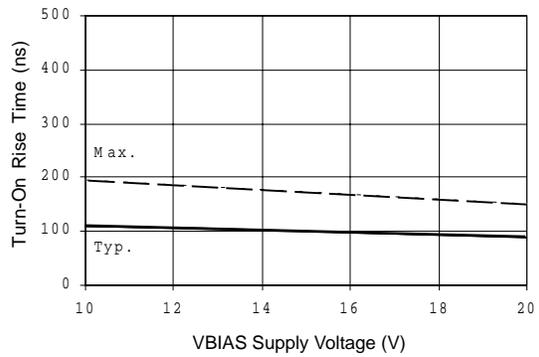


Figure 9B. Turn-On Rise Time vs Voltage

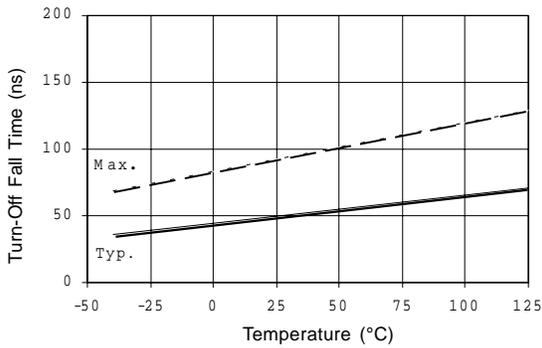


Figure 10A. Turn-Off Fall Time vs Temperature

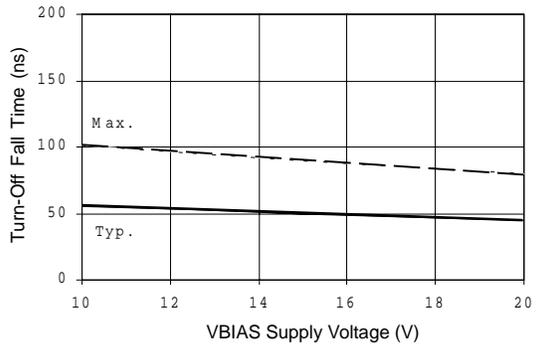


Figure 10B. Turn-Off Fall Time vs Voltage

IR2104(S)&(PbF)

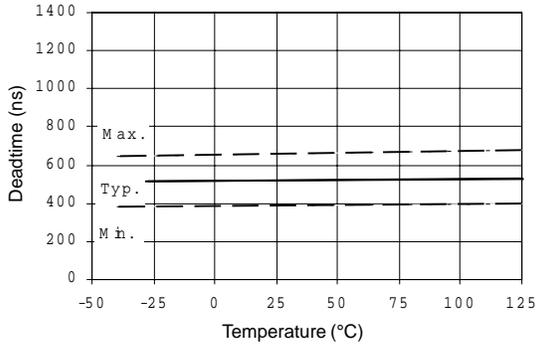


Figure 11A. Deadtime vs Temperature

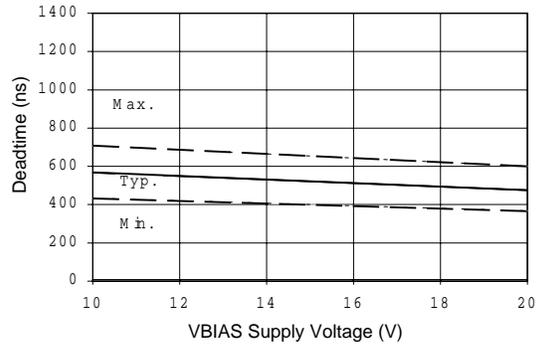


Figure 11B. Deadtime vs Voltage

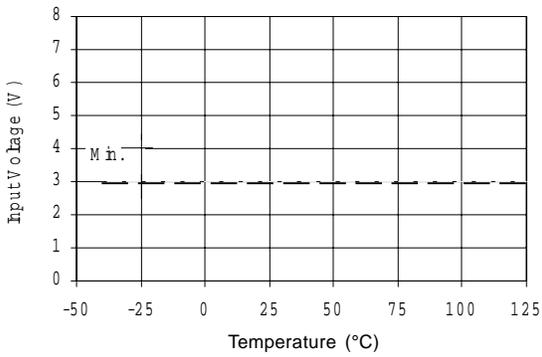


Figure 12A. Logic "1" (HO) & Logic "0" (LO) & Inactive SD Input Voltage vs Temperature

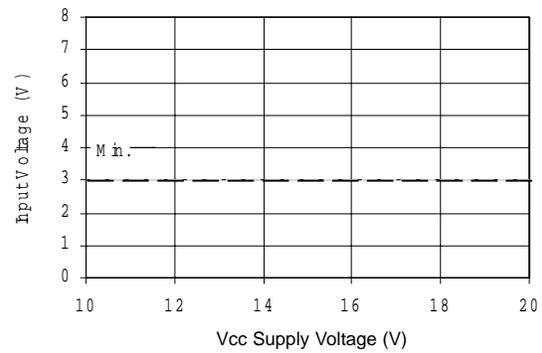


Figure 12B. Logic "1" (HO) & Logic "0" (LO) & Inactive SD Input Voltage vs Voltage

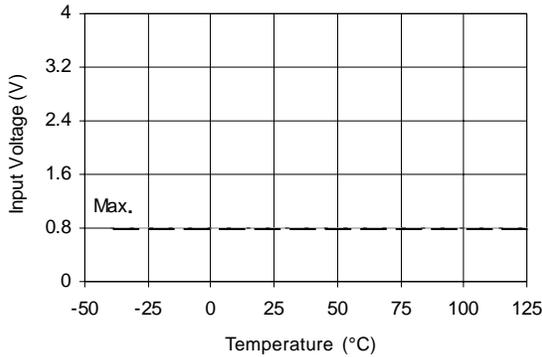


Figure 13A. Logic "0" (HO) & Logic "1" (LO) & Active SD Input Voltage vs Temperature

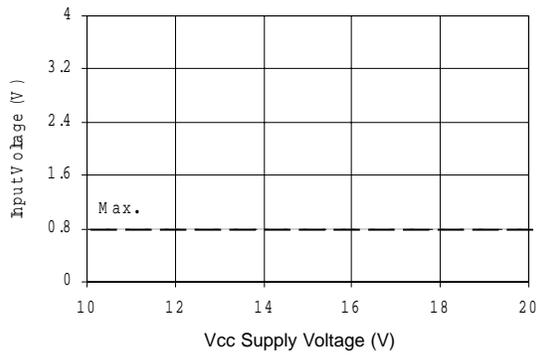


Figure 13B. Logic "0" (HO) & Logic "1" (LO) & Active SD Input Voltage vs Voltage

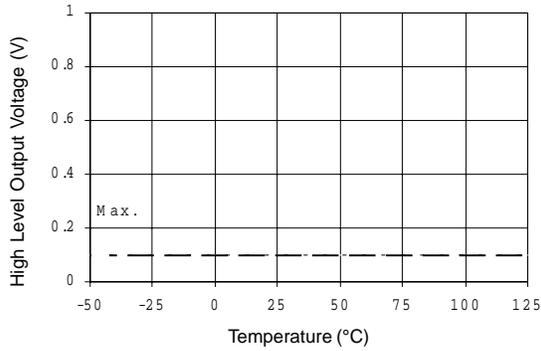


Figure 14A. High Level Output vs Temperature

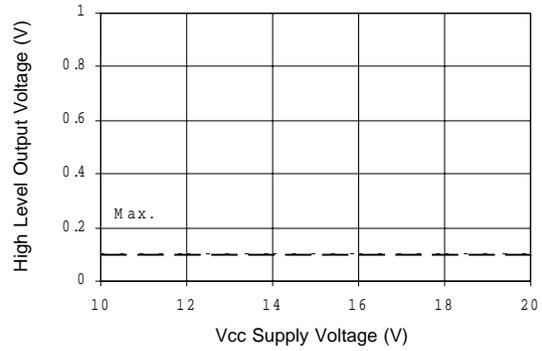


Figure 14B. High Level Output vs Voltage

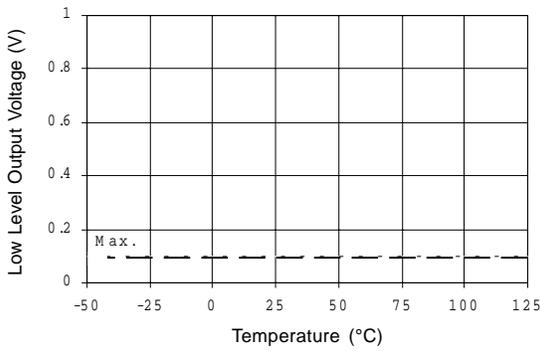


Figure 15A. Low Level Output vs Temperature

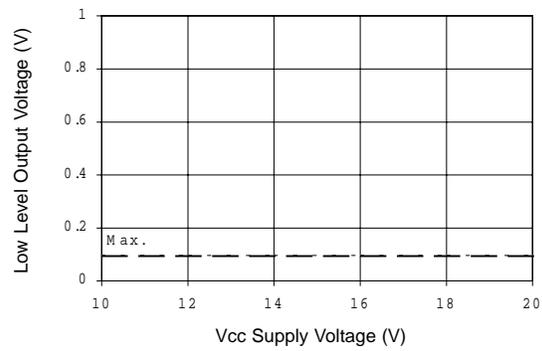


Figure 15B. Low level Output vs Voltage

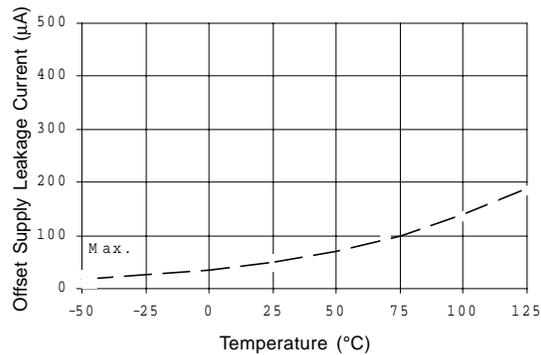


Figure 16A. Offset Supply Current vs Temperature

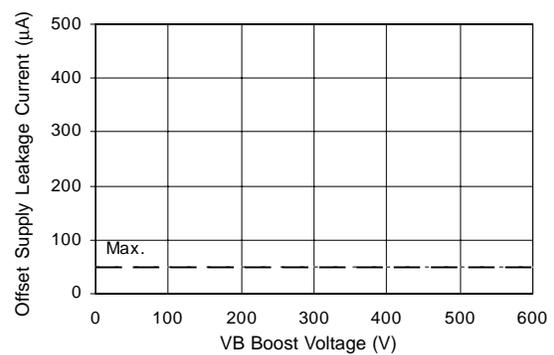


Figure 16B. Offset Supply Current vs Voltage

IR2104(S)&(PbF)

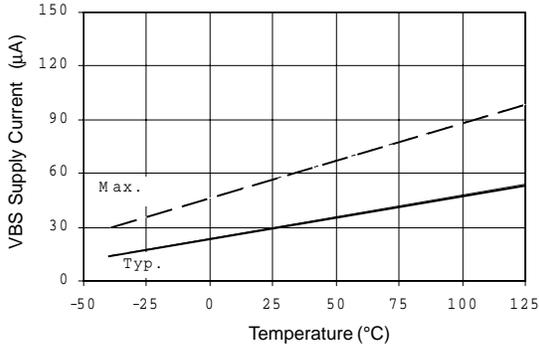


Figure 17A. V_{BS} Supply Current vs Temperature

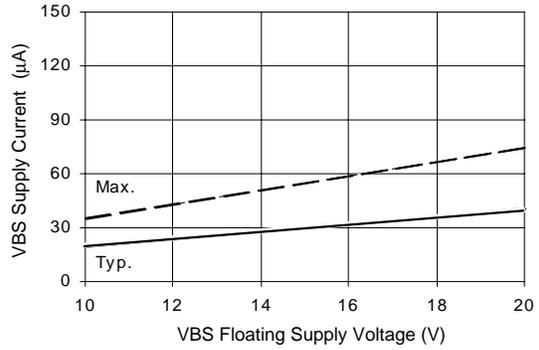


Figure 17B. V_{BS} Supply Current vs Voltage

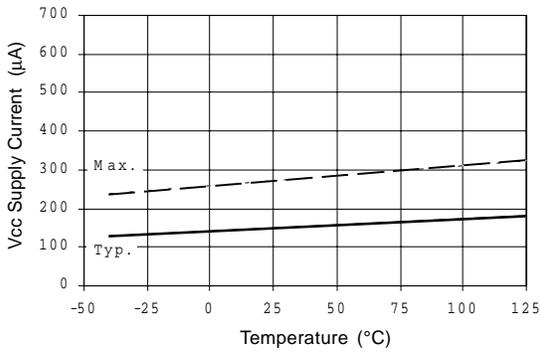


Figure 18A. V_{CC} Supply Current vs Temperature

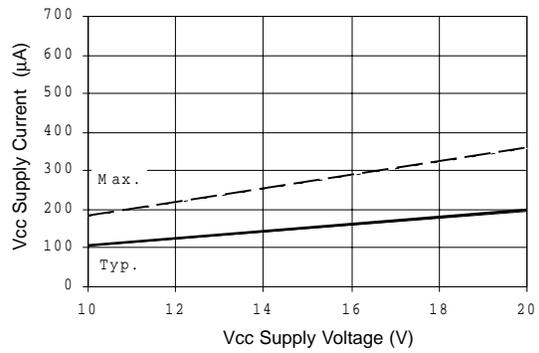


Figure 18B. V_{CC} Supply Current vs Voltage

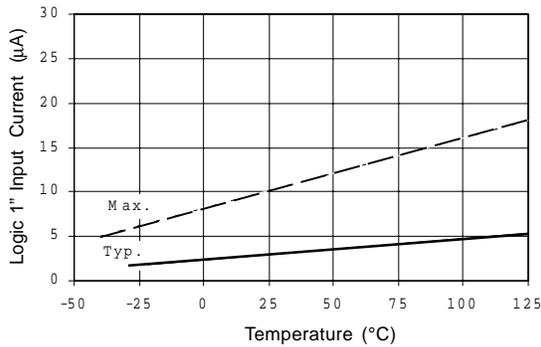


Figure 19A. Logic "1" Input Current vs Temperature

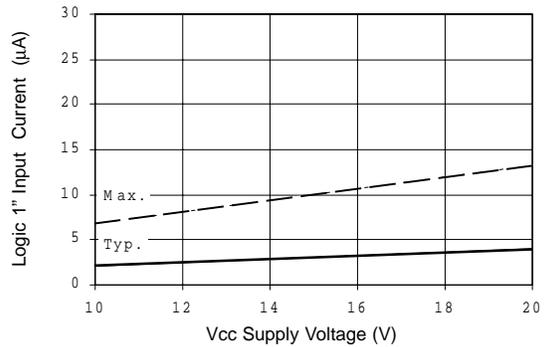


Figure 19B. Logic "1" Input Current vs Voltage

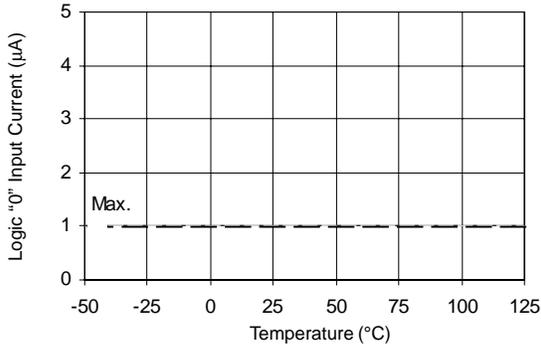


Figure 20A. Logic "0" Input Current vs Temperature

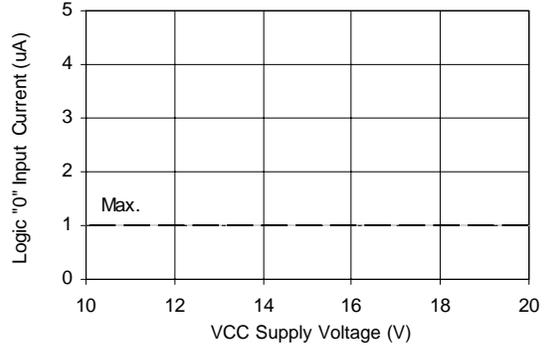


Figure 20B. Logic "0" Input Current vs Voltage

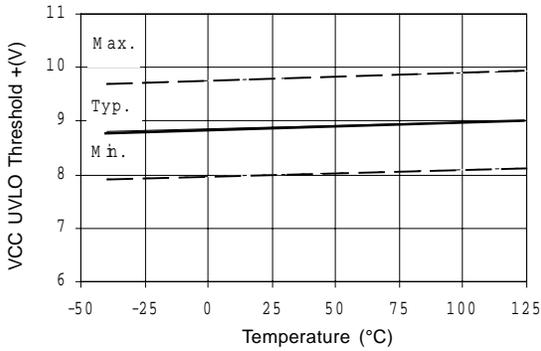


Figure 21A. Vcc Undervoltage Threshold(+) vs Temperature

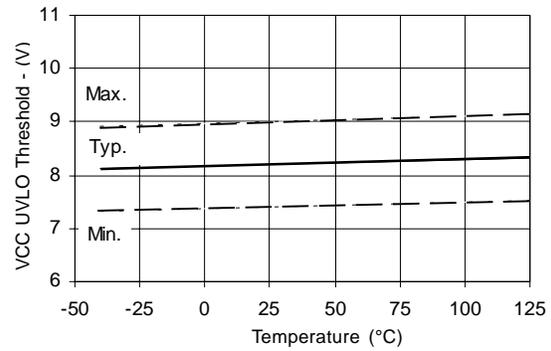


Figure 21B. Vcc Undervoltage Threshold(-) vs Temperature

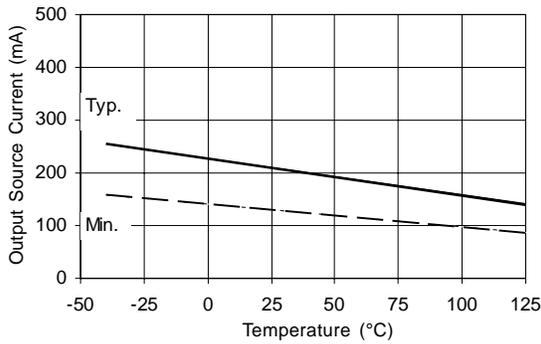


Figure 22A. Output Source Current vs Temperature

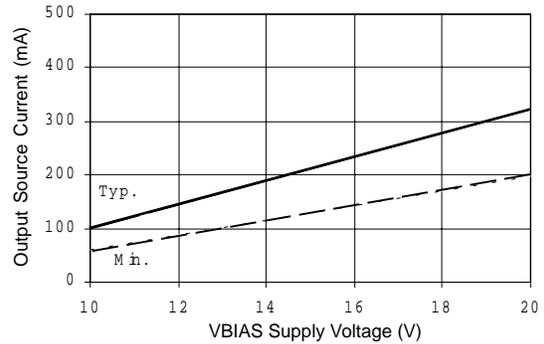


Figure 22B. Output Source Current vs Voltage

IR2104(S)&(PbF)

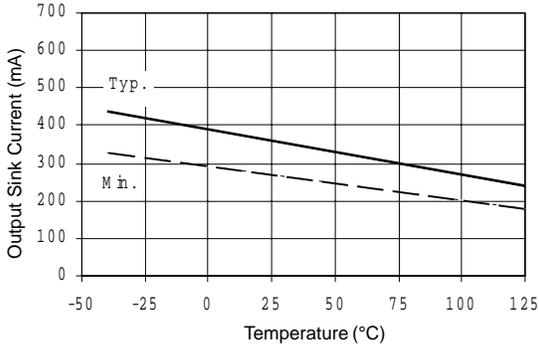


Figure 23A. Output Sink Current vs Temperature

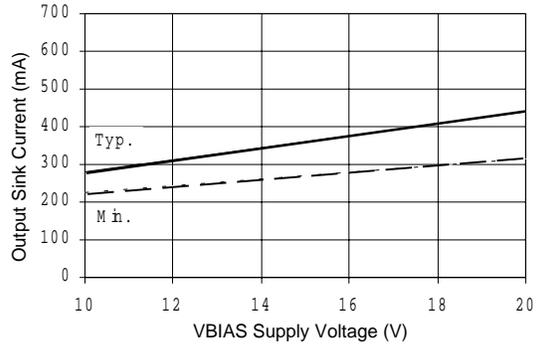
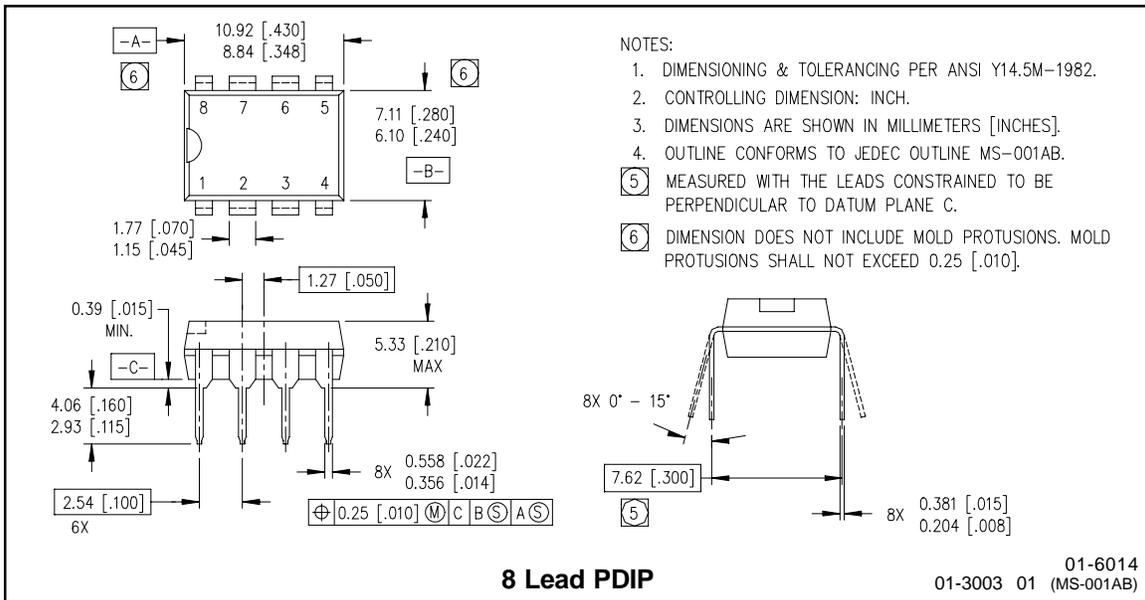
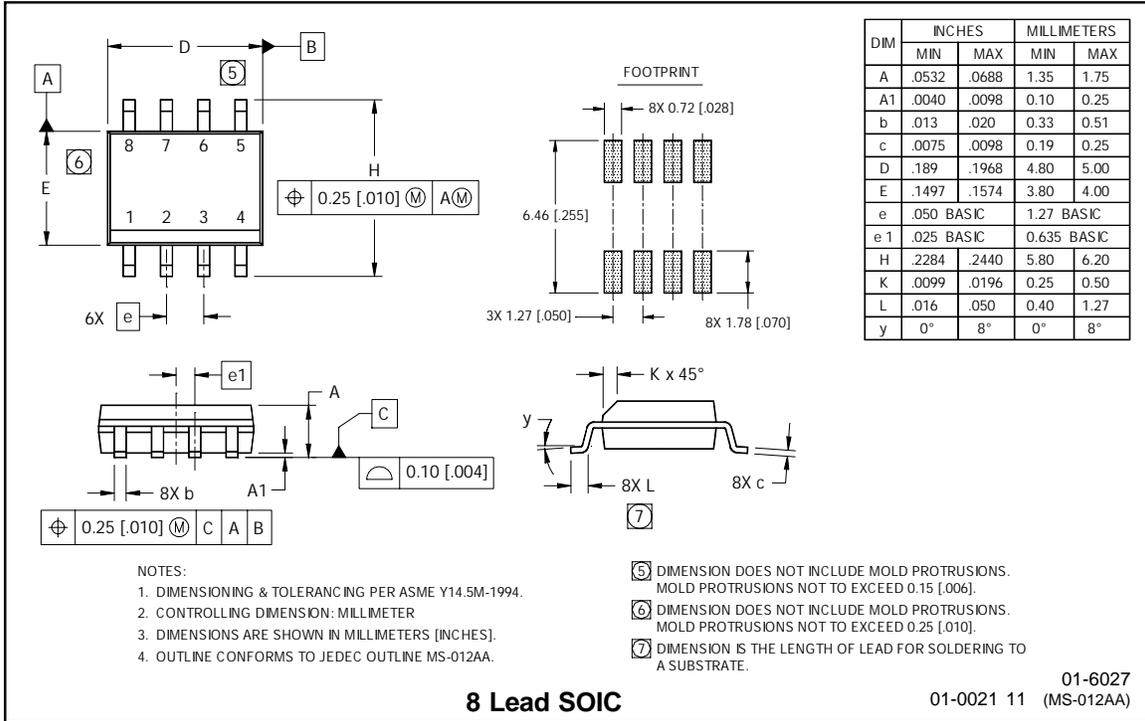


Figure 23B. Output Sink Current vs Voltage

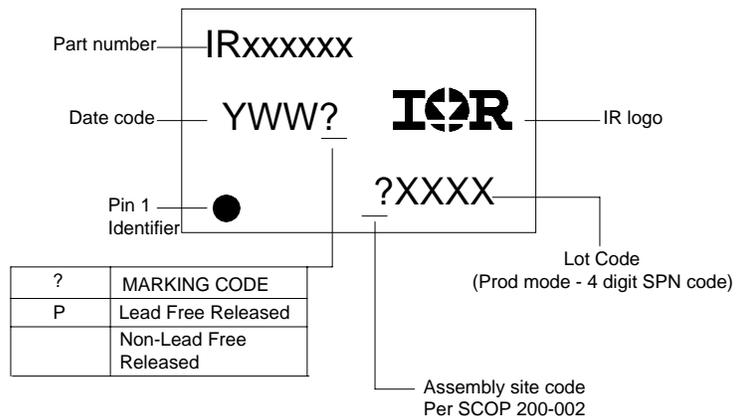
Case Outlines





IR2104(S)&(PbF)

LEADFREE PART MARKING INFORMATION



ORDER INFORMATION

Basic Part (Non-Lead Free)

8-Lead PDIP IR2104 order IR2104
8-Lead SOIC IR2104S order IR2104S

Leadfree Part

8-Lead PDIP IR2104 order IR2104PbF
8-Lead SOIC IR2104S order IR2104SPbF

IR2111(S) & (PbF)

HALF-BRIDGE DRIVER

Features

- Floating channel designed for bootstrap operation
Fully operational to +600V
Tolerant to negative transient voltage
dV/dt immune
- Gate drive supply range from 10 to 20V
- Undervoltage lockout for both channels
- CMOS Schmitt-triggered inputs with pull-down
- Matched propagation delay for both channels
- Internally set deadtime
- High side output in phase with input
- Also available LEAD-FREE

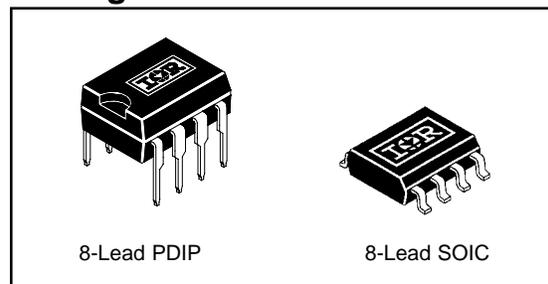
Description

The IR2111(S) is a high voltage, high speed power MOSFET and IGBT driver with dependent high and low side referenced output channels designed for half-bridge applications. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. Logic input is compatible with standard CMOS outputs. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. Internal deadtime is provided to avoid shoot-through in the output half-bridge. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 600 volts.

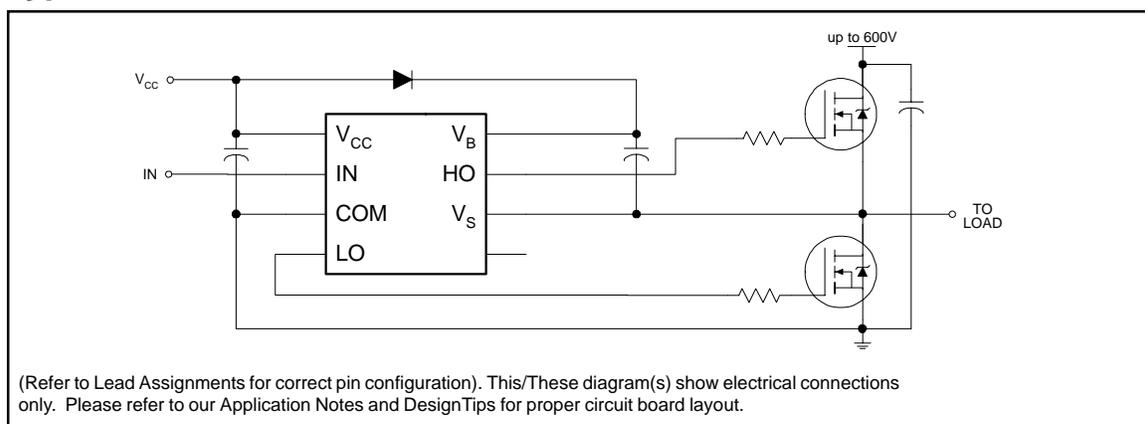
Product Summary

V_{OFFSET}	600V max.
$I_{\text{O}+/-}$	200 mA / 420 mA
V_{OUT}	10 - 20V
$t_{\text{on/off}}$ (typ.)	750 & 150 ns
Deadtime (typ.)	650 ns

Packages



Typical Connection



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Additional information is shown in figures 7 through 10.

Symbol	Definition	Min.	Max.	Units	
V _B	High side floating supply voltage	-0.3	625	V	
V _S	High side floating supply offset voltage	V _B - 25	V _B + 0.3		
V _{HO}	High side floating output voltage	V _S - 0.3	V _B + 0.3		
V _{CC}	Low side and logic fixed supply voltage	-0.3	25		
V _{LO}	Low side output voltage	-0.3	V _{CC} + 0.3		
V _{IN}	Logic input voltage	-0.3	V _{CC} + 0.3		
dV _S /dt	Allowable offset supply voltage transient (figure 2)	—	50	V/ns	
P _D	Package power dissipation @ T _A ≤ +25°C	(8 Lead PDIP)	—	1.0	W
		(8 lead SOIC)	—	0.625	
R _{thJA}	Thermal resistance, junction to ambient	(8 lead PDIP)	—	125	°C/W
		(8 lead SOIC)	—	200	
T _J	Junction temperature	—	150	°C	
T _S	Storage temperature	-55	150		
T _L	Lead temperature (soldering, 10 seconds)	—	300		

Recommended Operating Conditions

The input/output logic timing diagram is shown in figure 1. For proper operation the device should be used within the recommended conditions. The V_S offset rating is tested with all supplies biased at 15V differential.

Symbol	Definition	Min.	Max.	Units
V _B	High side floating supply absolute voltage	V _S + 10	V _S + 20	V
V _S	High side floating supply offset voltage	Note 1	600	
V _{HO}	High side floating output voltage	V _S	V _B	
V _{CC}	Low side and logic fixed supply voltage	10	20	
V _{LO}	Low side output voltage	0	V _{CC}	
V _{IN}	Logic input voltage	0	V _{CC}	
T _A	Ambient temperature	-40	125	°C

Note 1: Logic operational for V_S of -5 to +600V. Logic state held for V_S of -5V to -V_{BS}. (Please refer to the Design Tip DT97-3 for more details).

Dynamic Electrical Characteristics

V_{BIAS} (V_{CC} , V_{BS}) = 15V, C_L = 1000 pF and T_A = 25°C unless otherwise specified. The dynamic electrical characteristics are measured using the test circuit shown in figure 3.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
t_{on}	Turn-on propagation delay	550	750	950	ns	$V_S = 0V$
t_{off}	Turn-off propagation delay	—	150	180		$V_S = 600V$
t_r	Turn-on rise time	—	80	130		
t_f	Turn-off fall time	—	40	65		
DT	Deadtime, LS turn-off to HS turn-on & HS turn-off to LS turn-on	480	650	820		
MT	Delay matching, HS & LS turn-on/off	—	30	—		

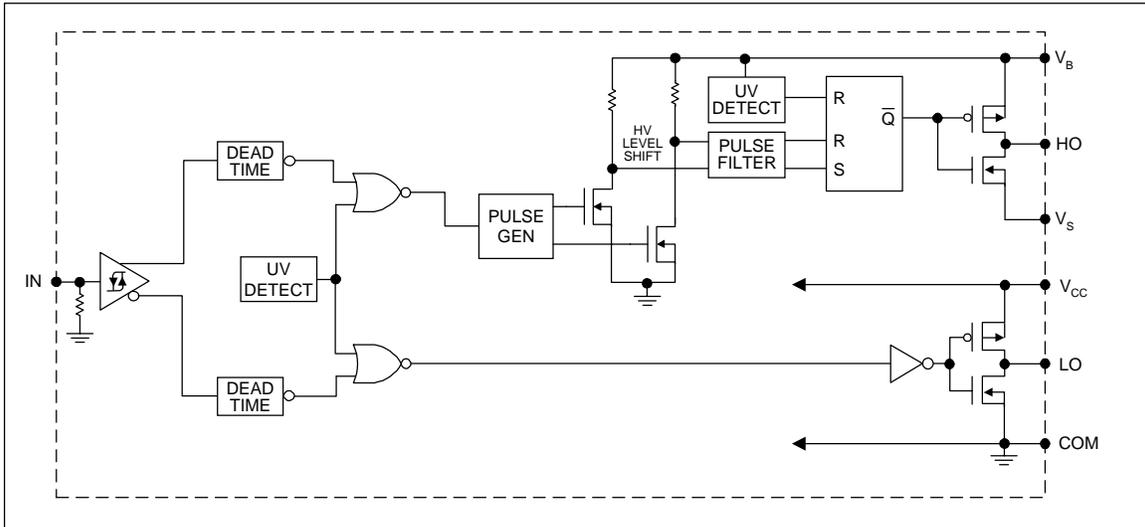
Static Electrical Characteristics

V_{BIAS} (V_{CC} , V_{BS}) = 15V and T_A = 25°C unless otherwise specified. The V_{IN} , V_{TH} and I_{IN} parameters are referenced to COM. The V_O and I_O parameters are referenced to COM and are applicable to the respective output leads: HO or LO.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
V_{IH}	Logic "1" input voltage for HO & logic "0" for LO	6.4	—	—	V	$V_{CC} = 10V$
		9.5	—	—		$V_{CC} = 15V$
		12.6	—	—		$V_{CC} = 20V$
V_{IL}	Logic "0" input voltage for HO & logic "1" for LO	—	—	3.8		$V_{CC} = 10V$
		—	—	6.0		$V_{CC} = 15V$
		—	—	8.3		$V_{CC} = 20V$
V_{OH}	High level output voltage, $V_{BIAS} - V_O$	—	—	100	mV	$I_O = 0A$
V_{OL}	Low level output voltage, V_O	—	—	100		$I_O = 0A$
I_{LK}	Offset supply leakage current	—	—	50	μA	$V_B = V_S = 600V$
I_{QBS}	Quiescent V_{BS} supply current	—	50	100		$V_{IN} = 0V$ or V_{CC}
I_{QCC}	Quiescent V_{CC} supply current	—	70	180		$V_{IN} = 0V$ or V_{CC}
I_{IN+}	Logic "1" input bias current	—	30	50		$V_{IN} = V_{CC}$
I_{IN-}	Logic "0" input bias current	—	—	1.0		$V_{IN} = 0V$
V_{BSUV+}	V_{BS} supply undervoltage positive going threshold	7.6	8.6	9.6	V	
V_{BSUV-}	V_{BS} supply undervoltage negative going threshold	7.2	8.2	9.2		
V_{CCUV+}	V_{CC} supply undervoltage positive going threshold	7.6	8.6	9.6		
V_{CCUV-}	V_{CC} supply undervoltage negative going threshold	7.2	8.2	9.2		
I_{O+}	Output high short circuit pulsed current	200	250	—	mA	$V_O = 0V$, $V_{IN} = V_{CC}$ $PW \leq 10 \mu s$
I_{O-}	Output low short circuit pulsed current	420	500	—		$V_O = 15V$, $V_{IN} = 0V$ $PW \leq 10 \mu s$

IR2111(S) & (PbF)

Functional Block Diagram



Lead Definitions

Symbol	Description
IN	Logic input for high side and low side gate driver outputs (HO & LO), in phase with HO
V _B	High side floating supply
HO	High side gate drive output
V _S	High side floating supply return
V _{CC}	Low side and logic fixed supply
LO	Low side gate drive output
COM	Low side return

Lead Assignments

<p>8 Lead DIP</p>	<p>8 Lead SOIC</p>
IR2111	IR2111S
Part Number	

IR2111(S) & (PbF)

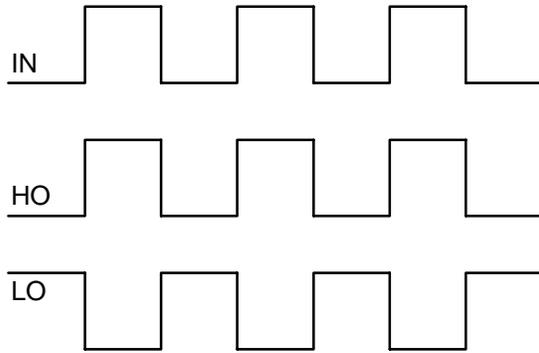


Figure 1. Input/Output Timing Diagram

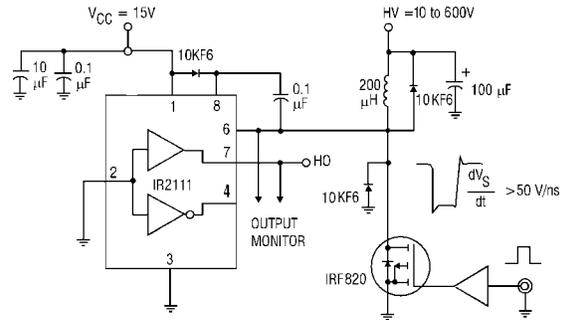


Figure 2. Floating Supply Voltage Transient Test Circuit

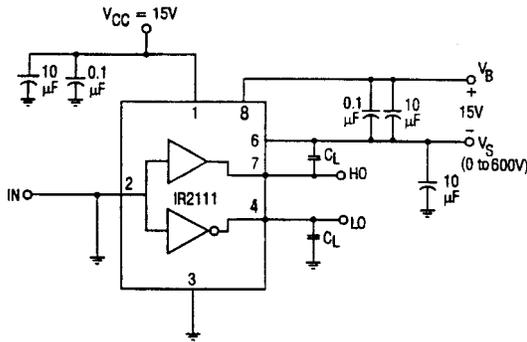


Figure 3. Switching Time Test Circuit

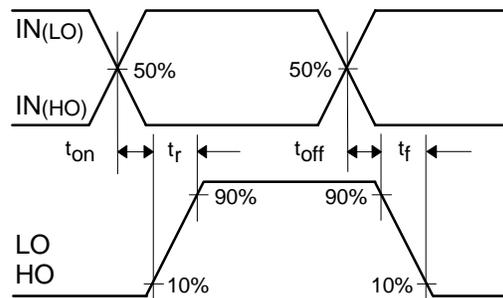


Figure 4. Switching Time Waveform Definition

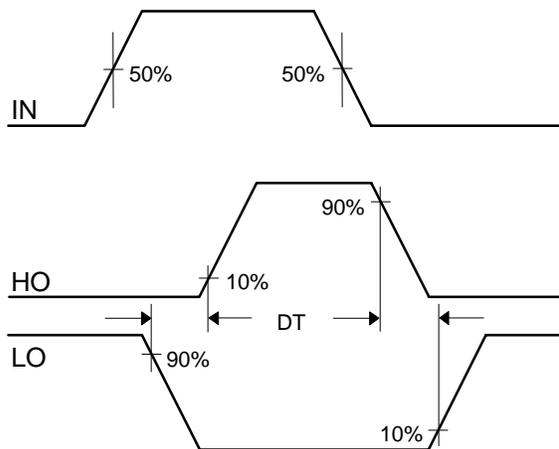


Figure 5. Deadtime Waveform Definitions

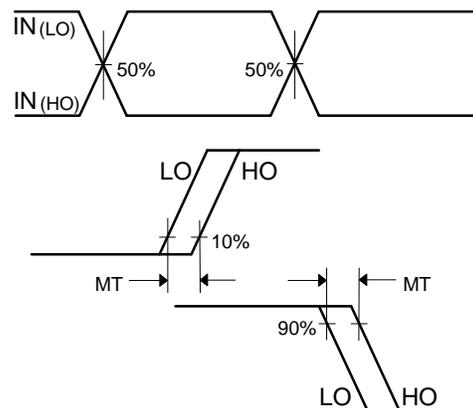


Figure 6. Delay Matching Waveform Definitions

IR2111(S) & (PbF)

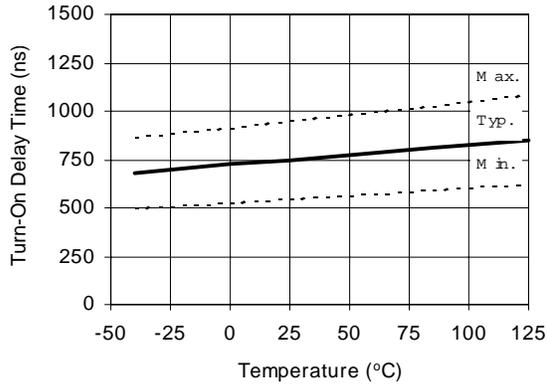


Figure 11A Turn-On Time vs Temperature

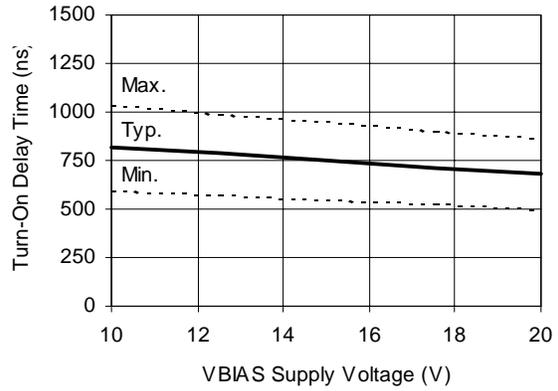


Figure 11B Turn-On Time vs Voltage

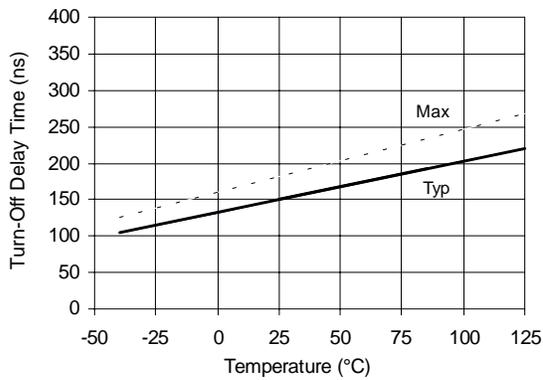


Figure 12A Turn-Off Time vs Temperature

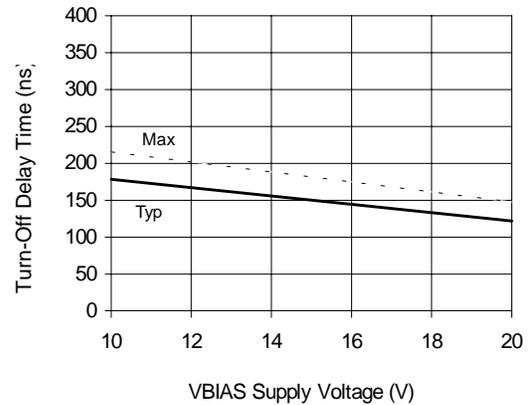


Figure 12B Turn-Off Time vs Voltage

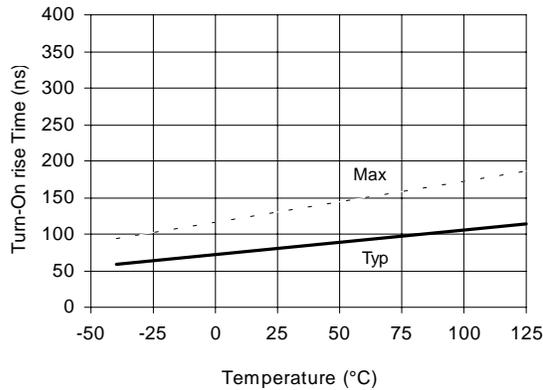


Figure 13A Turn-On Rise Time vs Temperature

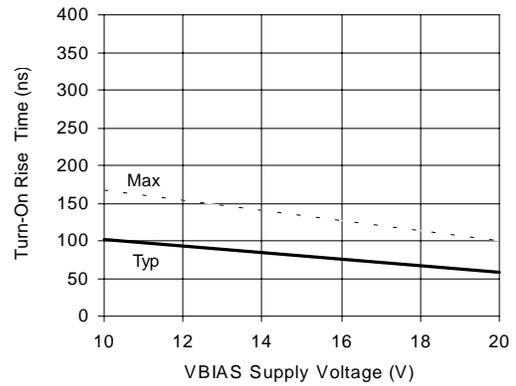


Figure 13B Turn-On Rise Time vs Voltage

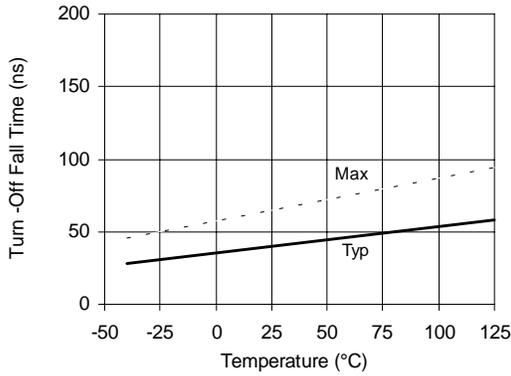


Figure 14A Turn-Off Fall Time vs Temperature

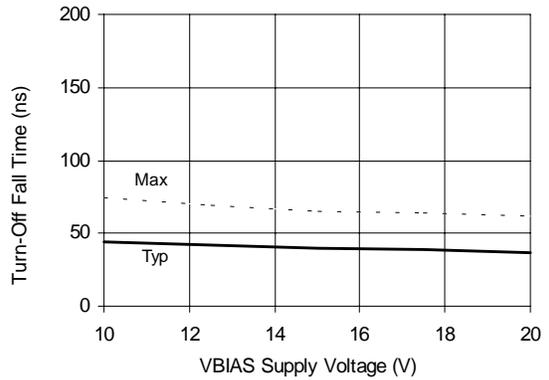


Figure 14B Turn-Off Fall Time vs Voltage

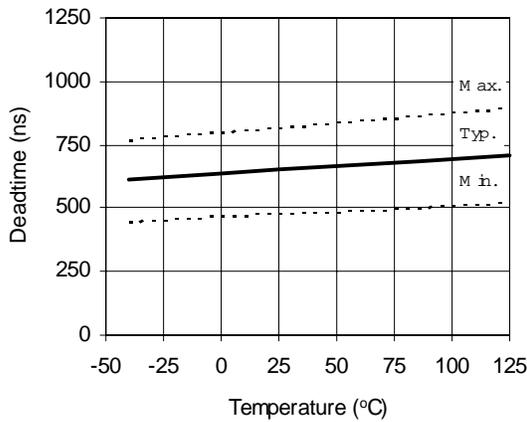


Figure 15A Dead Time vs Temperature

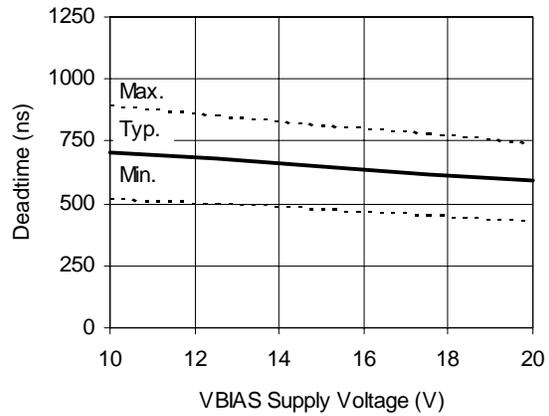


Figure 15B Dead Time vs Voltage

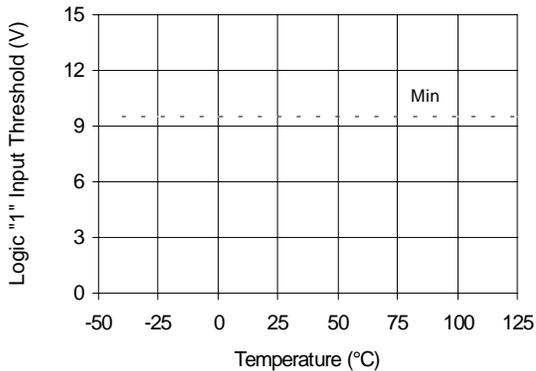


Figure 16A Logic "1" Input voltage for HO & Logic "0" for LO vs Temperature

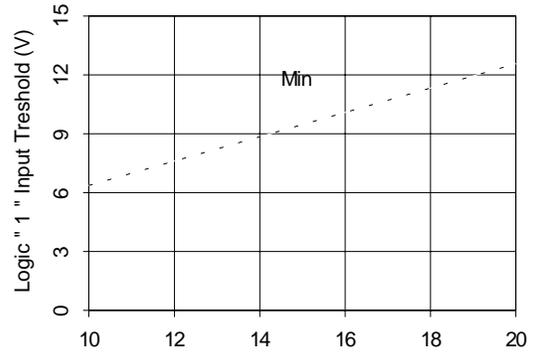


Figure 16B Logic "1" Input voltage for HO & Logic "0" for LO vs Voltage

IR2111(S) & (PbF)

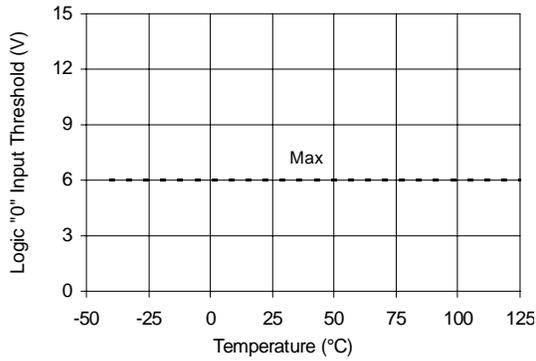


Figure 17A Logic "0" Input voltage for HO & Logic "1" for LO vs Temperature

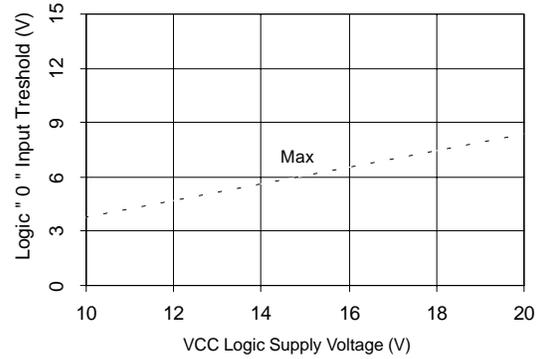


Figure 17B Logic "0" Input voltage for HO & Logic "1" for LO vs Voltage

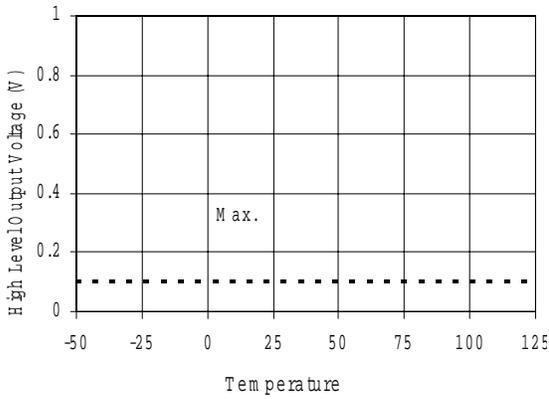


Figure 18A. High Level Output vs. Temperature

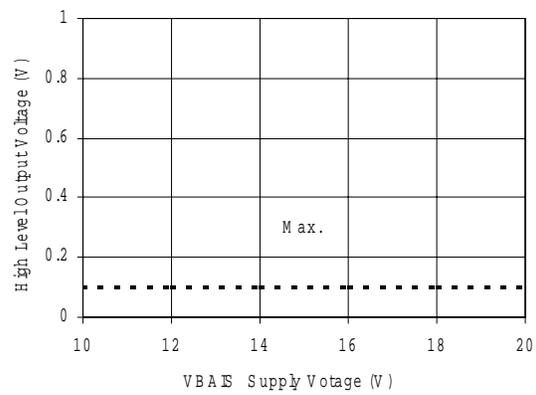


Figure 18B. High Level Output vs. Voltage

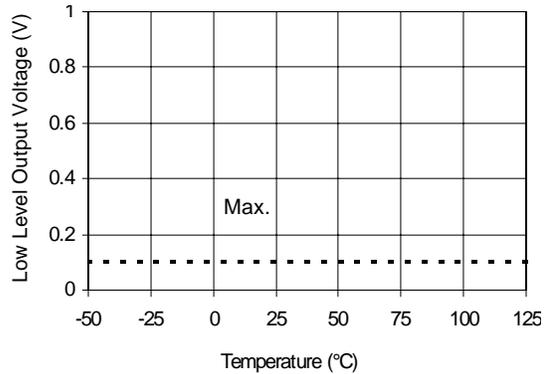


Figure 19A. Low Level Output vs. Temperature

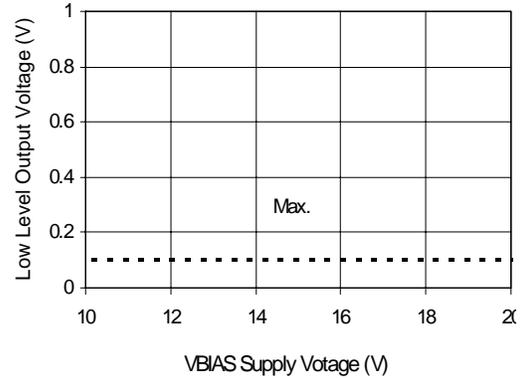


Figure 19B. Low Level Output vs. Voltage

IR2111(S) & (PbF)

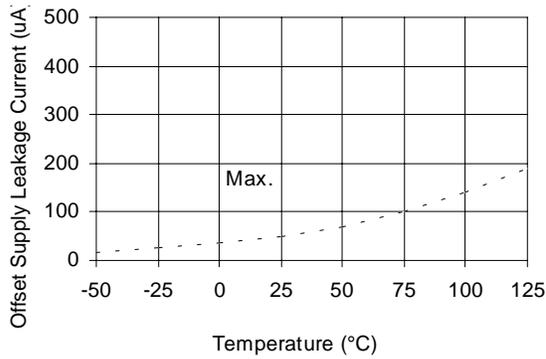


Figure 20A Offset Supply Current vs Temperature

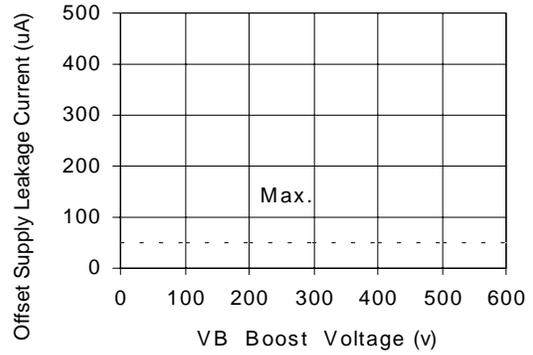


Figure 20B Offset Supply Current vs Voltage

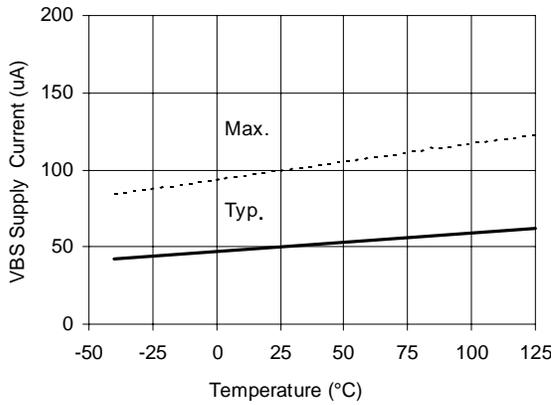


Figure 21A VBS Supply Current vs Temperature

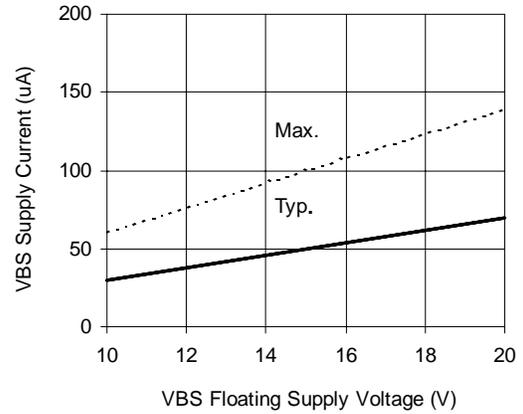


Figure 21B VBS Supply Current vs Voltage

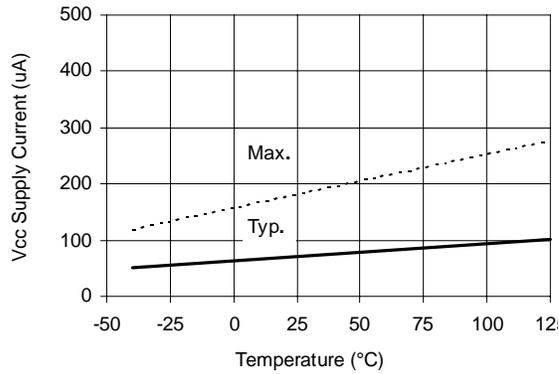


Figure 22A VCC Supply Current vs Temperature

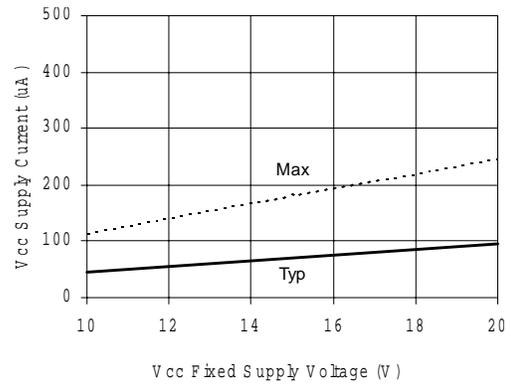


Figure 22B VCC Supply Current vs Voltage

IR2111(S) & (PbF)

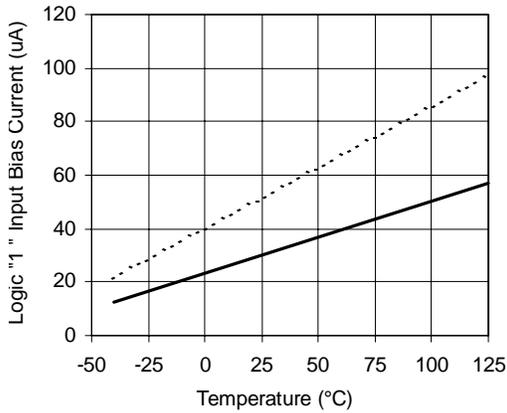


Figure 23A Logic "1" Input Current vs Temperature

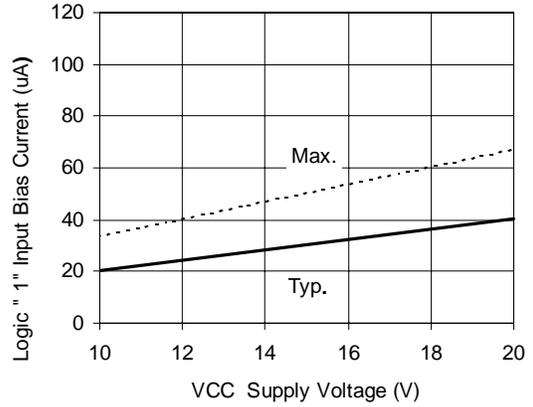


Figure 23B Logic "1" Input Current vs V_{CC} Voltage

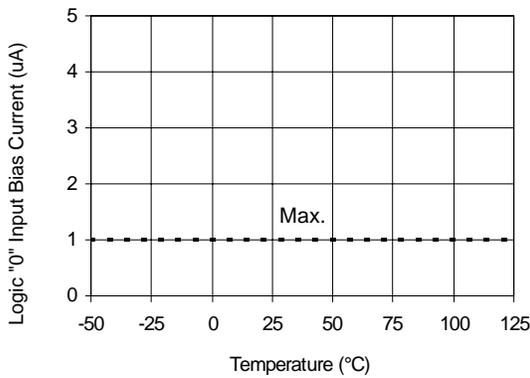


Figure 24A. Logic "0" Input Current vs. Temperature

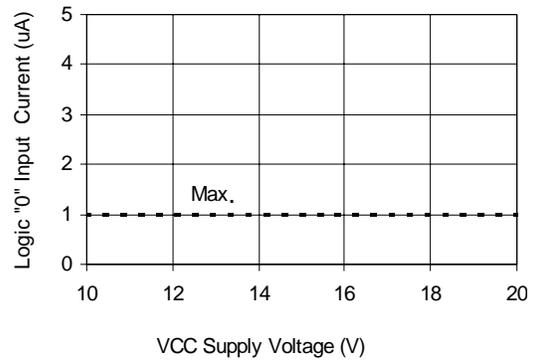


Figure 24B. Logic "0" Input Current vs. V_{CC} Voltage

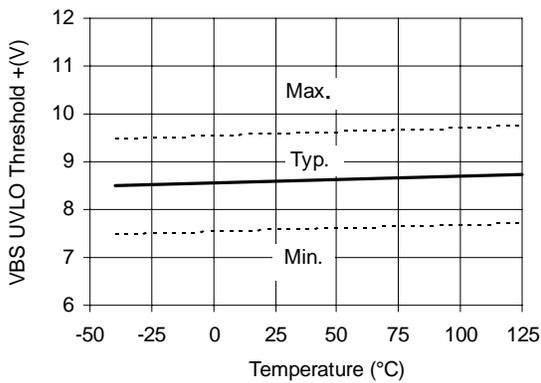


Figure 25 VBS Undervoltage Threshold (+) vs Temperature

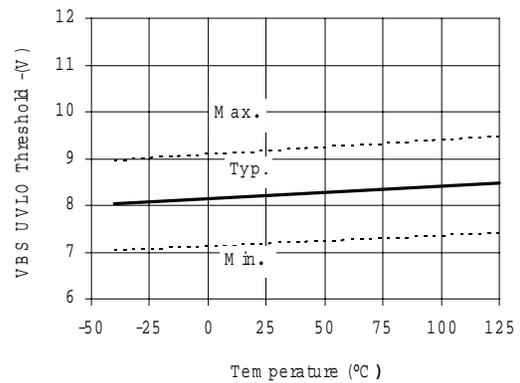


Figure 26 VBS Undervoltage Threshold (-) vs Temperature

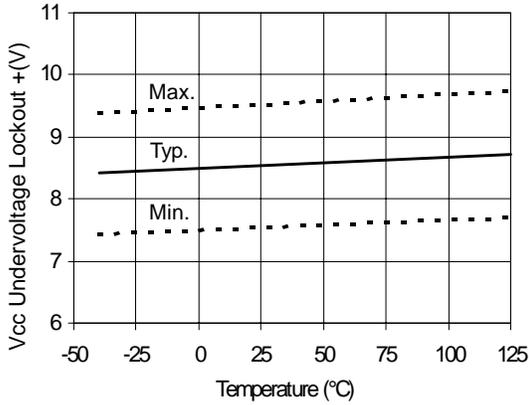


Figure 27 Vcc Undervoltage (-) vs Temperature

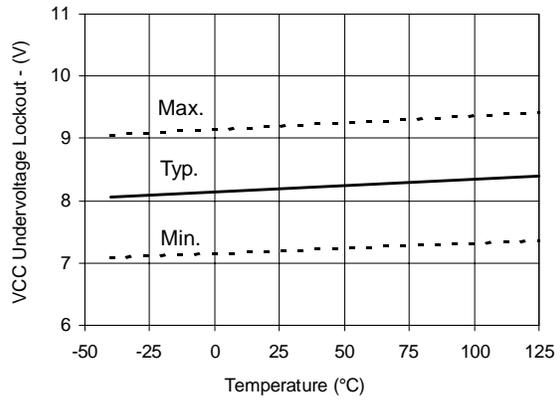


Figure 28 Vcc Undervoltage (-) vs Temperature

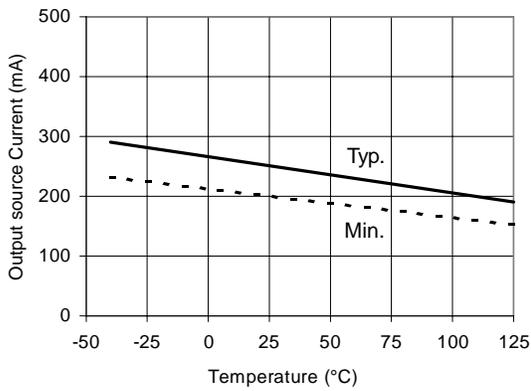


Figure 29A Output Source Current vs Temperature

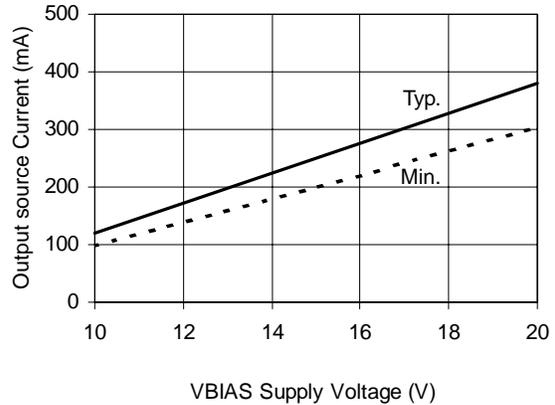


Figure 29B Output Source Current vs Voltage

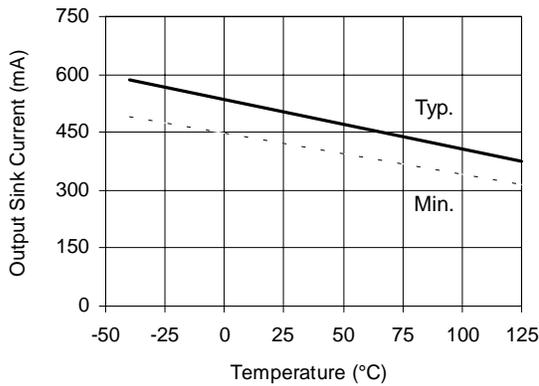


Figure 30A Output Sink Current vs Temperature

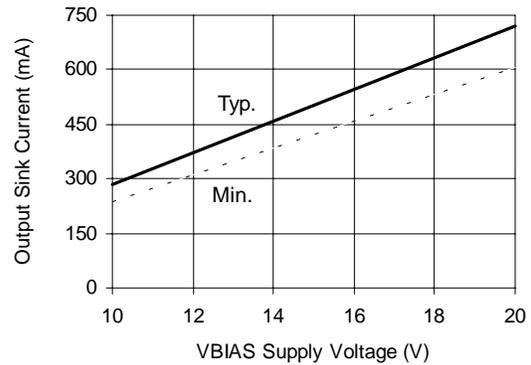


Figure 30B Output Sink Current vs Voltage

IR2111(S)&(PbF)

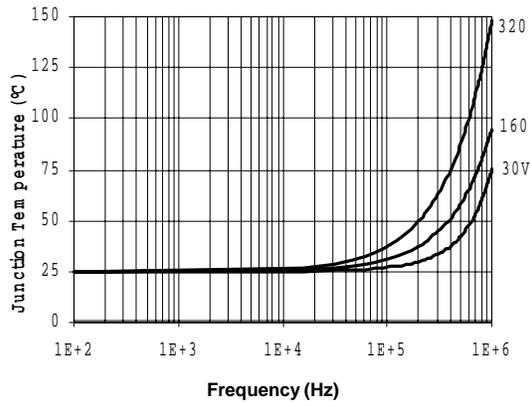


Figure 31. IR2111 T_J vs. Frequency (IRFBC20)
R_{GATE} = 33Ω, V_{CC} = 15V

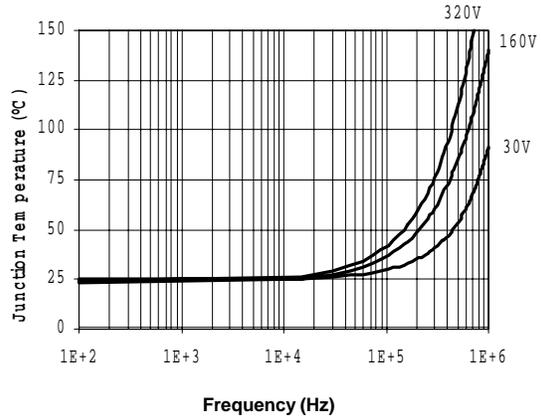


Figure 32. IR2111 T_J vs. Frequency (IRFBC30)
R_{GATE} = 22Ω, V_{CC} = 15V

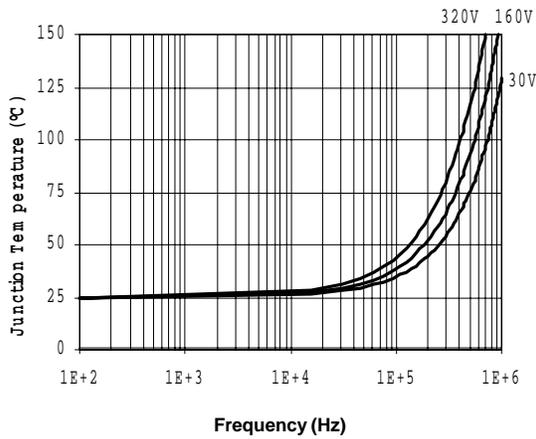


Figure33. IR2111 T_J vs. Frequency (IRFBC40)
R_{GATE} = 15Ω, V_{CC} = 15V

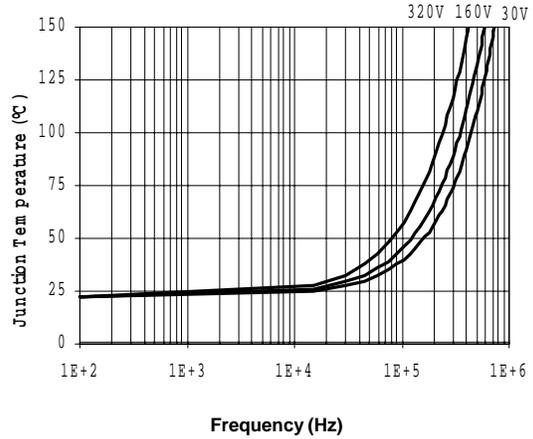


Figure 34. IR2111 T_J vs. Frequency (IRFPC50)
R_{GATE} = 10Ω, V_{CC} = 15V

IR2111(S) & (PbF)

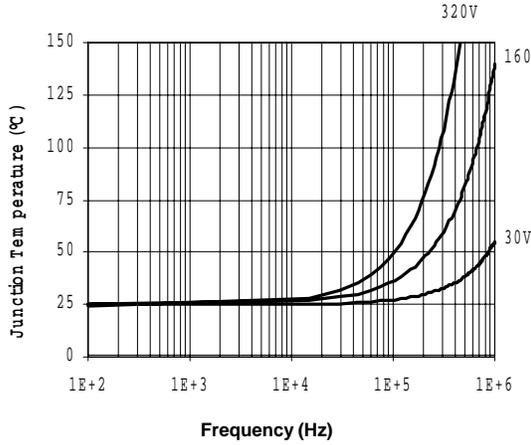


Figure 35. IR2111S T_J vs. Frequency (IRFBC20)
R_{GATE} = 33Ω, V_{CC} = 15V

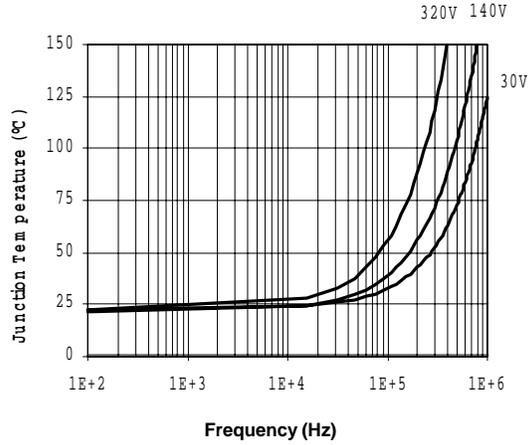


Figure 36. IR2111S T_J vs. Frequency (IRFBC30)
R_{GATE} = 22Ω, V_{CC} = 15V

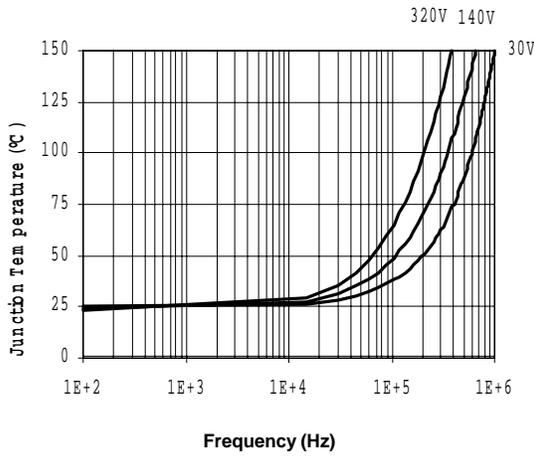


Figure 37. IR2111S T_J vs. Frequency (IRFBC40)
R_{GATE} = 15Ω, V_{CC} = 15V

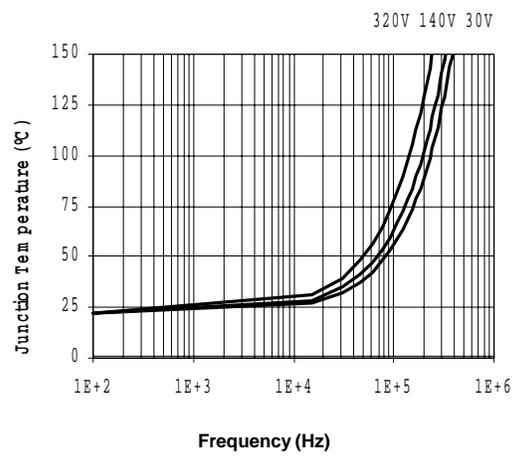
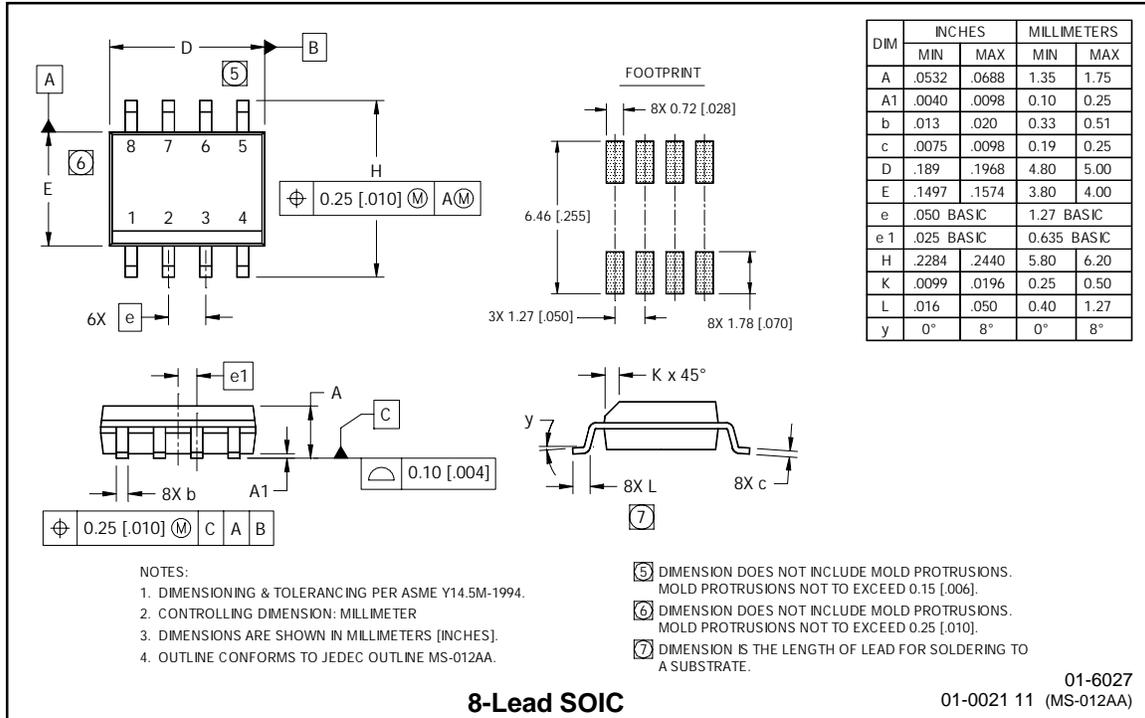
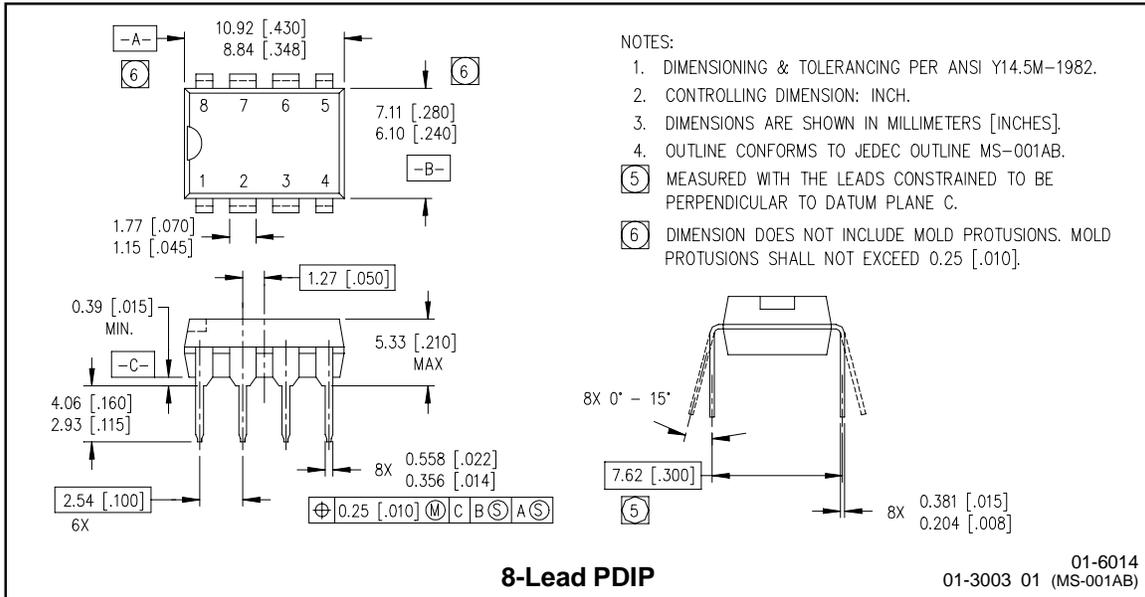


Figure 38. IR2111S T_J vs. Frequency (IRFPC50)
R_{GATE} = 10Ω, V_{CC} = 15V

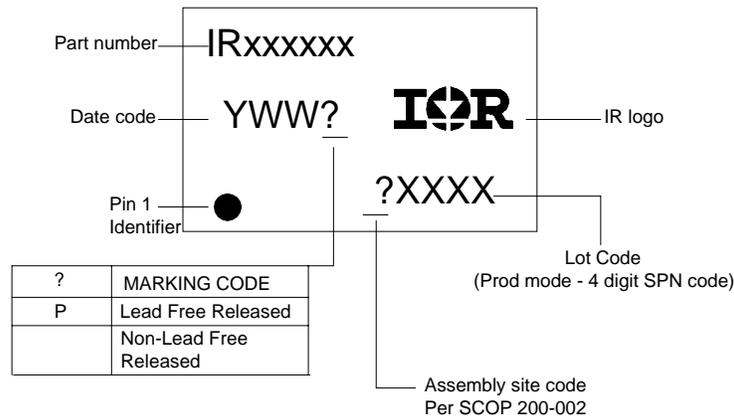
IR2111(S) & (PbF)

International
IRF Rectifier

Case outlines



LEADFREE PART MARKING INFORMATION



ORDER INFORMATION

Basic Part (Non-Lead Free)

8-Lead PDIP IR2111 order IR2111
 8-Lead SOIC IR2111S order IR2111S

Leadfree Part

8-Lead PDIP IR2111 order IR2111PbF
 8-Lead SOIC IR2111S order IR2111SPbF

LMD18200 - 3A, 55V H-Bridge

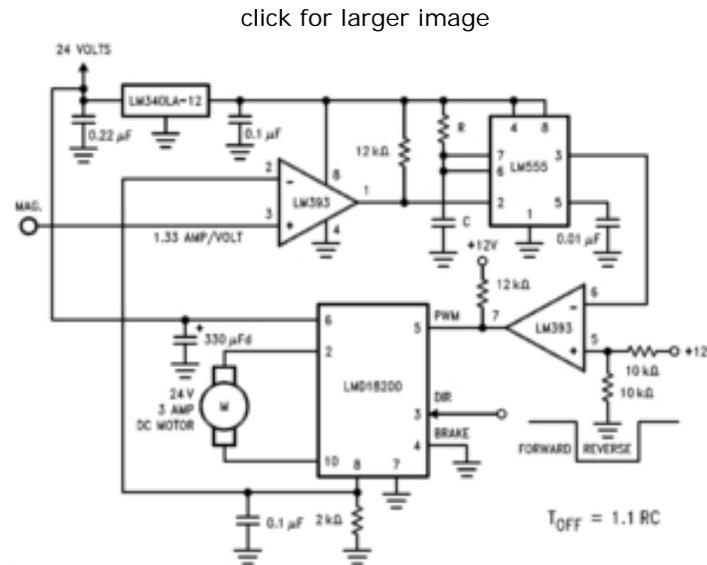


- [Datasheet](#)
- [Packaging](#)
- [Samples & Pricing](#)
- [Eval. Boards](#)
- [Reliability](#)
- [Models](#)
- [Application Notes](#)
- [Knowledge Base](#)

Features

- Delivers up to 3A continuous output
- Operates at supply voltages up to 55V
- Low $R_{DS(ON)}$ typically 0.3Ω per switch
- TTL and CMOS compatible inputs
- No "shoot-through" current
- Thermal warning flag output at 145°C
- Thermal shutdown (outputs off) at 170°C
- Internal clamp diodes
- Shorted load protection
- Internal charge pump with external bootstrap capability

Typical Application



Parametric Table [expand](#)

Channels	2 Channels
----------	------------

Also Recommended

- [LM5102](#) Dual High Side / Low Side 100V Mosfet Driver.
- [LM5104](#) Dual High Side / Low Side 100V Mosfet Driver, Single PWM Input
- [LM4652](#) Quad 8A 55V 200Mohm Mosfet In TO220 (Also Used For Class D Audio)
- [LMD18245](#) Has Current Sensing Circuitry And Digital (4 Bit DAC) Control Of Motor Current
- [LMD18201](#) LMD18200 Without Current Sensing Circuitry

Additional Resources

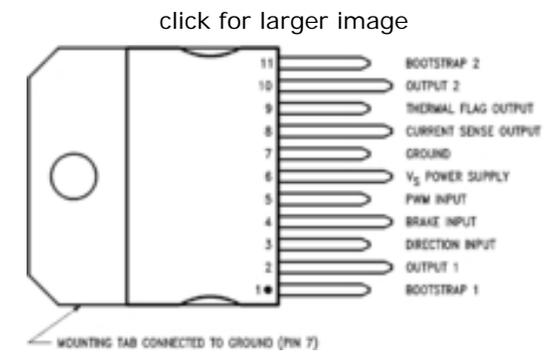
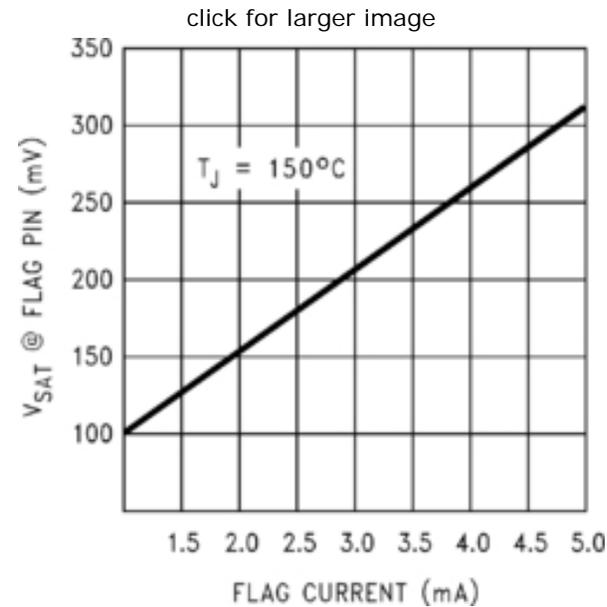
[Application Notes \(see below\)](#)

Connection Diagram

General Description

The LMD18200 is a 3A H-Bridge designed for motion control applications. [More...](#)

Typical Performance



Applications

- DC and stepper motor drives
- Position and velocity servomechanisms
- Factory automation robots
- Numerically controlled machinery
- Computer printers and plotters

Datasheet

 RoHS Compliance Information	Size in Kbytes	Date	 View Online	 Download	 Receive via Email
LMD18200 3A, 55V H-Bridge	880 Kbytes	2-May-05	View Online	Download	Receive via Email
LMD18200 3A, 55V H-Bridge (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	396 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる
LMD18200 Mil-Aero Datasheet MNLMD18200-2-X	81 Kbytes		View Online	Download	Receive via Email

If you have trouble printing or viewing PDF file(s), see [Printing Problems](#).

Package Availability, Models, Samples & Pricing

Package	Factory Lead Time	Samples &	Budgetary Pricing	Std	Package
---------	-------------------	-----------	-------------------	-----	---------

Part Number	Type	Pins	Spec.	MSL Rating	Peak Reflow	RoHS Report	Weeks	Qty	Models	Electronic Orders	Qty	\$US each	Pack Size	Marking Format
LMD18200T	TO-220	11	STD	1	NA		Full production		N/A	Samples Buy Now	250 +	\$8.51	rail of 20	NSUZXYTT LMD18200T P+
			NOPB	1	NA	RoHS	12 weeks	2550						
5962-9232501MXA	CERDIP	24					Full production		N/A	Buy Now	250 +	\$141.00	rail of 15	NS ZSSXXYYA LMD18200-2D/883 Q 5962-9232501MXA
							12 weeks	336						
LMD18200 MDC	Unpackaged Die						Full production		N/A				gel pak of N/A	-
							N/A	5000						

General Description

The LMD18200 is a 3A H-Bridge designed for motion control applications. The device is built using a multi-technology process which combines bipolar and CMOS control circuitry with DMOS power devices on the same monolithic structure. Ideal for driving DC and stepper motors; the LMD18200 accommodates peak output currents up to 6A. An innovative circuit which facilitates low-loss sensing of the output current has been implemented.

Reliability Metrics

Part Number	Process	Early Failure Rate - Rejects	Sample Size (EFR)	PPM *	Rel. Rejects	Device Hours	Long Term Failure Rates (FITS)	MTTF
LMD18200 MDC	LMDMOS	0	9745	0	0	715000	5	202883167
LMD18200T	LMDMOS	0	9745	0	0	715000	5	202883167

For more information on Reliability Metrics, please click [here](#).

Application Notes

Title	Size in Kbytes	Date	 View Online	 Download	 Receive via Email
AN-558: Introduction to Power MOSFETs and Their Applications	258 Kbytes	10-Jun-96	View Online	Download	Receive via Email
Introduction to Power MOSFETs and Their Applications (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	258 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる
AN-694: Application Note 694 A DMOS 3A, 55V, H-Bridge: The LMD18200	408 Kbytes	26-Sep-02	View Online	Download	Receive via Email
Application Note 694 A DMOS 3A, 55V, H-Bridge: The LMD18200 (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	400 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる
AN-828: Application Note 828 Increasing the High Speed Torque of Bipolar Stepper Motors	808 Kbytes	1-Oct-02	View Online	Download	Receive via Email
Application Note 828 Increasing the High Speed Torque of Bipolar Stepper Motors (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	362 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる

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[Information as of 27-Sep-2006]

LMD18245 - 3A, 55V DMOS Full-Bridge Motor Driver



- [Datasheet](#)
- [Packaging](#)
- [Samples & Pricing](#)
- [Eval. Boards](#)
- [Reliability](#)
- [Models](#)
- [Application Notes](#)
- [Knowledge Base](#)

Features

- DMOS power stage rated at 55V and 3A continuous
- Low $R_{DS(ON)}$ of typically 0.3Ω per power switch
- Internal clamp diodes
- Low-loss current sensing method
- Digital or analog control of motor current
- TTL and CMOS compatible inputs
- Thermal shutdown (outputs off) at $T_J = 155^\circ\text{C}$
- Overcurrent protection
- No shoot-through currents
- 15-lead TO-220 molded power package

Typical Application

[See Datasheet for Application Information](#)

Parametric Table [expand](#)

Channels	2 Channels
----------	------------

Typical Performance

[*click for larger image](#)

Also Recommended

- [LMD18200](#) LMD18245 Without Digital Control Circuitry
- [LM5102](#) Dual 100V High Side / Low Side Mosfet Driver
- [LM5104](#) Dual 100V High Side / Low Side Mosfet Driver W/ Single Pwm Input
- [LM4652](#) Quad 8A 55V Mosfet In TO220 (Also Used For Class D Audio)

Additional Resources

[Application Notes \(see below\)](#)

Block Diagram

[*click for larger image](#)

General Description

LMD18245T	TO-220	15	STD	1	NA	RoHS	Full production		N/A	Samples	250+	\$9.61	rail of 20	NSUZXYTT LMD18245T P+
			NOPB	1	NA		6 weeks	1250						
LMD18245 MDC	Unpackaged Die						Full production		N/A				tray of N/A	-
							N/A	5000						

Obsolete Parts

Obsolete Part	Alternate Part or Supplier	Source	Last Time Buy Date
LMD18245J-QML	LMD18200-2D/883	NATIONAL SEMICONDUCTOR	03/08/2000

General Description

The LMD18245 full-bridge power amplifier incorporates all the circuit blocks required to drive and control current in a brushed type DC motor or one phase of a bipolar stepper motor. The multi-technology process used to build the device combines bipolar and CMOS control and protection circuitry with DMOS power switches on the same monolithic structure. The LMD18245 controls the motor current via a fixed off-time chopper technique.

An all DMOS H-bridge power stage delivers continuous output currents up to 3A (6A peak) at supply voltages up to 55V. The DMOS power switches feature low $R_{DS(ON)}$ for high efficiency, and a diode intrinsic to the DMOS body structure eliminates the discrete diodes typically required to clamp bipolar power stages.

An innovative current sensing method eliminates the power loss associated with a sense resistor in series with the motor. A four-bit digital-to-analog converter (DAC) provides a digital path for controlling the motor current, and, by extension, simplifies implementation of full, half and microstep stepper motor drives. For higher resolution applications, an external DAC can be used.

Reliability Metrics

Part Number	Process	Early Failure Rate - Rejects	Sample Size (EFR)	PPM *	Rel. Rejects	Device Hours	Long Term Failure Rates (FITS)	MTTF
LMD18245 MDC	LMDMOS	0	9745	0	0	715000	5	202883167
LMD18245T	LMDMOS	0	9745	0	0	715000	5	202883167

For more information on Reliability Metrics, please click [here](#).

Application Notes

Title	Size in Kbytes	Date	 View Online	 Download	 Receive via Email
AN-558: Introduction to Power MOSFETs and Their Applications	258 Kbytes	10-Jun-96	View Online	Download	Receive via Email
Introduction to Power MOSFETs and Their Applications (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	258 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる
AN-694: Application Note 694 A DMOS 3A, 55V, H-Bridge: The LMD18200	408 Kbytes	26-Sep-02	View Online	Download	Receive via Email
Application Note 694 A DMOS 3A, 55V, H-Bridge: The LMD18200 (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	400 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる
AN-828: Application Note 828 Increasing the High Speed Torque of Bipolar Stepper Motors	808 Kbytes	1-Oct-02	View Online	Download	Receive via Email
Application Note 828 Increasing the High Speed Torque of Bipolar Stepper Motors (JAPANESE) 本サイトの日本語版データシートは最新版ではない場合があります。ご検討およびご採用にあたっては、最新の英語版データシートを必ずご確認下さい。	362 Kbytes		オンラインで見る	ダウンロード	Eメールで受けとる

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[Information as of 27-Sep-2006]

UDx2916

Dual Full-Bridge PWM Motor Driver

[Data Sheet](#) (PDF)

Selection Guides:

[Automotive ICs,](#)

[Bridge and Half-Bridge Motor Drivers,](#)

[Power Motor Drivers,](#)

[Power Motor Drivers - Full-Bridge,](#)

[Printer Applications](#)

Check Distributor Stock

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this product is updated**

Technical Publications:

[UDN2916 Current Ratio / Timing Diagrams](#) (PDF)

Features

- 750 mA Continuous Output Current
- 45 V Output Sustaining Voltage
- Internal Clamp Diodes
- Internal PWM Current Control
- Low Output Saturation Voltage
- Internal Thermal Shutdown Circuitry
- Similar to Dual PBL3717, UC3770

Description

The UDN2916B, UDN2916EB, and UDN2916LB motor drivers are designed to drive both windings of a bipolar stepper motor or bidirectionally control two dc motors. Both bridges are capable of sustaining 45 V and include internal pulse-width modulation (PWM) control of the output current to 750 mA. The outputs have been optimized for a low output saturation voltage drop (less than 1.8 V total source plus sink at 500 mA).

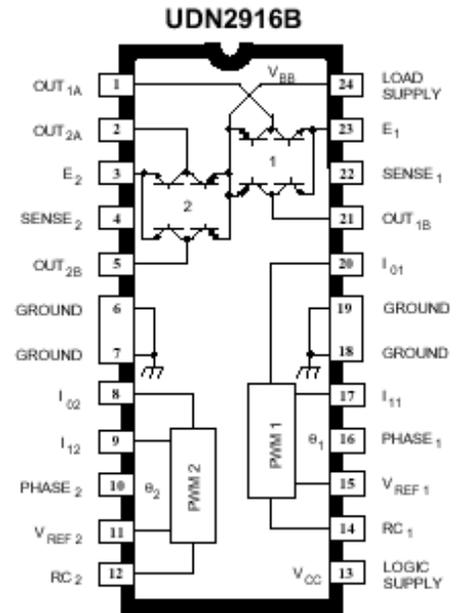
For PWM current control, the maximum output current is determined by the user's selection of a reference voltage and sensing resistor. Two logic-level inputs select output current limits of 0, 33, 67, or 100% of the maximum level. A PHASE input to each bridge determines load current direction.

The bridges include both ground clamp and flyback diodes for protection against inductive transients. Internally generated delays prevent cross-over currents when switching current direction. Special power-up sequencing is not required. Thermal protection circuitry disables the outputs if the chip temperature exceeds safe operating limits.

The UDN2916B is supplied in a 24-pin dual in-line plastic batwing package with a copper lead-frame and heat sinkable tabs for improved power dissipation capabilities. The UDN2916EB is supplied in a 44-lead power PLCC for surface mount applications. The UDN2916LB is supplied in a 24-lead surface-mountable SOIC. Their batwing construction provides for maximum package power dissipation in the smallest possible construction. The UDN2916B, UDN2916EB, and UDN2916LB are available for operation from -20°C to 85°C . The UDQ2916B and UDQ2916LB are available for operation from -40°C to 105°C . All packages are lead (Pb) free, with 100% matte tin leadframe.

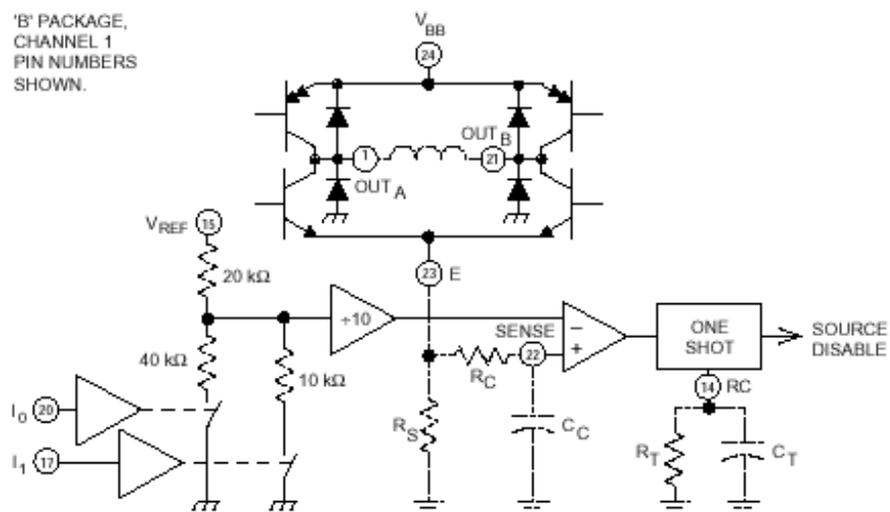


PWM Current Control Circuitry



Dwg. PP-005

'B' PACKAGE,
CHANNEL 1
PIN NUMBERS
SHOWN.



Dwg. EP-007B

Complete Part Numbers

Part Number	Package Type	RoHS Compliant	Part Composition/ RoHS Data	Temperature	Comments	Samples
UDN2916B	24-pin DIP	No	view data	-20°C to 85°C	<i>not for new design</i>	samples not available
UDN2916B-T	24-pin DIP	Yes	view data	-20°C to 85°C	<i>new</i>	
UDN2916EB	44-lead PLCC	No	view data	-20°C to 85°C	<i>not for new design</i>	samples not available
UDN2916EB-T	44-lead PLCC	Yes	view data	-20°C to 85°C	<i>new</i>	
UDN2916LB	24-lead SOIC	No	view data	-20°C to 85°C	<i>not for new design</i>	samples not available
UDN2916LB-T	24-lead SOIC	Yes	view data	-20°C to 85°C	<i>new</i>	
UDQ2916B	24-pin DIP	No	view data	-40°C to 105°C	<i>not for new design</i>	samples not available
UDQ2916B-T	24-pin DIP	--	--	-40°C to 105°C	<i>new</i>	Contact Marketing Representative
UDQ2916EB	44-lead PLCC	No	view data	-40°C to 105°C	<i>not for new design</i>	samples not available
UDQ2916LB	24-lead SOIC	No	view data	-40°C to 105°C	<i>not for new design</i>	samples not available
UDQ2916LB-T	24-lead SOIC	Yes	view data	-40°C to 105°C	<i>new</i>	

Related Applications

[Automotive](#) > [Body Systems](#) > [Electric Mirrors](#)

[Automotive](#) > [Body Systems](#) > [HVAC](#)

[Automotive](#) > [Powertrain Systems](#) > [Cruise Control](#)

[Automotive](#) > [Powertrain Systems](#) > [Throttle Control](#)

[Automotive](#) > [Powertrain Systems](#) > [Throttle Control](#) (stepper motor drive)

[Automotive](#) > [Safety & Convenience Systems](#) > [Pedal Position](#)

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A3955

Full-Bridge PWM Microstepping Motor Driver

[Data Sheet](#) (PDF)

Selection Guides:

[Bridge and Half-Bridge Motor Drivers](#),

[Power Motor Drivers](#),

[Power Motor Drivers - Full-Bridge](#),

[Printer Applications](#)

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this product is updated

Technical Publications:

[Monolithic, Programmable, Full-Bridge Motor Driver Integrates PWM Current Control and Mixed-Mode Microstepping](#) (PDF),

[Smart, Constant-Torque, Microstepping Drive Circuitry for Under \\$10](#) (PDF)

FAQs:

[3955: Full-Bridge PWM Microstepping Motor Driver FAQ](#)

Press Releases:

[Full-Bridge PWM Microstepper for Digital Control of Bipolar Stepper Motors](#)

Product Ads:

[Experienced Drivers Only](#)

Product Evaluation Kits:

[A3955SB Product Evaluation Kit](#)

Features

- 1.5 A Continuous Output Current
- 50 V Output Sustaining Voltage
- Internal PWM Motor Control
- 3-Bit Non-Linear DAC
- Fast, Mixed Fast/Slow, and Slow Current-Decay Modes
- Internal Transient-Suppression Diodes
- Internal Thermal Shutdown Circuitry
- Crossover-Current and UVLO Protection



Description

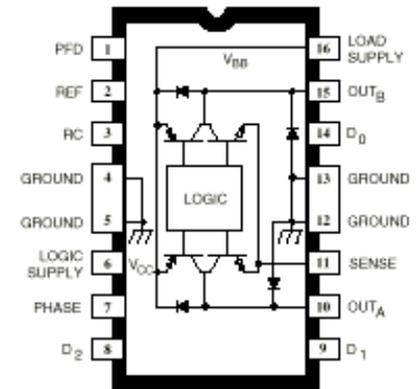
The 3955 is designed for driving one winding of a bipolar stepper motor in a microstepping mode. The outputs are rated for continuous output currents to ± 1.5 A and operating voltages to 50 V. Internal pulse-width modulated (PWM) current control combined with an internal three-bit non-linear digital-to-analog converter allows the motor current to be controlled in full-, half-, quarter-, or eighth-step (microstepping) modes. Nonlinear increments minimize the number of control lines necessary for microstepping. Microstepping provides for increased step resolution, and reduces torque variations and resonance problems at low speed.

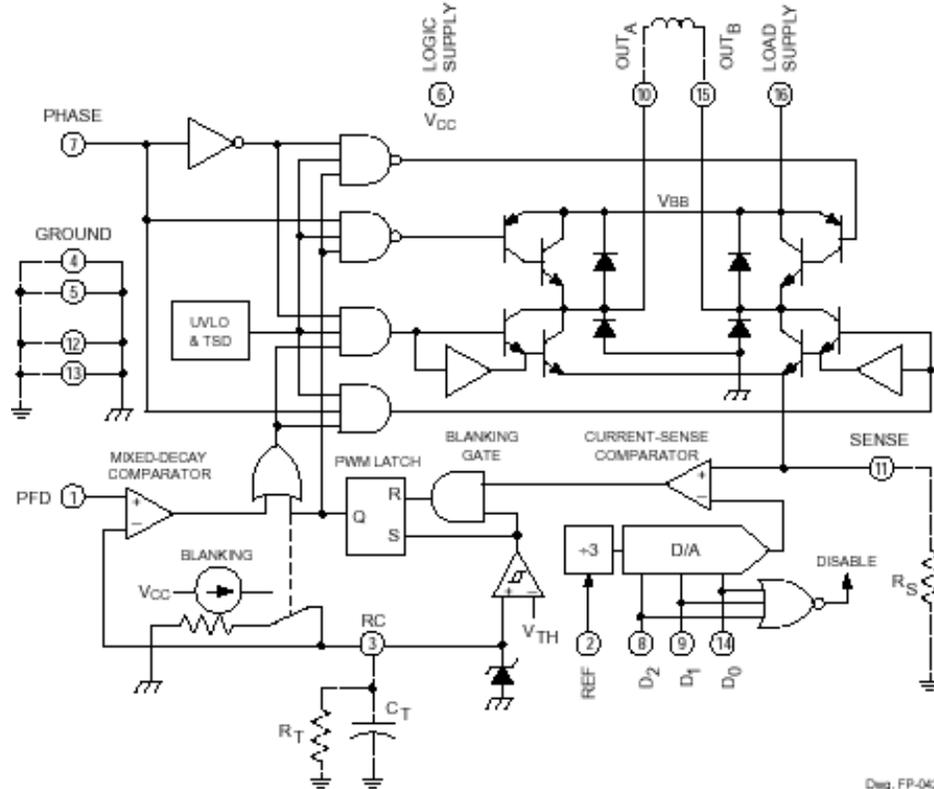
Internal circuitry determines whether the PWM current-control circuitry operates in a slow (recirculating) current-decay mode, fast (regenerative) current-decay mode, or in a mixed current decay with the remainder of the fixed off time spent in slow current decay. The combination of user-selectable current-sensing resistor and reference voltage, digitally selected output current ratio; and slow, fast, or mixed current-delay modes provides users with a broad, variable range of motor control.

Internal circuit protection includes thermal shutdown with hysteresis, transient-suppression diodes, and crossover current protection. Special power-up sequencing is not required.

The A3955S— is supplied in a choice of two power packages; a 16-pin dual-in-line plastic package with copper heat-sink tabs (suffix 'B'), and a 16-lead plastic SOIC with copper heat-sink tabs (suffix 'LB'). For both package styles, the power tab is at ground potential and needs no electrical isolation. Both packages are available in a lead-free version (leadframe plating 100% matte tin), indicated by suffix -T.

Functional Block Diagram



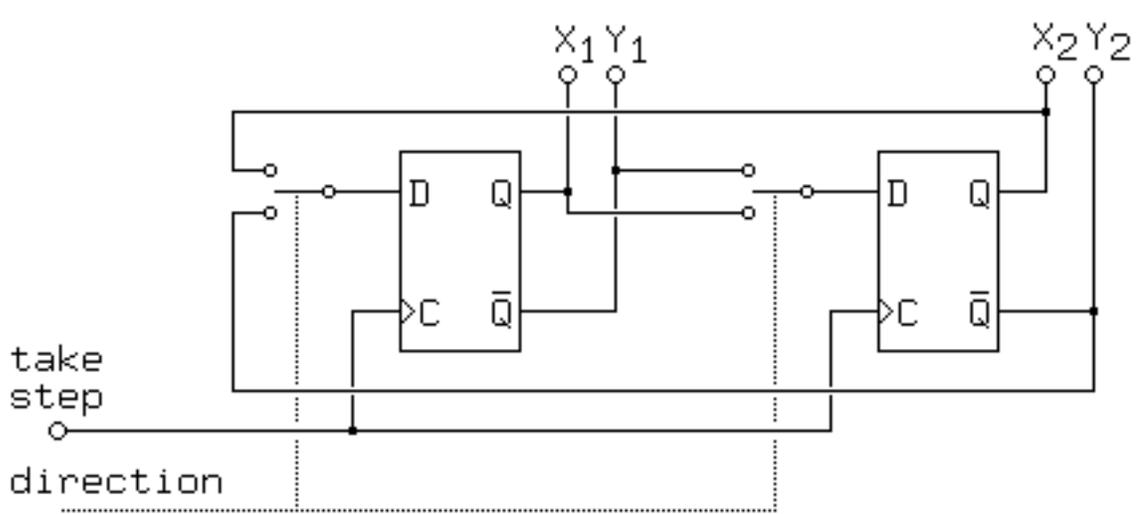


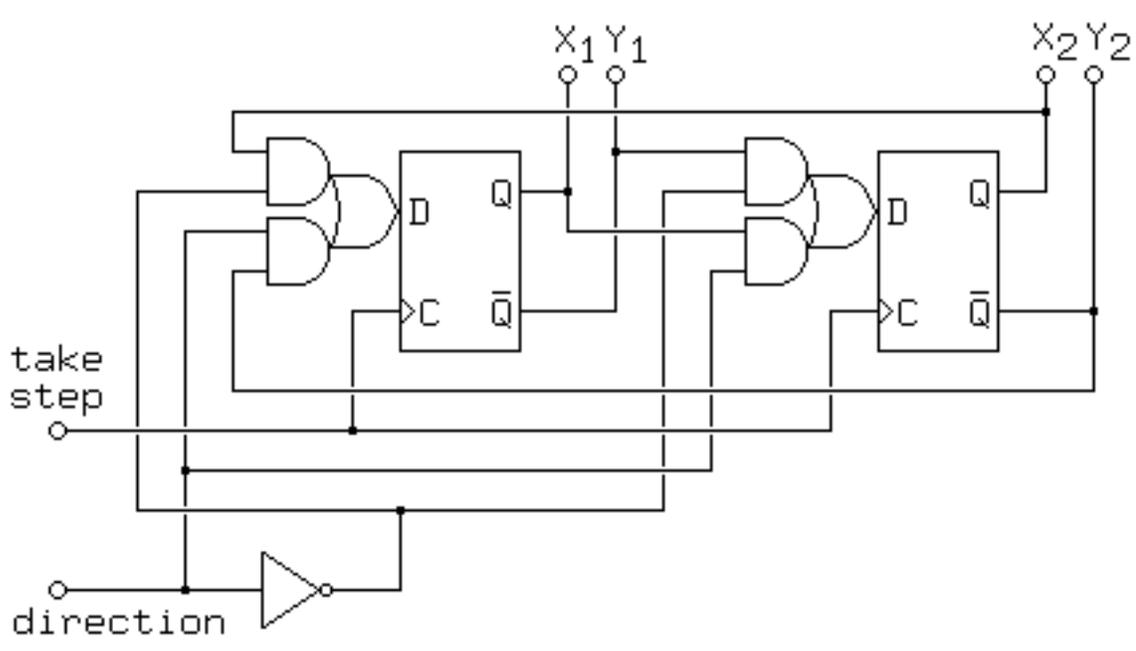
Complete Part Numbers

Part Number	Package Type	RoHS Compliant	Part Composition/ RoHS Data	Temperature	Comments	Samples
A3955SB	16-pin DIP	No	view data	-20°C to 85° C	<i>not for new design</i>	samples not available
A3955SB-T	16-pin DIP	Yes	view data	-20°C to 85° C		
A3955SLB	16-lead SOIC	No	view data	-20°C to 85° C	<i>not for new design</i>	samples not available
A3955SLB-T	16-lead SOIC	Yes	view data	-20°C to 85° C		

Related Applications

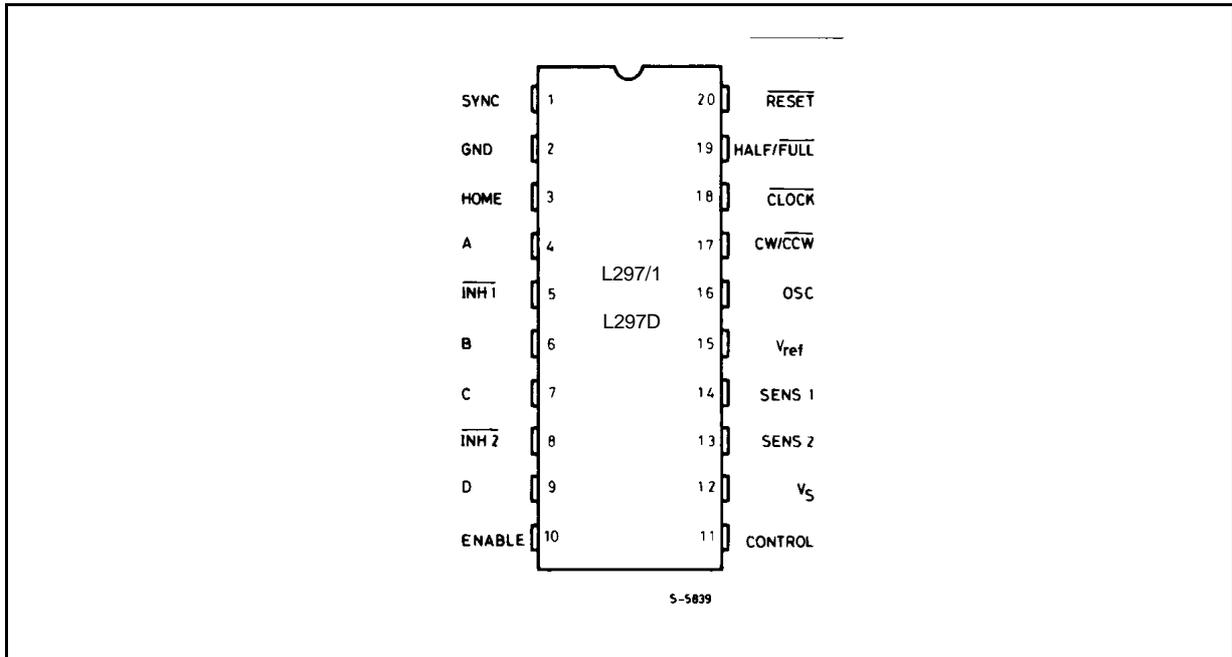
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[Computer & Office Peripherals](#) > [Data Storage](#) > [Tape Load/Unload Motor](#)
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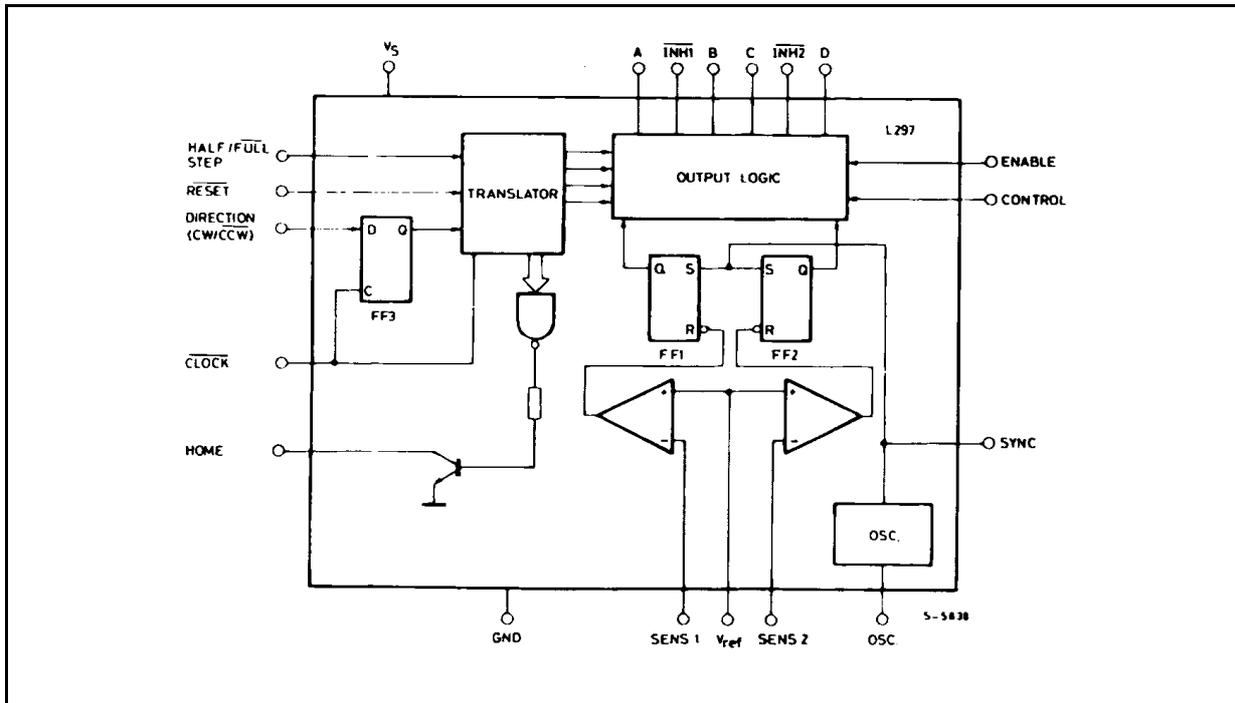


L297

PIN CONNECTION (Top view)



BLOCK DIAGRAM (L297/1 - L297D)



PIN FUNCTIONS - L297/1 - L297D

N°	NAME	FUNCTION
1	SYNC	Output of the on-chip chopper oscillator. The SYNC connections of all L297s to be synchronized are connected together and the oscillator components are omitted on all but one. If an external clock source is used it is injected at this terminal.
2	GND	Ground connection.
3	HOME	Open collector output that indicates when the L297 is in its initial state (ABCD = 0101). The transistor is open when this signal is active.
4	A	Motor phase A drive signal for power stage.
5	$\overline{\text{INH1}}$	Active low inhibit control for driver stage of A and B phases. When a bipolar bridge is used this signal can be used to ensure fast decay of load current when a winding is de-energized. Also used by chopper to regulate load current if CONTROL input is low.
6	B	Motor phase B drive signal for power stage.
7	C	Motor phase C drive signal for power stage.
8	$\overline{\text{INH2}}$	Active low inhibit control for drive stages of C and D phases. Same functions as INH1.
9	D	Motor phase D drive signal for power stage.
10	ENABLE	Chip enable input. When low (inactive) INH1, INH2, A, B, C and D are brought low.
11	CONTROL	Control input that defines action of chopper. When low chopper acts on INH1 and INH2; when high chopper acts on phase lines ABCD.
12	V_s	5V supply input.
13	SENS ₂	Input for load current sense voltage from power stages of phases C and D.
14	SENS ₁	Input for load current sense voltage from power stages of phases A and B.
15	V_{ref}	Reference voltage for chopper circuit. A voltage applied to this pin determines the peak load current.
16	OSC	An RC network (R to V_{CC} , C to ground) connected to this terminal determines the chopper rate. This terminal is connected to ground on all but one device in synchronized multi - L297 configurations. $f \cong 1/0.69 RC$
17	$\text{CW}/\overline{\text{CCW}}$	Clockwise/counterclockwise direction control input. Physical direction of motor rotation also depends on connection of windings. Synchronized internally therefore direction can be changed at any time.
18	$\overline{\text{CLOCK}}$	Step clock. An active low pulse on this input advances the motor one increment. The step occurs on the rising edge of this signal.

PIN FUNCTIONS - L297/1 - L297D (continued)

N°	NAME	FUNCTION
19	HALF/FULL	Half/full step select input. When high selects half step operation, when low selects full step operation. One-phase-on full step mode is obtained by selecting FULL when the L297's translator is at an even-numbered state. Two-phase-on full step mode is set by selecting FULL when the translator is at an odd numbered position. (The home position is designate state 1).
20	RESET	Reset input. An active low pulse on this input restores the translator to the home position (state 1, ABCD = 0101).

THERMAL DATA

Symbol	Parameter	DIP20	SO20	Unit	
R _{th-j-amb}	Thermal resistance junction-ambient	max	80	100	°C/W

CIRCUIT OPERATION

The L297 is intended for use with a dual bridge driver, quad darlington array or discrete power devices in step motor driving applications. It receives step clock, direction and mode signals from the systems controller (usually a microcomputer chip) and generates control signals for the power stage.

The principal functions are a translator, which generates the motor phase sequences, and a dual PWM chopper circuit which regulates the current in the motor windings. The translator generates three different sequences, selected by the HALF/FULL input. These are normal (two phases energised), wave drive (one phase energised) and half-step (alternately one phase energised/two phases energised). Two inhibit signals are also generated by the L297 in half step and wave drive modes. These signals, which connect directly to the L298's enable inputs, are intended to speed current decay when a winding is de-energised. When the L297 is used to drive a unipolar motor the chopper acts on these lines.

An input called CONTROL determines whether the chopper will act on the phase lines ABCD or the inhibit lines INH1 and INH2. When the phase lines

are chopped the non-active phase line of each pair (AB or CD) is activated (rather than interrupting the line then active). In L297 + L298 configurations this technique reduces dissipation in the load current sense resistors.

A common on-chip oscillator drives the dual chopper. It supplies pulses at the chopper rate which set the two flip-flops FF1 and FF2. When the current in a winding reaches the programmed peak value the voltage across the sense resistor (connected to one of the sense inputs SENS₁ or SENS₂) equals V_{ref} and the corresponding comparator resets its flip flop, interrupting the drive current until the next oscillator pulse arrives. The peak current for both windings is programmed by a voltage divider on the V_{ref} input.

Ground noise problems in multiple configurations can be avoided by synchronising the chopper oscillators. This is done by connecting all the SYNC pins together, mounting the oscillator RC network on one device only and grounding the OSC pin on all other devices.

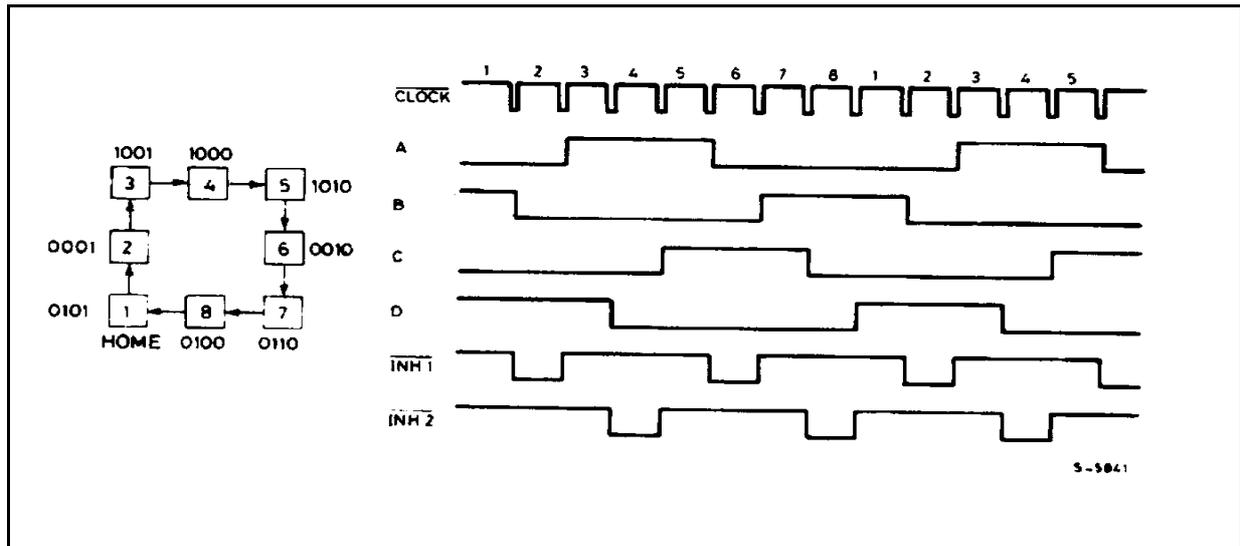
MOTOR DRIVING PHASE SEQUENCES

The L297's translator generates phase sequences for normal drive, wave drive and half step modes. The state sequences and output waveforms for these three modes are shown below. In all cases the translator advances on the low to high transition of CLOCK.

Clockwise rotation is indicated; for anticlockwise rotation the sequences are simply reversed. RESET restores the translator to state 1, where ABCD = 0101.

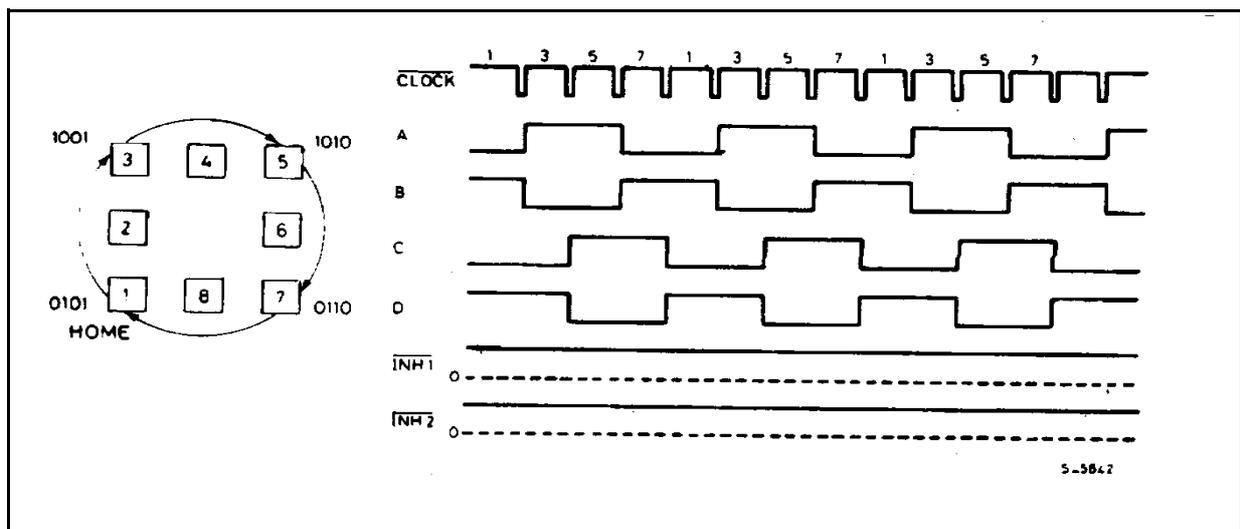
HALF STEP MODE

Half step mode is selected by a high level on the HALF/FULL input.



NORMAL DRIVE MODE

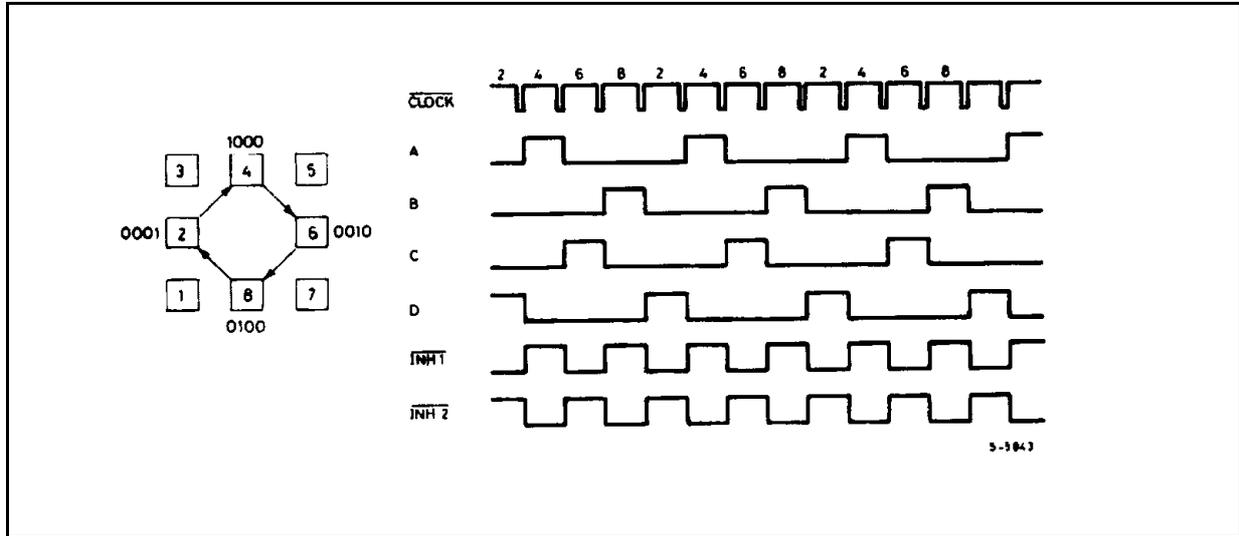
Normal drive mode (also called "two-phase-on" drive) is selected by a low level on the HALF/FULL input when the translator is at an odd numbered state (1, 3, 5 or 7). In this mode the $\overline{INH1}$ and $\overline{INH2}$ outputs remain high throughout.



MOTOR DRIVING PHASE SEQUENCES (continued)

WAVE DRIVE MODE

Wave drive mode (also called "one-phase-on" drive) is selected by a low level on the HALF/FULL input when the translator is at an even numbered state (2, 4, 6 or 8).



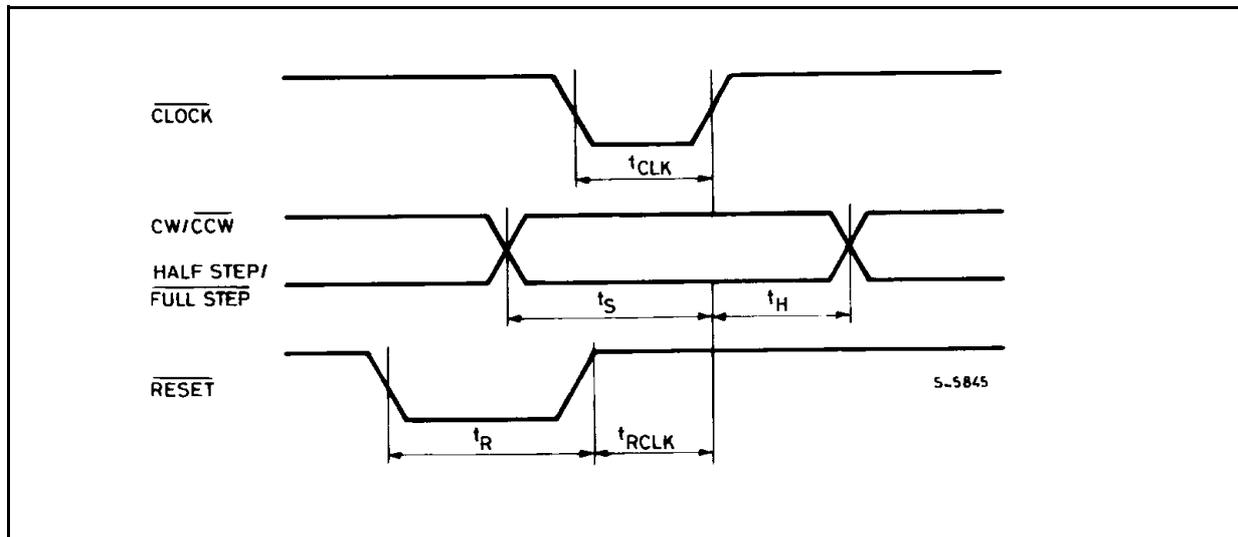
ELECTRICAL CHARACTERISTICS (Refer to the block diagram $T_{amb} = 25^{\circ}\text{C}$, $V_s = 5\text{V}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ	Max.	Unit
V_s	Supply voltage (pin 12)		4.75		7	V
I_s	Quiescent supply current (pin 12)	Outputs floating		50	80	mA
V_i	Input voltage (pin 11, 17, 18, 19, 20)	Low			0.6	V
		High	2		V_s	V
I_i	Input current (pin 11, 17, 18, 19, 20)	$V_i = L$		100		μA
		$V_i = H$			10	μA
V_{en}	Enable input voltage (pin 10)	Low			1.3	V
		High	2		V_s	V
I_{en}	Enable input current (pin 10)	$V_{en} = L$			100	μA
		$V_{en} = H$			10	μA
V_o	Phase output voltage (pins 4, 6, 7, 9)	$I_o = 10\text{mA}$ V_{OL}			0.4	V
		$I_o = 5\text{mA}$ V_{OH}	3.9			V
V_{inh}	Inhibit output voltage (pins 5, 8)	$I_o = 10\text{mA}$ $V_{inh L}$			0.4	V
		$I_o = 5\text{mA}$ $V_{inh H}$	3.9			V
V_{SYNC}	Sync Output Voltage	$I_o = 5\text{mA}$ $V_{SYNC H}$	3.3			V
		$I_o = 5\text{mA}$ $V_{SYNC V}$			0.8	

ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Test conditions	Min.	Typ	Max.	Unit
I_{leak}	Leakage current (pin 3)	$V_{CE} = 7\text{ V}$			1	μA
V_{sat}	Saturation voltage (pin 3)	$I = 5\text{ mA}$			0.4	V
V_{off}	Comparators offset voltage (pins 13, 14, 15)	$V_{ref} = 1\text{ V}$			5	mV
I_o	Comparator bias current (pins 13, 14, 15)		-100		10	μA
V_{ref}	Input reference voltage (pin 15)		0		3	V
t_{CLK}	Clock time		0.5			μs
t_s	Set up time		1			μs
t_H	Hold time		4			μs
t_R	Reset time		1			μs
t_{RCLK}	Reset to clock delay		1			μs

Figure 1.



APPLICATION INFORMATION

TWO PHASE BIPOLAR STEPPER MOTOR CONTROL CIRCUIT

This circuit drives bipolar stepper motors with winding currents up to 2A. The diodes are fast 2A types.

Figure 2.

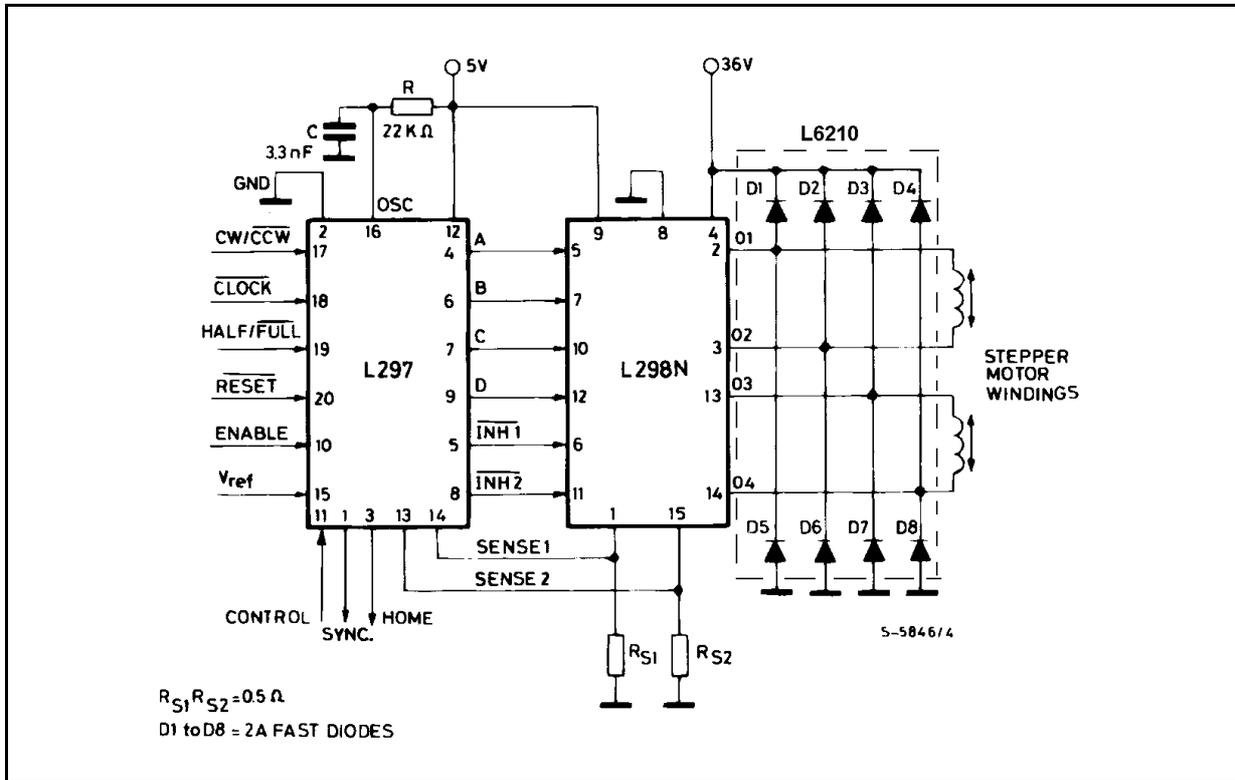
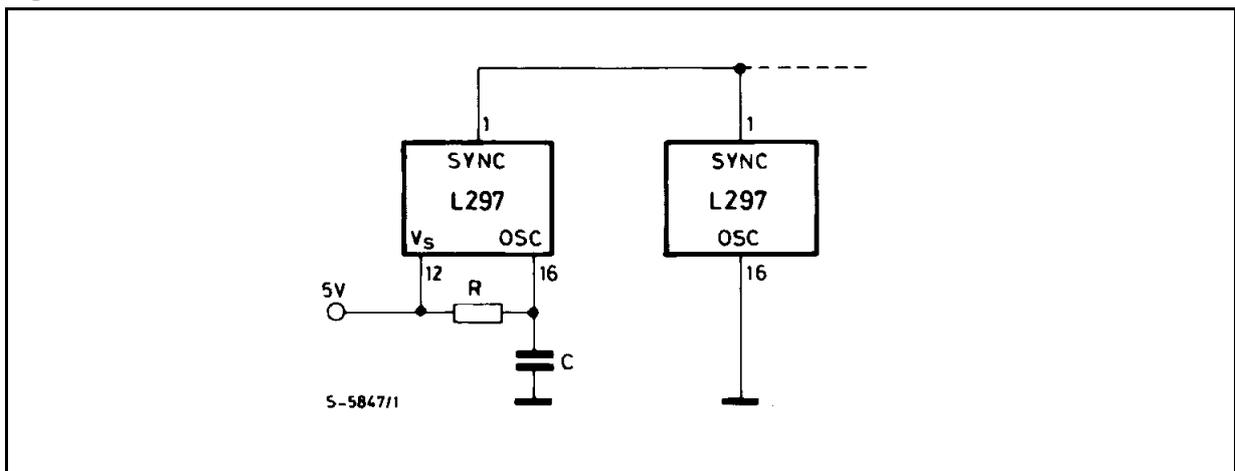
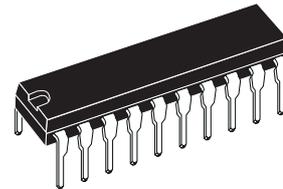


Figure 3 : Synchronising L297s

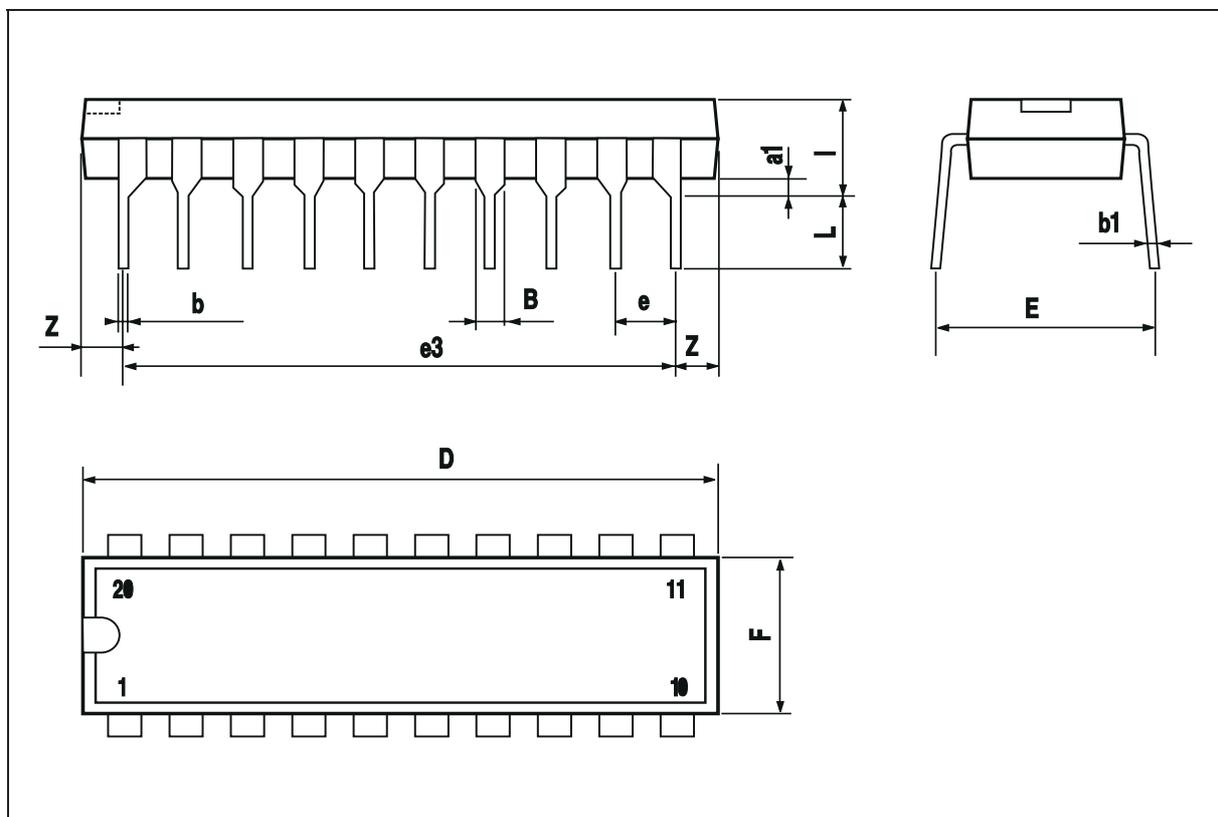


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.254			0.010		
B	1.39		1.65	0.055		0.065
b		0.45			0.018	
b1		0.25			0.010	
D			25.4			1.000
E		8.5			0.335	
e		2.54			0.100	
e3		22.86			0.900	
F			7.1			0.280
I			3.93			0.155
L		3.3			0.130	
Z			1.34			0.053

OUTLINE AND MECHANICAL DATA

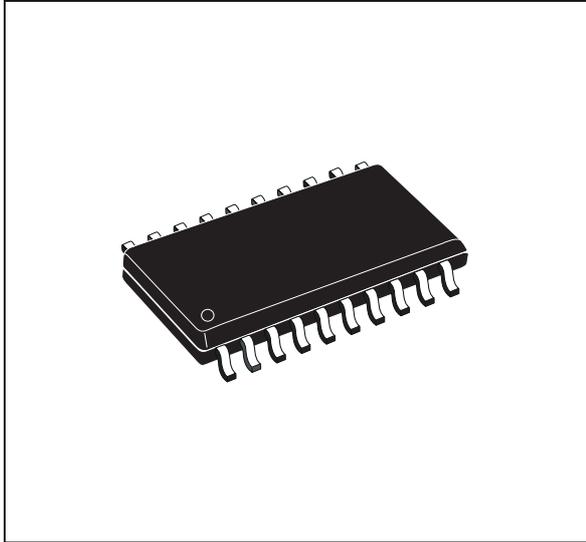


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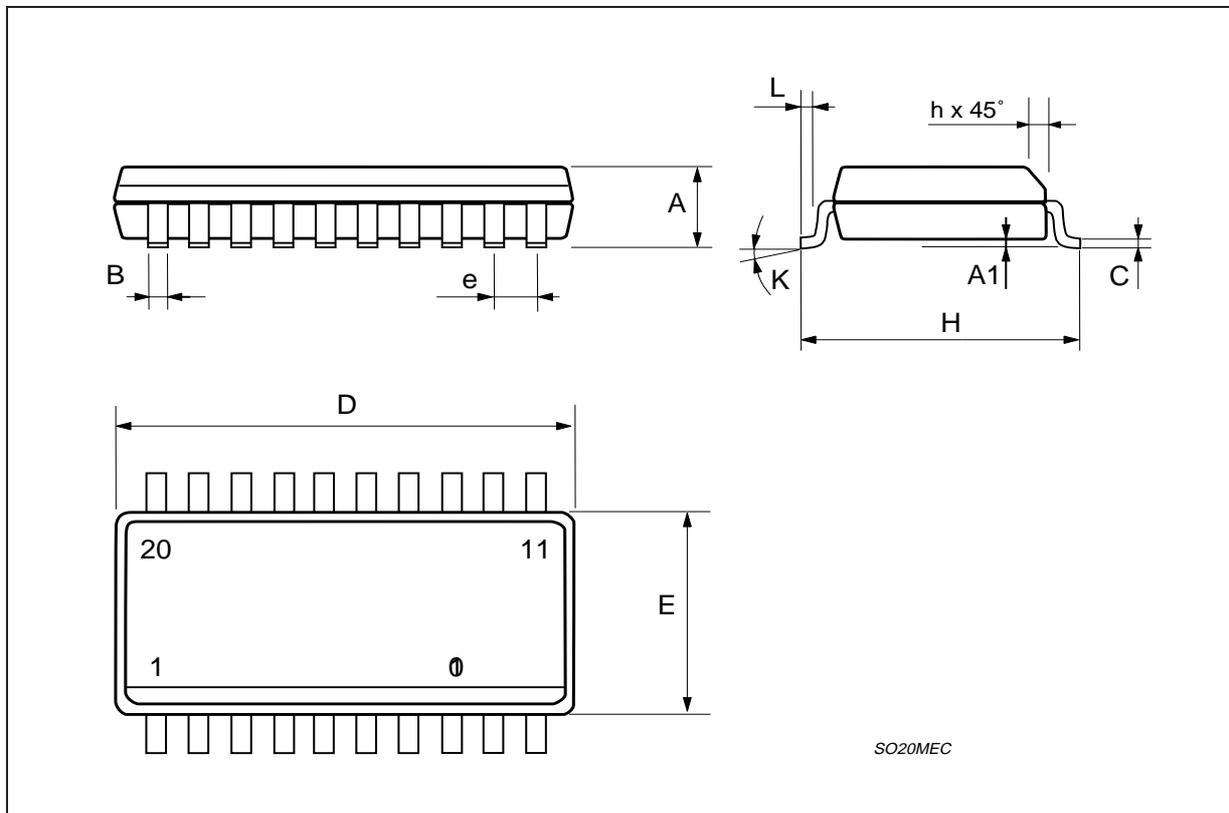


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	2.35		2.65	0.093		0.104
A1	0.1		0.3	0.004		0.012
B	0.33		0.51	0.013		0.020
C	0.23		0.32	0.009		0.013
D	12.6		13	0.496		0.512
E	7.4		7.6	0.291		0.299
e		1.27			0.050	
H	10		10.65	0.394		0.419
h	0.25		0.75	0.010		0.030
L	0.4		1.27	0.016		0.050
K	0° (min.)8° (max.)					

OUTLINE AND MECHANICAL DATA



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